

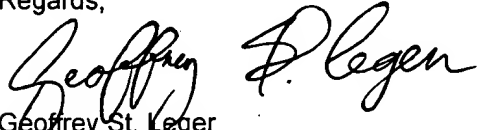
December 12, 2002

Dear Ms. Liang,

Attached please find the results of your search request for application #09/626,965. I searched Dialog's foreign patent files, technical databases, product announcement files and general files; along with the Internet, ACM and IEEE.

Please let me know if you have any questions.

Regards,

A handwritten signature in cursive script, appearing to read "Geoffrey St. Leger".

Geoffrey St. Leger  
4B30/308-7800

Access DB# 9177

## SEARCH REQUEST FORM

Scientific and Technical Information Center

Requester's Full Name: Cover Liang Examiner #: 91180 Date: 12/11/2  
Art Unit: 9172 Phone Number 305-9985 Serial Number: 09/006965  
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If more than one search is submitted, please prioritize searches in order of need.

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Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc. if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: Component Management System

Inventors (please provide full names): \_\_\_\_\_

Earliest Priority Filing Date: 9/27/99

\*For Sequence Searches Only\* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.

New Focus: Any art on a  
Firmware Database

12/11/2

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**Results 1 - 4 of 4** short listing

**1** MDS: An improved total system for firmware development 100%

Kazutoshi Takahashi , Etsuo Takahashi , Tatsushige Bito , Toshinori Aoyama , Akihiko Yamada

**Proceedings of the fifteenth annual workshop on microprogramming on Microprogramming** October 1982

A general purpose, total system MDS1,2 (Microprogramming Design-support System) has been developed to hasten the introduction of various kinds of firmware over the widest possible range of computer from the largest to the smallest (microcomputer). Not only many types of assembly language but also machine dependent high-level languages can be used and physical address assignment can be performed automatically with MDS. This paper describes an overvi ...

**2** Trends in non-software support for input-output functions 98%

Ken J. McDonell

**Proceedings of the 3rd workshop on Computer architecture : Non-numeric processing** January 1977

Input-output subsystem architectures have evolved over the past 20-odd years to the point where two divergent approaches have found acceptance in current computer systems; the 'IBM channel' is the archetype of the lower level alternative, while the functionally more complex techniques involve a wide spectrum of distributed processor architectures supporting database and/or storage management functions independently with respect to the central processor. The paper traces the historical devel ...

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N. N. Oliver , John D. Joyce

**Proceedings of the annual conference** October 1976

Although some relational information systems have recently become available for production use, very few, if any of them, contain facilities to collect performance data. This paper describes a method for implementing a performance monitor and

some of the data collected by this performance monitor which was recently installed in the REGIS (RElational General Information System). REGIS is currently being used within General Motors. The performance monitor is used to collect data about the use ...

#### 4 A mapping algorithm for computer-assisted exploration in the design of 97% embedded systems



**ACM Transactions on Design Automation of Electronic Systems (TODAES)** January 2001

Volume 6 Issue 1

We present a technique for automatic exploration of architectural alternatives in the design of complex electronic embedded systems and systems-on-a-chip. The technique transforms the problem into a set of simple model-to-model operations and a mapping algorithm that becomes the core of the entire design process. The mapping algorithm is formulated as an assignment-type problem (ATP), which is, in turn, solved by a straightforward optimization method. The result is a design assistance tool, ...

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#### Results 1 - 4 of 4      short listing

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MDS: An Improved Total System  
for Firmware Development

Kazutoshi Takahashi, Etsuo Takahashi, Tatsushige Bito,  
Toshinori Aoyama, and Akihiko Yamada

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Nippon Electric Co., Ltd.  
Fuchu City, Tokyo 183  
Japan

Abstract:

A general purpose, total system MDS<sup>1,2</sup> (Microprogramming Design-support System) has been developed to hasten the introduction of various kinds of firmware over the widest possible range of computer from the largest to the smallest (microcomputer).

Not only many types of assembly language but also machine dependent high-level languages can be used and physical address assignment can be performed automatically with MDS.

This paper describes an overview of MDS, the design approach to the high-level languages, and three examples which have been developed.

1. Introduction.

Recently, due to the development of LSI technology, a variety of microcomputers have been introduced into the field, and many types of peripheral controllers depend heavily upon such microcomputers in their central control element. Such peripheral controllers require a large amount of firmware development.

Additionally every user finds it necessary to speed up operating system functions and so the use of microprogramming has increased dramatically.

Consequently, the objectives for firmware development systems are the following.

i) Adaptability:

A wide range of firmware, ranging from central processors of large scale computers to microcomputers, must be produced.

ii) Readability/Writability:

As a result of the increasing requirements for firmware, the increasing number of design personnel necessitates easily acceptable read/write tools to deal with the greatly increased development demands.

iii) Flexibility:

Firmware development tools should be sufficiently flexible to cope with a large number of variations in microarchitectures and should be able to handle successive LSI chip series.

iv) Productivity/Reliability/Maintainability:

The shortening of the development period has become imperative. The ability to make error-free firmware is a vital step in achieving this aim.

Many approaches have been taken toward these recognized needs. For example, the implementation of a general purpose compiler<sup>4</sup>, a hierarchical support system<sup>5</sup>, interactive debugging tools, or general purpose simulators. There are, however, many problems in the development of a compiler which can process a machine independent high-level language usable in a wide range of firmware<sup>12</sup>.

Although many machine dependent high-level languages have been announced, all compilers capable of processing such languages have been exclusive<sup>14</sup>.

Microprogramming Design-support System (MDS) has been developed to satisfy those objectives and can support compiling functions for machine dependent high-level languages and automatic address assignment functions.

In section 2, an overview of MDS will be introduced, in section 3, an approach to the high-level language will be described, and in section 4, three examples will be explained.

2. An overview of MDS.

MDS is a general purpose tool for firmware development. In this section the outline of each component included in MDS will be described. Figure 1 shows the system configuration of MDS.

i) Source File Management Program:

This accepts source library descriptions, source firmware descriptions and interpreted data for preprocessor using batch operation or TSS, and manages them in one generation cycle on the firmware data base.

ii) Library Processor:

This inputs the source library descriptions containing the instruction formats, commands, branching methodology, and converts them to table format for efficient assembly utilization.

iii) Preprocessor<sup>7,8</sup>:

This inputs and analyzes various firmware descriptions, and converts this information according to the user's process-oriented description, into a standard format for delivery to general purpose tools. This processor will be described later.

iv) Address Assignment Program<sup>9</sup>:

This inputs the branching information output from preprocessor in the standard format, analyzes the restrictions of each word, and assigns each word to an optimized position on the memory area. This also will be described later.

v) Assembler:

As many general purpose microprogram assemblers have been introduced previously<sup>6</sup>, the special MDS features are described below.

- . general purpose macro-assemblers utilizing the table reference method (not assembler generator method) have been incorporated .

- . modular programming and free format description are available.

- . variable length instruction is available at a maximum length of 216 bits per word.

- . default values may be stored in unused instruction fields and word areas.

- . numerous varieties of useful lists are produced for checking and debugging (entry/exit lists, cross reference lists, address mapping lists and assemble lists with the from- and to-label in each word), with a wide number of available options.

vi) Linker:

For each source module, the assembler outputs a relocatable object module. The linker combines several relocatable object modules and outputs the absolute microprogramming object. Two features of this linker are the global link function and the production of a wealth of list (module linkage lists, cross reference lists between modules, etc.).

vii) Analyzer:

This contains firmware trace functions and symbolic execution functions, and is currently under development. At present, these functions are performed by the preprocessor on each target machine.

viii) Simulator:

MIXS<sup>10</sup> (Mixed Level Simulator) inputs the objective hardware model written in FDL (Functional Description Language) and the firmware object bit pattern, and simulates the hardware/firmware execution. MIXS serves to facilitate the improvement of firmware reliability and a shorter firmware design cycle.

ix) Post Process:

This contains a number of programs enabling the mounting of firmware objects onto material items for subsequent loading into RAM or writing into ROM. These material items could be magnetic tapes, floppy disks or paper tapes.

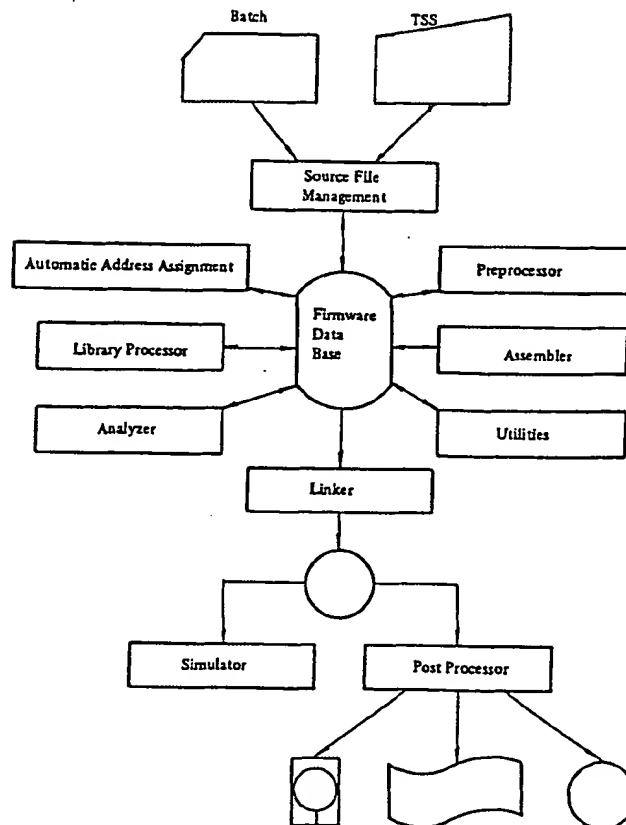


Figure 1. System Configuration

### 3. An approach to the high-level languages.

It is generally recognized that firmware description language must meet three design goals<sup>11</sup>.

- i) The language must facilitate the use of structured programming.
- ii) The language should be readable for maintenance and machine independent for portability.
- iii) It should be able to compile the language into a very efficient object for all firmware.

However, it is also realized that it is rather hard to design machine independent high-level languages and effective compilers<sup>12</sup>. Therefore, "tailored language"<sup>13</sup> which means machine dependent high-level language has been adopted as a firmware description language.

For the following reason, the preprocessor functions have been added prior to the assembling process function.

- i) If the compiler had been generated for each machine dependent language (for example MIL<sup>14</sup>), "shortening of the development period" could not have been accomplished because the time necessary for developing each compiler had to be taken into account.
- ii) The existing MDS assembler is already a very powerful one.
- iii) If there is a problem concerning performance, designers have to be able to use assembler level languages in place of high-level languages.

Particularly in horizontal microprogramming, the following restrictions are placed on the allocation of each word.

- i) restrictions on the word itself.  
ex. entry point at hardware breakpoint.
- ii) restrictions between one word and next word.  
ex. . the branching range (short branch/long branch)  
    . the branching method (single way branch/multiway branch)  
    . Subroutine calls (caller and returned word)

General purpose assembler in the MDS, itself, has several assembly control statements concerning address assignment (ex. #ORG, #MORG, #EQU etc.). However, when the firmware designers use these statements they have to take the restrictions mentioned above, into consideration. This makes it difficult to meet the objectives discussed in section 1.

Thus the preprocessor functions and the automatic address assignment functions are absolutely necessary, if the designer wants to raise the language level.

In the following part of this section, tools to perform these functions are described.

#### 3.1 Preprocessor.

If the preprocessor is designed using ordinary language for each tailored language, a lot of time and manpower is required. Because there are many different functions to be done for each tailored language.

In order to easily implement the general purpose preprocessor, the interpretive method has been introduced. Upon putting the process definition language into the interpreter the processing functions are defined and performed.

Since, generality and extensibility are necessary in process definition language, the bootstrapping method is used. As illustrated in figure 3, in the case of operation as a preprocessor or as a translator, both use the same interpreter.

The following are the features of the process definition language designed for the interpreter.

- i) The language has extensibility because it incorporates the bootstrapping method (The extended statements of DO/CASE/WHILE/ITERATE/LOOP/LEAVE etc. and the indentation function can be used now).
- ii) The language is tightly coupled with the firmware data base, so that the input data and output data can be handled easily.
- iii) Since table handling statements (SETUP/STORE/SEARCH etc.) and character string processing statements (GET/SET/REPLACE/SCAN etc.) are available, the preprocessor is suitable for language processing.
- iv) Cross reference output statements (DEFINE/REFER) and file output statements (WRITE/PRINT/WORKOUT etc.) for the interface with other programs are helpful in defining the preprocess and in the use of the preprocessor.

By developing the general purpose preprocessor in this way, the firmware designer can describe firmware, in the tailored language, which has been decided on solely by the firmware designer himself.

The preprocessor can perform not only the functions of format conversion and compiler, but can also easily accomplish firmware checking. And the firmware designer can use the preprocessor everywhere else, for example, firmware conversion, two level microprogramming support and split microprogramming support etc.

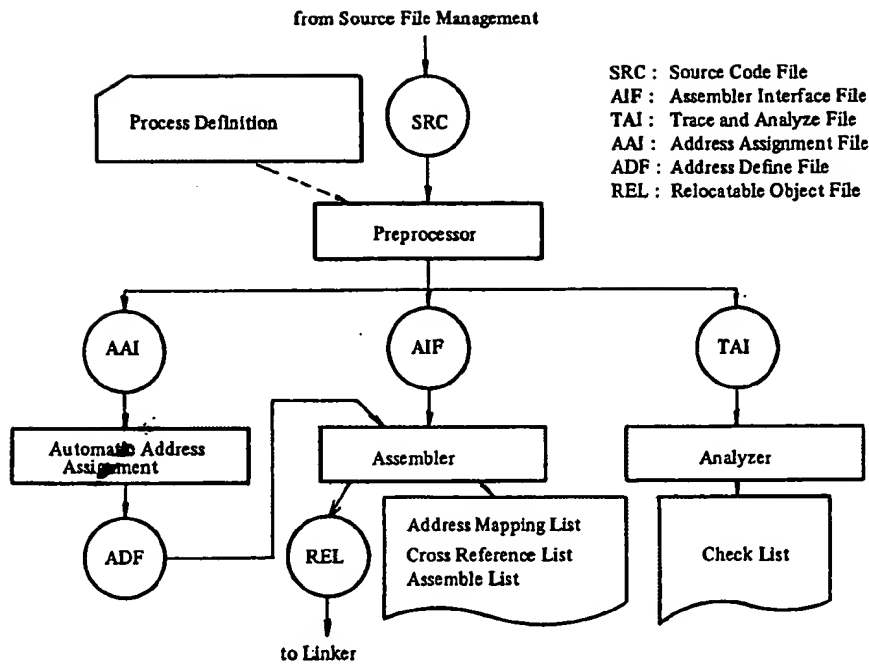


Figure 2. Outline of Preprocessor Interface

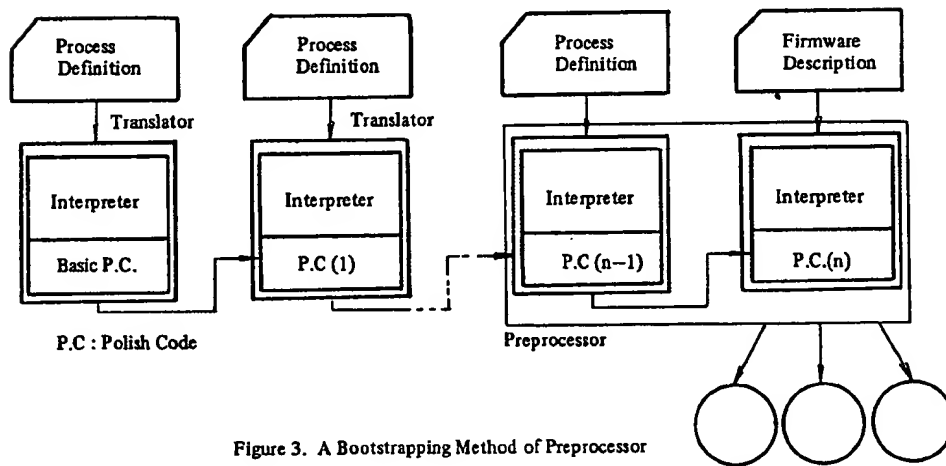


Figure 3. A Bootstrapping Method of Preprocessor

### 3.2 The automatic address assignment program.

The automatic address assignment program accepts information output from the preprocessor in the standard sequence control form.

Initially this program recognizes the network structure of the firmware on the basis

of the address restrictions on each word, then arranges the branches and cuts down the loops, and converts the network structure into several trees. Finally it strictly assigns each word included in each tree in compliance with the restrictions and outputs the corresponding list with the labels and their physical address into the list file and firmware data base.

The following are the characteristics of the automatic address assignment program.

- i) If any firmware made in tailored language is input and performed by the preprocessor, the designer can receive the optimized address assignment map as a result.
- ii) Reserved area options are available when the designer wants to reserve some space for a particular use.
- iii) The packing rate option is helpful for minor changes to firmware.
- iv) If there are any changes in firmware, non-changing words remain at the same address and only changing words are re-assigned to new address.
- v) The address assignment result is output into the firmware data base and the designer is able to handle it with ease.

#### 4. Examples.

##### 4.1 GCP HLL.

GCP (general control processor) is a general purpose control processor possessing vertical sixteen bits microinstructions. Before the development of GCP, the GCP HLL (high-level language) is defined and the preprocessor is prepared.

The advantages of GCP HLL are:

- i) to be able to use structured microprogramming, but still retaining the GO TO statement,
- ii) to be able to use assembly language together with GCP HLL,
- iii) to be able to use all registers including in the GCP hardware because of the machine dependent language or the tailored language.

Figure 4 shows an example of the process definition for the preprocessor for GCP HLL and figure 5 shows a sample description of the microprogram using GCP HLL.

##### 4.2 Split microprogramming support<sup>11</sup>.

ACOS System 1000 has adopted the split microprogramming and its total number of bits/words is about 700, so the preprocessor is ready to support split microprogramming.

- i) The preprocessor is able to split each microoperation described on each line into proper control storage, and to reduce the size of each control storage space by means of analyzing each microoperation.

```

MDS GEN ( G01) //PROG GCPHLLP SRC EXOMLP
.....1.....2.....3.....4.....5.....

* .....NOTE:.....
*
* MAIN CONTROL PROCEDURE
* .....END:.....
*
*PROC GCPHLLP ;
  SETUP 50,16,16,SEQTABNUM
  IDBASE :=12
  CNTCHR:="/", GEN
  IF DIRECTIVEY60-3="RAM"
    I ADPRNC:="4",ADPRNCX:="2",ADPRNCY:="6"
  END
  LOOP
    I READ
    I IF IEHD="Y"
    * + LEAVE+2
    I END
    I ICARDY72-2:=SPACE
    I IF ICARDY0=SPACE
    I I EVP ICARDY1-71
    I ELSE
    I I CASE
    I I OF ICARDY0="/"
    I I I PRINT (ICARDY0-74,126,CSEQ)
    I I I ICARDY0:=SPACE
    I I I WRITE ICARDY0
    I I OTHER
    I I I PRINT (ICARDY0-74,126,CSEQ)
    I I I WRITE ICARDY0
    I I END
  I END
END
POP " ",SEQTABNUM
IF TABEND=ZERO
  I MESS ERRQ5

```

Figure 4. Process Definition

```

MDS GEN ( G01) //PROG GCPTEST SRC GCPHLLPX
.....1.....2.....3.....4.....5.....

* .....NOTE:.....
*
* GCP-HLLP TEST DATA
* .....END:.....
*
  IF GR1L.AND.'00'
    I GR2L=GR1L
  ELSE
    I GR2M='A0'
  END
  CASE
  OF GR1='0'
    I GR2=ACC
  OF GR1='1'
    I GR3=ACC
  OTHER
    I GR2=GR3
  END
  WHILE GR1<'FS'
    I ACC=MM(GRA,+2,2)
    I MM(GRB,+2,2)=ACC
    I GR1=GR1+'1'
  END
  ITERATE
  I ACC=FPS(GRA,+1)
  I FPS(GRB,+1)=ACCL
  I GR1=.SUB.'1'
  UNTIL GR1='0000'
  CALL TAGO
  LOOP I
  I ACC=CS(GRA,+1)
  I CS(GRA)=ACC
  I GR2=GR2.ADD.'1'
  * LEAVE 1 ON GR2>'FF'
  I IF GRAML=ACC
  * + LEAVE 2
  I END
END
RETURN
RETURN WITH POPFW

```

Figure 5. GCP HLL Description

			MDS 80-ASS(020) MODULE:VUCSM 000.00 LIB:UCSLTB 000.01 DATE:91.09.27 PAGE:1										
SEQ	ADR	IMAGE	.....1.....2.....3.....4.....5.....6.....7.....										
11689			*DNR=000*DFX=000*DRG=000*DPA=000*DPB=000*DEB=000.										
11690	04261	564440000000000000004264	VC0648	ALEN	VC0643	00	00	00	00	00	00	<< 11675	A
11691			BSEA(DRG2)										
11692			*DNR=000*DFX=000*DRG=000*DPA=000*DPB=000*DEB=000.										
11693	04262	405224000006600001604263	VC0641	ABLE	VC0642	70	00	03	02	02	02	<< 11687	A
11694			BSEA(DFX1)										
11695			M	RXRV1:1, #FXRDC-BFX(LDX), #FXR1C-BFX-LU(LDX)									
11696			M	RXRV1:0, #FOWIO, #SHROH, #SHR1H, TRWIO, #DERQ, #TSRD									
11697			M	/ SET NEW IC TO RXR									
11698			*DNR=000*DFX=000*DRG=003*DPA=000*DPB=000*DEB=007.										
11699	04263	564420000000000000004265	VC0642	ALEN	VC0650	00	00	00	00	00	00		A
11700			BSEA(DRG1)										
11701			*DNR=000*DFX=000*DRG=000*DPA=000*DPB=000*DEB=000.										
11702	04264	200006240021000000420270	VC0643	ABSE	VC0640	70	00	02	02	02	02	<< 11690	A
11703			EOXC, #SPMA(40)										
11704			M	RXRV1:1, #SPARC-(SD), #SPMXS:SEGDH, #SPMRV1									
11705			M	RVR1:0, #SHROH, #SHR1H, TRWIO, #DERQ, #TSRD, #SET-PFUTDR2									
11706			M	/ SET CLIMB 2ND WORD TO #ARO, #FXR1									
11707			M	/ SET NEW IC TO RXR									
11708			*DNR=000*DFX=001*DRG=004*DPA=000*DPB=000*DEB=002.										
11709	04265	204242241460200000414266	VC0650	ABSE	VC0651	70	01	01	02	02	02	<< 11699	A
11710			BSEA(DFX2), EOXC, #STC(24)										
11711			M	FXRV1:1, #FXR1LC11, #SCRC-SFTC, #RSXC-FXR2(LS)									
11712			M	#FXRDC-RSX(SHT)									
11713			M	#DERQ, #FXRDC, #FXR1UUC:0, #FXM1ULC:0, #RXRV1:0, #WOWIO									
11714			M	#SHROH, #SHR1H, TRWIO, #TSRD									
11715			M	/ SET CLIMB 2ND WORD TO #ARO, #FXR1									
11716	04266	564400000000000000004267	*DNR=000	DFX=063	DRG=001	DPA=000	DPB=000	DEB=002.	00	00	00		A
11717			VC0651	ALEN	VC0652	00	00	00					
11718			BSEA(DRG0)										
11719			*DNR=000*DFX=000*DRG=000*DPA=000*DPB=000*DEB=000.										
11720	04267	204220010120000000574673	VC0652	ABSE	VC0670	70	02	00	02	02	02		A
11721			BSEA(DFX1), #SPMA(371)										
11722			M	#RXRV1:1, #FXRDC-RSX(SHT), #FXRUC, #SPARC-(FW), #SPMW, #DERQ, LOGMD11, #RSXS:LOG									
11723			M	#TSRD, #FXRDC:0, #FXRV1:1, #FOWIO, #SHROH, #SHR1H, TRWIO									
11724			M	/ SET NEW IC TO #ARO									
11725			*DNR=000*DFX=005*DRG=000*DPA=000*DPB=000*DEB=002.										
			<< 11702										

Figure 6. The Microprogram Description for ACOS System 1000

PAGE:0209 11670-11725

Figure 6. The Microprogram Description for ACOS System 1000 PAGE:0209 (11670-11725)

FPA MICRO DIAG			(A05U0)		MDS 80-ASS(020) MODULE:FDIAG		000.00 LIB:M1600LIB 000.01		DATE:91.06.10 PAGE:1		
SEQ	ADR	IMAGE	.....1.....2.....3.....4.....5.....6.....7.....								
353	00052	9E143	R7L=F1A0RL	/ READ ADDR					A		
354	00054	AD0E2	R0N=R7L	/ EDIT ADDR DATA TO R6					A		
355	00056	8D7D3	R0R0<R5	/ CHECK ADDR DATA					A		
356	00058	42053	IF Z=0	60 FDI220	/ ERR ? GO ERR CODE SET					=>	00361
357	0005A	49010	R4=R4<->101	/ TEST COUNT -1					A		
358	0005C	54070	IF LZ=1	60 FDI300	/ THIS TEST END ? GO NEXT TEST					=>	00368
359	0005E	9A542	R5=5A5A1	/ TEST & COMPARE DATA CHANGE					A		
360	00060	5A5A3		/ REPEAT THIS TEST					A		
361	00062	07F40	GO FDI210						A		
361	00064	92540	FDI220 BTR=100021	/ SET ERR CODE					<<	00350	
362	00066	00022							A		
362	00068	965C3	GO1 VINZER1	/ BRANCH ERR PROCESS ROUTINE					A		
363	000AA	00003	#SKIP 3						=>	EXTERNAL BRANCH	
364			*****								
365			BANK REG W/R TEST								
366			*****								
367			#SKIP 1								
368	0006C	28022	FDI300 R4L=1021						<<	00358	
369	0006E	9A542	RS=000A1	/ SET TEST COUNT					A		
370	00070	000A3		/ TEST & COMPARE DATA SET					A		
370	00072	E1443	FDI310 FIBANK=RS						<<	00380	
371	00074	8C141	R0L=R0L	/ SET BANK					A		
372	00076	8CBF0	R0R0<RSH>	/ READ BANK					A		
373	00078	8CBF0	R0R0<RSH>	/ EDIT BANK DATA					A		
374	0007A	9C5B2	R0R0<RSH>	/ EDIT BANK DATA					A		
375	0007C	000F3	R0R0<RSH>	/ EDIT BANK DATA					A		
376	0007E	8D7D3	R4=R6<X>R5	/ CHECK BANK DATA					A		
376	00080	42053	IF Z=0	60 FDI320	/ ERR ? GO ERR CODE SET					A	
377	00082	49010	R4=R4<->101	/ TEST COUNT -1					=>	00381	
378	00084	54070	IF LZ=1	60 FDI400	/ THIS TEST END ? GO NEXT TEST					=>	00389
379	00086	9A542	RS=00051	/ TEST & COMPARE DATA CHANGE					A		
380	00088	00053		/ REPEAT THIS TEST					A		
380	0008A	07F31	GO FDI310						A		
381	0008C	92540	FDI320 BTR=100031	/ SET ERR CODE					<<	00370	
382	0008E	00033							A		
382	00090	965C3	GO1 VINZER1	/ BRANCH ERR PROCESS ROUTINE					A		
383	00092	00003	#SKIP 5						=>	EXTERNAL BRANCH	

Figure 7. JUCOM 1600 MLL Description

For these reasons, microprogrammers need not be conscious of which microoperations belong to any particular control storage space.

- ii) The preprocessor is able to check the discrepancies of the microoperation combination within the word or between the words, due to its use of the residual control or bit steering in the microprogram. Of course, MIXS, which was previously mentioned implements the final check.
- iii) The preprocessor performs the above functions and outputs information for automatic address assignment at the same time.

Figure 6 shows a sample description for the microprogram description used for ACOS System 1000 and its assemble list (it contains only the bit pattern for the main control storage).

#### 4.3 Microcomputer cross software.

µCOM 1600 microcomputer was developed by NEC 5 years ago. Although NEC has the exclusive compilers, the program property developed before has used assembly language in the firmware area.

Because the need to improve this situation has existed up until this time, µCOM 1600 MLL (middle level language) has just been developed and the preprocessor is now ready to use.

For that reason, MLL only improves readability/writability and there are no structured control statements. Figure 7 describes the µCOM 1600 MLL.

#### 5. Conclusion.

Machine dependent high-level languages (tailored languages) for each machine and the preprocessor to convert those languages into assembly level languages, have been developed.

The preprocessor has been designed as a general purpose and interpretive processor and this allows the designer to easily perform the required process using the process definition description.

The automatic address assignment program frees the designer from having to worry about the address assignment for each word.

By means of these tools, MDS has been radically extended and is able to satisfy the designer's request for high-level languages.

Currently, a general purpose analyzer which checks the logic of the firmware is being development. Together with the MIXS simulator, it will shorten the time necessary for developd various kinds of firmware and increase their reliability.

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 Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD**
**1 A component-based approach for embedded software development**
*I-Ling Yen; Goluguri, J.; Bastani, F.; Khan, L.; Linn, J.*

Object-Oriented Real-Time Distributed Computing, 2002. (ISORC 2002). Proc Fifth IEEE International Symposium on , 2002

Page(s): 402 -410

[\[Abstract\]](#) | [\[PDF Full-Text \(349 KB\)\]](#) **CNF**
**2 An on-line repository for embedded software**
*Yen, I.-L.; Khan, L.; Prabhakaran, B.; Bastani, F.B.; Linn, J.*

Tools with Artificial Intelligence, Proceedings of the 13th International Conference on , 2001

Page(s): 314 -321

[\[Abstract\]](#) | [\[PDF Full-Text \(117 KB\)\]](#) **CNF**
**3 System for automated validation of embedded software in multiple operating configurations**
*Lingamarla, S.; Singh, G.; Limburg, J.; Watson, M.; Edwards, G.; Gobrogge,*

Automated Software Engineering, 1999. 14th IEEE International Conference on , 1999

Page(s): 323 -326

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<u>L3</u>	(database\$ or data base\$ or repositor\$ or archiv\$) near5 l1	13	<u>L3</u>
<u>L2</u>	embedded adj (chip* or microchip* or hardware or software)	3	<u>L2</u>
<u>L1</u>	(firmware or firm adj ware)	116	<u>L1</u>

END OF SEARCH HISTORY

File 348:EUROPEAN PATENTS 1978-2002/Dec W01

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File 349:PCT FULLTEXT 1979-2002/UB=20021205,UT=20021128

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Set	Items	Description
S1	9167	FIRMWARE OR FIRM()WARE
S2	62	S1(5N) (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIVE???)
S3	2585	EMBEDDED(1W) (CHIP? ? OR MICROCHIP? ? OR PART? ? OR ELEMENT? ? OR MODULE? ? OR HARDWARE OR SOFTWARE OR PROCESSOR? ? OR MICROPROCESSOR? ?)
S4	121035	BIOS OR CMOS OR MICROCODE OR BOOTSTRAP OR PROGRAMMABLE OR -PROGRAMMED
S5	28952	S4(3N) (CHIP? ? OR CHIPSET? ? OR MICROCHIP? ? OR BOARD? ? OR HARDWARE OR COMPONENT? ? OR PART? ? OR ROM? ? OR PROM? ? OR -EPROM? ? OR EPROM? ? OR SEMICONDUCT??? OR SEMI(W)CONDUCT??? -OR DEVICE? ?)
S6	26	S2 AND IC=G06F
S7	36	S2 NOT S6
S8	9	S3(5N) (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIVE? ?)
S9	52	S5(3N) (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIVE? ?)

6/5,K/2 (Item 2 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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00298839

**Data access system for a file access processor**  
**Datenzugriffssystem für einen Dateizugriffsprozessor**  
**Système d'accès de données pour un processeur d'accès de fichier**  
PATENT ASSIGNEE:

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INVENTOR:

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13760, (US)

Bennett, Robert Bradley, 3718 Country Club Road, Endwell N.Y. 13760, (US)

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PATENT (CC, No, Kind, Date): EP 312786 A2 890426 (Basic)  
EP 312786 A3 910327  
EP 312786 B1 951227

APPLICATION (CC, No, Date): EP 88115429 880921;

PRIORITY (CC, No, Date): US 110370 871019

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-017/30

CITED REFERENCES (EP A):

NEC RESEARCH & DEVEL., no. 51, October 1978, pages 1-10; K. HAKOZAKI et  
al.: "A conceptual design of a generalized database subsystem"  
DISTRUBUTED DATABASES, STATE OF THE ART REPORT, Maidenhead, INFOTECH  
INTERNAT., 1979, pages 75-85, of VI+570 pp.8 refs.; W.W. CHU: "Design  
considerations of file directory systems for distrubuted databases"  
IBM TECHNICAL DISCLOSURE BULLETIN, vol. 25, no. 7A, December 1982, pages  
3280-3281, New York, US; R.W. DEY et al.: "Anticipation of data set  
expansion";

ABSTRACT EP 312786 A2

A Data Access System for a File Access Processor for servicing requests  
from a set of Application Support Processors, which can exist in a global  
network, with each Application Support Processor sharing access to data  
in files stored by the File Access Processor. The File Access Processor  
manages access to a set of data files and information about files held in  
file directories, which allow for managing file collections, can relate  
to each other hierarchically, and may be shared. Each Application Support  
Processor also maintains therein an internal cache of file information to  
improve performance by reducing communications required with the File  
Access Processor for information about files. The File Access Processor  
provides the Application Support Processors with information for updating  
and maintenance of local caches of directory and file description  
information through a centralized accumulation and distribution of cache  
change notifications. The Data Access System also provides for permanent  
storage in the form of catalogs for retaining shared information about  
files, directories, and control information. A separate access method and  
storage facility is utilized for these catalogs from that utilized for  
file data, permitting flexibility in access to catalog information which  
require access only occasionally, while retaining high performance in the  
access to file data.

ABSTRACT WORD COUNT: 210

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 890426 A2 Published application (Alwith Search Report  
;A2without Search Report)  
Examination: 891004 A2 Date of filing of request for examination:  
890809  
Search Report: 910327 A3 Separate publication of the European or  
International search report  
Change: 930331 A2 Representative (change)  
Change: 930512 A2 Representative (change)

Examination: 930519 A2 Date of despatch of first examination report:  
930402  
Change: 940914 A2 Representative (change)  
Grant: 951227 B1 Granted patent  
Oppn None: 961218 B1 No opposition filed  
Lapse: 971015 B1 Date of lapse of the European patent in a  
Contracting State: GB 960921

LANGUAGE (Publication,Procedural,Application): English; English; English  
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPABF1	753
CLAIMS B	(English)	EPAB96	678
CLAIMS B	(German)	EPAB96	599
CLAIMS B	(French)	EPAB96	877
SPEC A	(English)	EPABF1	8884
SPEC B	(English)	EPAB96	9043
Total word count - document A			9638
Total word count - document B			11197
Total word count - documents A + B			20835

INTERNATIONAL PATENT CLASS: G06F-017/30

...SPECIFICATION discusses concepts of distributed data bases but does not show specific rules for hardware and software design.

The paper "A Conceptual Design of a Generalized **Database** Subsystem" describes hardware and **firmware** components for the management of **data bases**. The described management system is incorporated in a separate controller, for instance a microcomputer. Purpose of this management system is to separate data base functions...

6/5,K/3 (Item 3 from file: 348)

DIALOG(R) File 348:EUROPEAN PATENTS

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00131810

**Plant management system**

**Werkführungssystem**

**Systeme de gestion d'usine**

PATENT ASSIGNEE:

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INVENTOR:

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, (US)

LEGAL REPRESENTATIVE:

Rentzsch, Heinz, Dipl.-Ing. et al (9532), Honeywell Holding AG Patent-  
und Lizenzabteilung Postfach 10 08 65, 63008 Offenbach, (DE)

PATENT (CC, No, Kind, Date): EP 138135 A2 850424 (Basic)

EP 138135 A3 870325

EP 138135 B1 930811

APPLICATION (CC, No, Date): EP 84111709 841001;

PRIORITY (CC, No, Date): US 540061 831007

DESIGNATED STATES: BE; DE; FR; GB

INTERNATIONAL PATENT CLASS: G06F-017/00

CITED PATENTS (EP A): US 4296464 A; US 4296464 A; JP 57114901 A; JP  
57114901 A

CITED REFERENCES (EP A):

PROCEEDINGS OF THE INTERNATIONAL FEDERATION OF AUTOMATIC CONTROL, Proc.  
6, 6th World Congress, Boston, 24th-30th August 1975, pages 1-7; M.A.  
HASSAN et al.: "A computer network for improved control"

PROCEEDINGS OF 17th IEEE COMPUTER SOCIETY INTERNATIONAL CONFERENCE,  
Washington, D.C., US, 5th-8th September 1978, pages 229-235, IEEE, New  
York, US; C.R. BERG: "A communication system for distributed process  
control"

PROCEEDINGS OF THE CONFERENCE ON MICROPROCESSORS IN AUTOMATION AND  
COMMUNICATIONS, London, GB, 19th-22nd September 1978, pages 155-168; H.  
DIETSCH: "Microprocessors in IEC-bus-compatible units"

ABSTRACT EP 138135 A2

Plant management system.

A plant management system includes one or more digital process control and data acquisition subsystems and a plant control network. Each control subsystem includes a data highway, and process control, and process interface units. The plant control network is a token-passing distributed plant control network in which a plurality of physical modules communicate with one another over a plant control bus. While there are a limited number of different types with each type having different functions, all physical modules have common units, one of which is a module central processor unit. Both the number of modules and the number of types of modules of a plant control network have both a maximum and a minimum. Reliability of the plant management system is improved by a provision for redundancy at the physical module level. One type of physical module provides data communication and translation facilities between the plant control network bus and the data highway of a control subsystem. Another type of module provides a universal operator station at which is available all information and capabilities needed by an operator to operate a plant.

ABSTRACT WORD COUNT: 188

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 850424 A2 Published application (Alwith Search Report  
;A2without Search Report)  
Change: 850619 A2 Title of invention (German) (change)  
Change: 850619 A2 Title of invention (French) (change)  
Search Report: 870325 A3 Separate publication of the European or  
International search report  
Examination: 871104 A2 Date of filing of request for examination:  
870907  
Examination: 881214 A2 Date of despatch of first examination report:  
881102  
Grant: 930811 B1 Granted patent  
Oppn: 940629 B1 Opposition 01/940509 Siemens AG; Postfach 22 16  
34; D-80506 Munchen; (DE)  
Lapse: 941130 B1 Date of lapse of the European patent in a  
Contracting State: BE 931031  
Amended: 961211 B2 Maintenance of the European patent as amended

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB96	648
CLAIMS B	(German)	EPAB96	522
CLAIMS B	(French)	EPAB96	748
SPEC B	(English)	EPAB96	5946
Total word count - document A			0
Total word count - document B			7864
Total word count - documents A + B			7864

INTERNATIONAL PATENT CLASS: G06F-017/00

...SPECIFICATION units that are linked together by a data highway and each of the major units of such a system includes a microprocessor with its own **firmware** and **data base** to provide a distributed control system. Such distributed digital data acquisition and control systems to date have been essentially limited to controlling part of the...

6/5,K/18 (Item 15 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00761431

A SYSTEM, METHOD, AND ARTICLE OF MANUFACTURE FOR PROVIDING COMMERCE-RELATED

**WEB APPLICATION SERVICES  
SYSTEME, PROCEDE ET ARTICLE MANUFACTURE DESTINES A LA FOURNITURE DE  
SERVICES D'APPLICATION DANS LE WEB LIES AU COMMERCE**

Patent Applicant/Assignee:

ACCENTURE LLP, 100 South Wacker Drive, Chicago, IL 60606, US, US  
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Inventor(s):

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Legal Representative:

BRUESS Steven C (agent), Merchant & Gould P.C., P.O. Box 2903,  
Minneapolis, MN 55402-0903, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200073957 A2-A3 20001207 (WO 0073957)

Application: WO 2000US14420 20000525 (PCT/WO US0014420)

Priority Application: US 99321492 19990527

Designated States: AE AG AL AM AT AT (utility model) AU AZ BA BB BG BR BY  
CA CH CN CR CU CZ CZ (utility model) DE DE (utility model) DK DK (utility  
model) DM DZ EE EE (utility model) ES FI FI (utility model) GB GD GE GH  
GM HR HU ID IL IN IS JP KE KG KP KR KR (utility model) KZ LC LK LR LS LT  
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(utility model) SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE  
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: **G06F-017/30**

International Patent Class: **G06F-017/60 ; G06F-009/44**

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 150171

English Abstract

A system, method, and article of manufacture are provided that afford a combination of commerce-related web application services. Various features are included such as allowing purchase of products and services via a displayed catalog. As an option, such catalog may be personalized. In various embodiments, a virtual shopping cart environment may be provided. Further, data, i.e. specifications, details, etc., relating to the products and services may be displayed along with a comparison between different products and services. Data relating to needs of a user may also be received for the purpose of outputting a recommendation of the products and services based on the inputted needs. Optionally, features of the products and services may be listed in order to allow the user to configure a specifically tailored product or service. Yet another aspect of the present invention includes outputting an estimate relating to a price and/or availability of the products and services. Further, an order for the products and services may be received after which a tax and a shipping fee are calculated. A status of the delivery of the ordered products and services may also be provided.

French Abstract

L'invention concerne un systeme, un procede et un article manufacture destines a la fourniture d'une combinaison de services d'application dans le Web lies au commerce. Le systeme presente plusieurs caracteristiques telles que l'achat de produits et de services grace a un catalogue affiche. En option, ce catalogue peut etre personnalise. Plusieurs modes de realisation peuvent comprendre un environnement de chariot de supermarche virtuel. En outre, des donnees, c.-a-d. des specifications, des details, etc., se rapportant aux produits et services peuvent etre affichees en meme temps qu'une comparaison entre differents produits et services. On peut aussi inclure des donnees relatives aux besoins d'un utilisateur afin de recommander des produits et services donnees sur la base des besoins entres. Eventuellement, on peut etablir une liste des



caracteristiques des produits et services afin de permettre a l'utilisateur de configurer un produit ou un service personnalise. Dans un autre aspect de la presente invention, on peut produire une estimation du prix et/ou de la disponibilite des produits et services. En outre, une commande peut etre recue et une taxe et des frais d'expedition calcules. Un etat de l'expedition des produits et services commandes peut egalement etre etabli.

Legal Status (Type, Date, Text)

Publication 20001207 A2 Without international search report and to be republished upon receipt of that report.  
Examination 20010222 Request for preliminary examination prior to end of 19th month from priority date  
Search Rpt 20010816 Late publication of international search report  
Republication 20010816 A3 With international search report.

Main International Patent Class: G06F-017/30

International Patent Class: G06F-017/60 ...

... G06F-009/44

Fulltext Availability:

Detailed Description

Detailed Description

... of the existing system by indicia coding the components on the pictorial representation.

In more detail, referring to operation 51a of Figure 1H-1, a **database** is created having three areas each adapted to contain a group of components of the system. In operation 51b, components of the existing system that...

6/5,K/20 (Item 17 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00757129 \*\*Image available\*\*

**AUTOMATIC BROKER TOOLS AND TECHNIQUES**

**OUTILS ET TECHNIQUES POUR COURTIER AUTOMATIQUE**

Patent Applicant/Assignee:

OGILVIE John W L, 1211 East Yale Avenue, Salt Lake City, UT 84105, US, US  
(Residence), US (Nationality)

Legal Representative:

OGILVIE John W L, Computer Law++, 8 East Broadway, Suite 725, Salt Lake City, UT 84111, US

Patent and Priority Information (Country, Number, Date):

Patent: WO 200070516 A1 20001123 (WO 0070516)

Application: WO 2000US10376 20000418 (PCT/WO US0010376)

Priority Application: US 99134383 19990515

Designated States: AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-017/60

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 17983

English Abstract

The invention provides sampling, escrowing, and other tools and

techniques for facilitating transactions that involve digital goods (800, 804). Transactions may be of the goods-for-payment type, or they may be barter transactions that exchange goods for goods. Digital goods (800, 804) may be escrowed with an automatic broker (504). The broker (504) may also create and distribute samples of the goods (802), which are created by techniques such as distorting or burdening part or all of a copy of the goods. In some cases the broker (504) may accept and act on approval or disapproval notices from the parties to a transaction, to cancel or complete a transaction. In other cases, the broker's participation is limited to providing and/or authenticating samples.

#### French Abstract

L'invention concerne des outils et des techniques automatiques d'échantillonnage, de dépôts, etc. pour faciliter les transactions portant sur des biens numériques (800, 804). Ces transactions peuvent être du type biens contre paiement, ou bien des opérations de troc, à savoir un échange de biens. Les biens numériques (800, 804) peuvent être déposés entre les mains d'un courtier automatique (504). Ce dernier (504) peut également créer et distribuer des échantillons de biens (802), créés au moyen de techniques permettant par exemple d'altérer une partie ou la totalité d'un exemplaire des biens. Dans certains cas, le courtier (504) peut accepter ou suivre les avis des parties de la transaction indiquant leur accord ou leur désaccord avec ladite transaction, pour annuler ou mener à terme une transaction. Dans d'autres cas, la participation du courtier se limite à la fourniture et/ou à l'authentification d'échantillons.

Legal Status (Type, Date, Text)

Publication 20001123 A1 With international search report.

Examination 20010823 Request for preliminary examination prior to end of 19th month from priority date

Main International Patent Class: G06F-017/60

Fulltext Availability:

Detailed Description

#### Detailed Description

... by those of skill in the pertinent art(s) using the teachings presented here and programming languages and tools such as Java, Pascal, C++, C, **database** languages, APIs, SDKs, assembly, **firmware**, microcode, and/or other languages and tools. Suitable signal formats may be embodied in analog or digital form, with or without error detection and/or...

6/5,K/22 (Item 19 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00554418 \*\*Image available\*\*

**METHOD AND APPARATUS FOR AUTHENTICATING VENDING MACHINE SALES DATA**

**PROCEDE ET APPAREIL D'AUTHENTIFICATION DE DONNEES RELATIVES AUX VENTES DE DISTRIBUTEUR AUTOMATIQUE**

Patent Applicant/Assignee:

WALKER DIGITAL LLC,  
TEDESCO Daniel E,  
JORASCH James A,

Inventor(s):

TEDESCO Daniel E,  
JORASCH James A,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200017791 A1 20000330 (WO 0017791)

Application: WO 99US18426 19990812 (PCT/WO US9918426)

Priority Application: US 98157150 19980918

Designated States: AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE

ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT

LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT

UA UG US UZ VN YU ZA ZW GH GM KE LS MW SD SL SZ UG ZW AM AZ BY KG KZ MD

RU TJ TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF  
CG CI CM GA GN GW ML MR NE SN TD TG

Main International Patent Class: **G06F-017/60**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 7183

#### English Abstract

A method and apparatus is disclosed that documents the authentication of sales data generated at an automatic sales machine. In operation, the apparatus accumulates sales data. The apparatus also encodes the sales data, thereby deriving encoded sales data (618). The apparatus further outputs the encoded sales data to a user (622). A method and apparatus for authenticating the documented sales data is also disclosed.

#### French Abstract

L'invention concerne un appareil et un procede permettant le recensement des donnees relatives aux ventes generees au niveau d'un distributeur automatique. En fonctionnement, l'appareil cumule les donnees relatives aux ventes. Il code les donnees relatives aux ventes, derivant ainsi des donnees relatives aux ventes codees (618). L'appareil envoie egalement les donnees codees relatives aux ventes a un utilisateur (622). Un procede et un appareil pour authentifier les donnees relatives aux ventes recensees sont egalement decrits.

Main International Patent Class: **G06F-017/60**

Fulltext Availability:

Detailed Description

#### Detailed Description

... 100 may encode the retrieved sales data using public key cryptography.

In this case, vending machine 100 would store a private key, preferably in a **database**, chip or **firmware** protected by a secure perimeter.

Vending machine 100 would encrypt the sales data with the private key to generate a code that is not decipherable...

**6/5,K/23 (Item 20 from file: 349)**

DIALOG(R)File 349:PCT FULLTEXT

(c) 2002 WIPO/Univentio. All rts. reserv.

00539954 \*\*Image available\*\*

**ADAPTIVELY SHRINKING SOFTWARE**

**REDUCTION ADAPTATIVE DE L'ENCOMBREMENT D'UN LOGICIEL**

Patent Applicant/Assignee:

OGILVIE John W L,

Inventor(s):

OGILVIE John W L,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200003327 A1 20000120 (WO 0003327)

Application: WO 98US21139 19981007 (PCT/WO US9821139)

Priority Application: US 9892615 19980713

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD

MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ

VN YU ZW GH GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH

CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW

ML MR NE SN TD TG

Main International Patent Class: **G06F-015/163**

International Patent Class: **G06F-009/00 ; G06F-009/46**

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 13600

#### English Abstract

Method, articles, signals, and systems are provided for adaptively shrinking software (208). The software includes one or more adaptive features (204). A tracking facility (600) tracks use of the adaptive features (204), and when it is determined that a given feature is unwanted or unnecessary in the software configuration preferred by a given user, a separation facility (602) separates the feature from the rest of the software. The feature is then archived or deleted. In this manner, resources such as disk space are conserved, program load time and memory requirements are reduced, and user interfaces and supporting code are tailored to meet the needs of particular users.

#### French Abstract

L'invention concerne des procedes, des articles, des signaux et des systemes permettant de reduire l'encombrement d'un logiciel (208). Ledit logiciel comprend une ou plusieurs fonctions adaptatives (204). Un organe de suivi (600) suit l'utilisation des fonctions adaptatives (204) et lorsqu'il est determine qu'une fonction est indesirable ou inutile dans la configuration d'un logiciel preferee par un utilisateur donne, un organe de separation (602) separe la fonction du reste du logiciel. La fonction est ensuite archivee ou supprimee. Ainsi, des ressources tels que l'espace disque sont conservees, le temps de chargement de programme et la capacite de memoire requise sont reduits, et les interfaces utilisateurs et le code de prise en charge sont adaptes aux besoins d'utilisateurs particuliers.

Main International Patent Class: **G06F-015/163**

International Patent Class: **G06F-009/00 ...**

**... G06F-009/46**

Fulltext Availability:

Detailed Description

Detailed Description

... by those of skill in the pertinent art(s) using the teachings presented here and programming languages and tools such as Java, Pascal, C++, C, **database** languages, APIs, SDKs, assembly, **firmware**, microcode, and/or other languages and tools.

A Configured Computer

Figure 2 illustrates a computer 200 configured for software feature selection according to the invention...

**6/5,K/24 (Item 21 from file: 349)**

DIALOG(R)File 349:PCT FULLTEXT

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00516886 \*\*Image available\*\*

**MESSAGE CONTENT PROTECTION AND CONDITIONAL DISCLOSURE**

**PROTECTION ET DIVULGATION CONDITIONNELLE DU CONTENU D'UN MESSAGE**

Patent Applicant/Assignee:

OGILVIE John W L,

Inventor(s):

OGILVIE John W L,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9948238 A1 19990923

Application: WO 98US23841 19981111 (PCT/WO US9823841)

Priority Application: US 9878175 19980316; US 98184206 19981102

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD

MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ

VN YU ZW GH GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH

CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW

ML MR NE SN TD TG

Main International Patent Class: H04L-009/00

International Patent Class: **G06F-013/00**

Publication Language: English  
Fulltext Availability:  
Detailed Description  
Claims  
Fulltext Word Count: 13901

English Abstract

Method and systems are provided for controlling the disclosure of sensitive information (310). Disclosure is controlled in the sense that (a) the information is not disclosed until predefined conditions (302) are met, such as the passage of a certain time without an authorized update request for secrecy, (b) copies of the information are protected by encryption (312) and by widespread, unpredictable storage, so that at least one copy will be available when disclosure is required, (c) the information is kept secret until disclosure is required, and (d) when disclosure is required, the information is sent to predefined destination such as email addresses or posted to web sites, in a predefined format.

French Abstract

Procede et systemes permettant de controler la divulgation d'informations sensibles (310). La divulgation est controlee par le fait que (a) l'information n'est pas devoilee tant que des conditions predefinies (302) ne sont pas satisfaites, telles que l'ecoulement d'un certain temps sans demande de mise a jour autorisee de secret, (b) la protection des copies des informations par cryptage (312) et par enregistrement repartit et imprevu si bien qu'au moins une copie est disponible lorsque la divulgation est requise, (c) la tenue secrete des informations jusqu'a la demande de divulgation et (d), lorsque la divulgation est demandee, l'envoi des informations a une destination predefinie telle qu'une adresse de courrier electronique, ou a des sites Web, dans un format predetermine.

International Patent Class: G06F-013/00

Fulltext Availability:  
Detailed Description

Detailed Description

... by those of skill in the pertinent art(s) using the teachings presented here and programming languages and tools such as Java, Pascal, C++, C, **database** languages, APIs, SDKs, assembly, **firmware**, microcode, and/or other languages and tools. Suitable signal formats

6/5,K/25 (Item 22 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
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00418748 \*\*Image available\*\*

**SYSTEMS AND METHODS FOR SECURE TRANSACTION MANAGEMENT AND ELECTRONIC RIGHTS PROTECTION**

**SYSTEMES ET PROCEDES DE GESTION DE TRANSACTIONS SECURISEES ET DE PROTECTION DE DROITS ELECTRONIQUES**

Patent Applicant/Assignee:

INTERTRUST TECHNOLOGIES CORP,

Inventor(s):

GINTER Karl L,  
SHEAR Victor H,  
SIBERT W Olin,  
SPAHN Francis J,  
VAN WIE David M,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9809209 A1 19980305

Application: WO 97US15243 19970829 (PCT/WO US9715243)

Priority Application: US 96706206 19960830

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES  
FI GB GE GH HU IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN  
MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZW

GH KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH DE DK ES FI  
FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG  
Main International Patent Class: **G06F-001/00**  
Publication Language: English  
Fulltext Availability:  
Detailed Description  
Claims  
Fulltext Word Count: 195626

#### English Abstract

The present invention provides systems and methods for electronic commerce including secure transaction management and electronic rights protection. Electronic appliances such as computers employed in accordance with the present invention help to ensure that information is accessed and used only in authorized ways, and maintain the integrity, availability, and/or confidentiality of the information. Secure subsystems used with such electronic appliances provide a distributed virtual distribution environment (VDE) that may enforce a secure chain of handling and control, for example, to control and/or meter or otherwise monitor use of electronically stored or disseminated information. Such a virtual distribution environment may be used to protect rights of various participants in electronic commerce and other electronic or electronic-facilitated transactions. Secure distributed and other operating system environments and architectures, employing, for example, secure semiconductor processing arrangements that may establish secure, protected environments at each node. These techniques may be used to support an end-to-end electronic information distribution capability that may be used, for example, utilizing the "electronic highway".

#### French Abstract

La presente invention concerne des systemes et des procedes de commerce electronique comprenant une gestion de transactions securisees et la protection de droits electroniques. Des appareils electroniques tels que des ordinateurs utilisent conformement a la presente invention contribuent a assurer que l'accès aux informations et l'utilisation des informations ne se font que par des voies autorisees et ils maintiennent l'integrite, la disponibilite et/ou la confidentialite des informations. Des sous-systemes securises utilises avec ces appareils electroniques constituent un environnement de distribution virtuel (VDE) reparti pouvant faire valoir une chaine securisee de traitement et de commande, par exemple, pour commander et/ou mesurer ou encore controler l'utilisation d'informations memorisees ou disseminees electroniquement. Cet environnement de distribution virtuel peut etre utilise pour proteger les droits de divers participants dans le commerce electronique et dans d'autres transactions electroniques ou dans lesquelles intervient l'electronique. Des environnements et des architectures de systemes repartis securises et autres systemes d'exploitation emploient, par exemple, des arrangements de traitement a semi-conducteurs securises pouvant etablir des environnements proteges securises a chaque noeud. On peut utiliser ces techniques pour apporter un soutien a une capacite de distribution d'informations electroniques de bout-en-bout pouvant etre utilisees, par exemple, en empruntant l'"autoroute electronique".

Main International Patent Class: **G06F-001/00**  
Fulltext Availability:  
Detailed Description

#### Detailed Description

... through an  
"appliance link" 510. SPU'firmware" 508 in this example is  
44 software" such as a "computer program(s)" "embedded" within  
- 184 chip 504. **Firmware** 508 makes the hardware 506 work.

Hardware 506 preferably contains a processor to perform  
instructions specified by firmware 508. "Hardware" 506 also  
contains long-term...

7/5,K/3 (Item 3 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2002 European Patent Office. All rts. reserv.

01230997

Methods and apparatus for managing devices without network attachments  
Verfahren und Vorrichtung zur Verwaltung von Geraten ohne Netzwerkanbindung  
Methodes et appareil pour des dispositifs de gestion sans connexions de  
reseau

PATENT ASSIGNEE:

LSI LOGIC CORPORATION, (561302), 1551 McCarthy Boulevard, Milpitas, CA  
95035, (US), (Applicant designated States: all)

INVENTOR:

Weber, Bret S., 2521 North Tee Time, Wichita, Kansas 67205, (US)  
Dekoning, Rodney A., 6876 SW Stone Gap, Augusta, Kansas 67010, (US)  
Delaney, William P., 11715 Siefkes Street, Wichita, Kansas 67230, (US)  
Jantz, Ray M., 1914 Capri Circle, Wichita, Kansas 67207, (US)  
Courtright II, William V., 117 Washington Road, Pittsburg, Pennsylvania  
15221, (US)

LEGAL REPRESENTATIVE:

Hallam, Arnold Vincent et al (31455), LEWIS & TAYLOR 5 The Quadrant,  
Coventry, CV1 2EL, (GB)

PATENT (CC, No, Kind, Date): EP 1067732 A2 010110 (Basic)  
EP 1067732 A3 020612

APPLICATION (CC, No, Date): EP 2000305819 000710;

PRIORITY (CC, No, Date): US 350515 990709

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: H04L-012/24; H04L-012/26

ABSTRACT EP 1067732 A2

A system and method for monitoring and managing devices on a network.  
The system and method preferably comprises a proxy server connected to  
the network and a managed device connected to the proxy server. The  
system further comprises storage means for storing a device management  
application program associated with the managed device, and a management  
station in communication with the managed device via the proxy server and  
in communication with the storage means. The management station  
preferably is configured to retrieve the device management application  
program from the storage means and process the device management  
application program. As the management station processes the device  
management application program, the management station is able to monitor  
and manage the managed device. In particular, the management station can  
send management commands to a controller of the managed device via the  
proxy server, and the management station can receive notifications from  
the managed device, also via the proxy server.

ABSTRACT WORD COUNT: 155

NOTE:

Figure number on first page: 1

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 010110 A2 Published application without search report  
Examination: 010110 A2 Date of request for examination: 20000731  
Change: 020612 A2 International Patent Classification changed:  
20020422

Search Report: 020612 A3 Separate publication of the search report

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200102	1562
SPEC A	(English)	200102	13206
Total word count - document A			14768
Total word count - document B			0
Total word count - documents A + B			14768

...SPECIFICATION 1004 with respect to firmware sets. Thus, management  
interface application 1014 preferably retrieves the firmware that it

needs for the destination device 1006 from a **firmware repository** 1022 residing on a web server 1008 or other suitable storage location (step 10H). The selected firmware is then loaded into the destination device 1006...

...systems (step 11L). As with the configuration application process illustrated in Fig. 10 and discussed above, each of the management interface applications 1124 retrieves a **firmware set** from **firmware repository** 1128 in server 1108 or other suitable storage location (step 11M), and applies the controller firmware set to the controller 1126 of the appropriate destination...

7/5,K/4 (Item 4 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
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01148150

**Gaming device identification method and apparatus**  
**Verfahren und Vorrichtung zum Kennzeichnen eines Spielgerates**  
**Procede et dispositif pour l'identification d'un appareil de jeu**  
PATENT ASSIGNEE:

International Game Technology, (2015071), 9295 Prototype Way, Reno,  
Nevada 89511-8986, (US), (Applicant designated States: all)

INVENTOR:

Wells, Bill, 4450 Rio Encantado, Reno, Nevada 89502, (US)

McGlone, James T., 30 Golden Currant, Reno, Nevada 89511, (US)

LEGAL REPRESENTATIVE:

Manitz, Finsterwald & Partner (100614), Postfach 22 16 11, 80506 Munchen,  
(DE)

PATENT (CC, No, Kind, Date): EP 1001391 A2 000517 (Basic)

EP 1001391 A3 010829

APPLICATION (CC, No, Date): EP 99119352 990929;

PRIORITY (CC, No, Date): US 172787 981014

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G07F-017/32

ABSTRACT EP 1001391 A2

An electronic memory which cannot be modified or erased, such as one-time programmable or add-only (OTP/AO) memory is used for storing direct or indirect indications of characteristics of a gaming terminal or its components. Preferably, a plurality of gaming terminal boards includes memory that has one or more rows of OTP/AO memory for storing information such as memory or board serial numbers, manufacture date, processor speed or type, software or hardware version numbers, and the like. Such information is preferably used in connection with assuring the appropriateness of modifications or configurations of the gaming terminal, such as downloading software or other software modification and/or changes to hardware configuration. OTP/AO information can be used in connection with verifying identity or characteristics of software or hardware, e.g. for warranty purposes. The non-erasable and non-modifiable nature of the information provides assurance that software downloads or other modifications will not be inappropriate because of inadvertent or intentional misidentification of a component.

ABSTRACT WORD COUNT: 158

NOTE:

Figure number on first page: 1

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 000517 A2 Published application without search report

Search Report: 010829 A3 Separate publication of the search report

Examination: 020626 A2 Date of request for examination: 20011112

Change: 020626 A2 Designated contracting states changed 20020507

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text Language Update Word Count

CLAIMS A (English) 200020 643



SPEC A (English) 200020 6801  
Total word count - document A 7444  
Total word count - document B 0  
Total word count - documents A + B 7444

...SPECIFICATION providing storage on hard drives or other well-known storage media). In the depicted embodiment, information, during program design process, is held in an engineering database 154. And software and firmware engineers use and modify such information via computers 156, 158 having at least indirect access to engineering database 154. Preferably, programs or other data which...

7/5,K/7 (Item 7 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
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00999983

COMPUTERIZED SHOPPING CART WITH STORAGE AND DISTRIBUTION SYSTEM, FOR  
SUPERMARKET USE  
RECHNERGESTUTZTER EINKAUFSWAGEN FUR EINEN SUPERMARKT MIT SPEICHER UND  
AUSGABESYSTEM  
CHARIOT INFORMATISE POUR SUPERMARCHE MUNI D'UN SYSTEME DE STOCKAGE ET DE  
DISTRIBUTION

PATENT ASSIGNEE:

Fiordelisi, Luigi, (2634210), Valle d'Aosta, 3, sc.2/Pal.a RM, 00048  
Nettuno, (IT), (Proprietor designated states: all)

INVENTOR:

Fiordelisi, Luigi, Valle d'Aosta, 3, sc.2/Pal.a RM, 00048 Nettuno, (IT)

LEGAL REPRESENTATIVE:

Lanzoni, Luciano (47636), c/o Bugnion S.p.A. Via Vittorio Emanuele  
Orlando, 83, 00185 Roma, (IT)

PATENT (CC, No, Kind, Date): EP 923768 A2 990623 (Basic)

EP 923768 B1 020724

WO 9842239 981001

APPLICATION (CC, No, Date): EP 98912702 980323; WO 98IT67 980323

PRIORITY (CC, No, Date): IT 97RM165 970325; IT 98RM129 980305

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;  
MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: G07F-007/00; G07G-001/00; A47F-009/04

CITED PATENTS (EP B): EP 654477 A; EP 673006 A; WO 97/50055 A; DE 3940605 A

; FR 2596902 A; FR 2699381 A; GB 2035647 A; GB 2214470 A; US 3787063 A;

US 4071740 A; US 5250789 A; US 5424524 A

CITED PATENTS (WO A): US 5637847 A

CITED REFERENCES (EP B):

ANONYMOUS: "Grocery Buggy" IBM TECHNICAL DISCLOSURE BULLETIN, vol. 31,  
no. 6, November 1988, pages 399-400, XP002013065 New York, US;

CITED REFERENCES (WO A):

ANONYMOUS: "Grocery Buggy" IBM TECHNICAL DISCLOSURE BULLETIN, vol. 31,  
no. 6, November 1988, pages 399-400, XP002013065 New York, US;

NOTE:

No A-document published by EPO

LEGAL STATUS (Type, Pub Date, Kind, Text):

Change: 010418 A2 Title of invention (German) changed: 20010302

Application: 990317 A2 International application (Art. 158(1))

Grant: 020724 B1 Granted patent

Examination: 011128 A2 Date of dispatch of the first examination  
report: 20011015

Application: 990623 A2 Published application (Alwith Search Report  
;A2without Search Report)

Examination: 990818 A2 Date of request for examination: 19990603

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200230	3319
CLAIMS B	(German)	200230	3333
CLAIMS B	(French)	200230	3560
SPEC B	(English)	200230	4132

Total word count - document A 0  
Total word count - document B 14344  
Total word count - documents A + B 14344

...SPECIFICATION connected to the national power grid; the logic developed by the programs shown in TABLE 9 in general comprises software procedures 16 of operating nature ( **data base** , others) both process control **firmware** 17 (opening lock, activating motor or other actions) and software 18 for communication towards external units; in particular the former, as in FIGURE 9a, comprise...

...CLAIMS correctness of the shopping operations via an electronic unit (6) housed therein, whereby the computerized product insertion unit comprises a SHOPPING-COMPUTER (6) with related **data base** (6d), operating software (16) and **firmware** (17) for process control of the related parts, whereby the mechanical cart comprises a compartment for containing (1c2) the stowed products closed / openable by a...

7/5,K/25 (Item 14 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
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00828336 \*\*Image available\*\*

**NETWORK-BASED CONTENT DISTRIBUTION SYSTEM**  
**SYSTEME DE DISTRIBUTION DE CONTENU EN RESEAU**

Patent Applicant/Assignee:

VERIMATRIX INC, 6650 Lusk Blvd., Suite B-203, San Diego, CA 92121-2776,  
US, US (Residence), US (Nationality)

Inventor(s):

COOPER Robin Ross, 10605 Queen Avenue, La Mesa, CA 92941, US,  
KULAKOWSKI Robert T, 7304 Nocho Tapatia, Rancho Santa Fe, CA 92067, US,

Legal Representative:

ALTMAN Daniel E (agent), Knobbe, Martens, Olson and Bear, LLP, 620  
Newport Center Drive, 16th Floor, Newport Beach, CA 92660, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200161913 A2-A3 20010823 (WO 0161913)

Application: WO 2001US5438 20010220 (PCT/WO US0105438)

Priority Application: US 2000183638 20000218; US 2000250445 20001130

Designated States: AE AG AL AM AT AT (utility model) AU AZ BA BB BG BR BY  
BZ CA CH CN CR CU CZ CZ (utility model) DE DE (utility model) DK DK  
(utility model) DM DZ EE EE (utility model) ES FI FI (utility model) GB  
GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA  
MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SK (utility model)  
SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: H04L-029/06

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 20251

**English Abstract**

A method and system for transferring electronic media information over a public network in such a way as to provide safeguards for inappropriate distribution of copyright or otherwise protected materials are described. The media information is transparently watermarked with a unique ID, such as one generated from X.509 Digital Certificate and public-key cryptography public/private key pairs, such that the information can be identified as belonging to a particular individual. A system and method for monitoring the movement of such watermarked files, positively identifying people who have inappropriately distributed copyright

materials over a public network without permission, and taking appropriate enforcement action against such people.

#### French Abstract

L'invention concerne un procede et un systeme permettant le transfert d'information multimedia electronique par l'intermediaire d'un reseau public, d'une maniere permettant de fournir une protection contre la distribution non autorisee de materiaux proteges par un droit d'auteur ou par d'autres mesures de protection. L'information multimedia est filigranee en transparence au moyen d'une identification unique, par exemple l'identification generee par les certificats numeriques X.509 et par les paires de cles publique/privree de cryptographie a cle publique, de maniere que l'information peut etre identifiee comme appartenant a un individu particulier. L'invention concerne en outre un systeme et un procede permettant de surveiller les mouvements de ces fichiers filigranes, et d'identifier formellement les personnes qui ont procede a une distribution non autorisee de materiaux proteges par un droit d'auteur dans un reseau public, et de prendre les mesures appropriees a l'encontre de ces personnes .

#### Legal Status (Type, Date, Text)

Publication 20010823 A2 Without international search report and to be republished upon receipt of that report.  
Examination 20011213 Request for preliminary examination prior to end of 19th month from priority date  
Search Rpt 20020627 Late publication of international search report  
Republication 20020627 A3 With international search report.  
Search Rpt 20020627 Late publication of international search report  
Correction 20021017 Corrected version of Pamphlet: pages 1/7-7/7, drawings, replaced by new pages 1/7-7/7; due to late transmittal by the receiving Office  
Republication 20021017 A3 With international search report.

#### Fulltext Availability:

Detailed Description

#### Detailed Description

... of example, components, such as, software components, objectoriented software components, class components and task components, processes, functions, attributes, procedures, subroutines, segments of program code, drivers, **firmware** , microcode, circuitry, data, **databases** , data structures, tables, arrays, and variables.

The various components of the system may advantageously communicate with each other and other components comprising the respective computers...

7/5,K/29 (Item 18 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00787341 \*\*Image available\*\*

**SYSTEM FOR MANAGING ROUTING SERVERS AND SERVICES**

**SYSTEME DE GESTION DE SERVEURS ET SERVICES DE ROUTAGE**

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Legal Representative:

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Patent and Priority Information (Country, Number, Date):

Patent: WO 200120859 A1 20010322 (WO 0120859)  
Application: WO 2000US9 20000110 (PCT/WO US00000009)  
Priority Application: US 99393658 19990910; US 99173750 19991230

Designated States: AE AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ DE DK

DM EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR

LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ

TM TR TT TZ UA UG UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: H04L-012/66

International Patent Class: H04L-012/64; H04M-007/00

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 69212

English Abstract

A method, system, and computer program product for routing network traffic (calls in a Voice over Internet Protocol (VoIP)), which expands the capabilities of existing systems by providing faster and more efficient direction of network traffic, is disclosed. A routing management system includes a routing manager which maintains a list of local routes, establishes and manages connections to the routing server(s), exports routes to the routing server(s), imports disseminated routes from the routing server(s), obtains static global and dynamic routes from the routing server(s), caches those routes for future use, finds all matching routes for a particular number dialed by the user, and prioritizing those routes based on timing, access and ordering information. An additional embodiment contains at least one routing server which provides look-up services for gateway server(s), allows export of local routes from gateway server(s), and distributes translation data; and at least one gateway server which handles calls received on either the Internet protocol (IP) or traditional telephony networks. The gateway server bridges calls between the different kinds of networks, interacts with users, interfaces with the routing system.

French Abstract

L'invention concerne un procede, un systeme et un produit informatique permettant d'acheminer le trafic reseau (appels de telephonie sur Internet, VoIP), ce qui augmente les capacites des systemes existants par une orientation plus rapide et plus efficace du trafic reseau. Un systeme de gestion du routage comprend un gestionnaire de routage qui conserve une listes de trajets locaux, cree et gere des connexions vers des serveurs de routage, exporte des trajets vers lesdits serveurs, importe des trajets dissemines provenant de ces serveurs, obtient des trajets dynamiques et globaux fixes provenant desdits serveurs, met en antememoire lesdits trajets pour des utilisations futures, trouve tous les trajets correspondants pour un numero particulier compose par l'utilisateur et donne la priorite aux trajets en fonction d'informations de synchronisation, d'acces et d'ordre, Selon un autre mode de realisation, au moins un serveur de routage offre des services de recherche de serveurs passerelles, permet l'exportation de trajets locaux provenant de ces serveurs, et distribue des donnees de traduction; et au moins un serveur passerelle traite des appels recus sur les reseaux du

protocole internet ou de telephonie classiques. Le serveur passerelle relie les differents types de reseaux, interagit avec les utilisateurs, faisant l'interface avec le systeme de routage.

Legal Status (Type, Date, Text)

Publication 20010322 A1 With international search report.

Publication 20010322 A1 With a statement concerning non-prejudicial disclosure or exception to lack of novelty.

Examination 20010802 Request for preliminary examination prior to end of 19th month from priority date

Fulltext Availability:

Detailed Description

Detailed Description

... a static route configuration window.

FIG. 124 shows a disseminated route configuration window.

FIG. 125 shows a disseminated routes properties panel.

FIG. 126 shows a **database** manager configuration screen.

FIG. 127 displays a database server list dialog.

FIG. 128 shows a database server window.

FIG. 129 shows a client configuration dialog...

7/5,K/30 (Item 19 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00752353 \*\*Image available\*\*

**PORTAL SYSTEM AND METHOD**

**SYSTEME DE PORTAIL ET PROCEDE ASSOCIE**

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Legal Representative:

ALTMAN Daniel E, Knobbe, Martens, Olson and Bear, LLP, 620 Newport Center  
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Patent and Priority Information (Country, Number, Date):

Patent: WO 200065773 A2 20001102 (WO 0065773)

Application: WO 2000US11320 20000427 (PCT/WO US0011320)

Priority Application: US 99131290 19990427; US 2000484561 20000118; US  
2000484352 20000118; US 2000484807 20000118; US 2000483907 20000118; US  
2000483904 20000118; US 2000484755 20000118; US 2000484353 20000118

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY CA CH CN CR CU CZ CZ  
(utility model) DE DE (utility model) DK DK (utility model) DM DZ EE EE  
(utility model) ES FI FI (utility model) GB GD GE GH GM HR HU ID IL IN IS  
JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX NO NZ PL PT  
RO RU SD SE SG SI SK SK (utility model) SL TJ TM TR TT TZ UA UG UZ VN YU  
ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: H04L-012/00

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 66018

#### English Abstract

The present invention is a system and method for providing a portal interface to information and services accessible over a communication network. A portal system (102) stores one or more portal definitions. Each portal definition defines a portal interface which is configured to be displayed in a browser (302) while the browser accesses different web objects. A user computer (104) transmits a subscriber identification to the portal system (102). The subscriber identification identifies a portal definition stored on the portal system (102). The portal system (102) receives the subscriber identification transmitted from the user computer (104) and retrieves the portal definition identified by the subscriber identification. The portal system (102) then transmits the retrieved portal definition to the user computer (104), and the portal interface (304) defined by the portal definition is displayed within the browser on the user computer. In one embodiment, the portal interface (304) is persistently displayed in the browser (302).

#### French Abstract

La presente invention concerne un systeme et un procede de creation d'interface de portail pour etabliir un lien avec des d'informations et des services accessibles par l'intermediaire d'un reseau de communication. Un systeme de portail (102) stocke une ou plusieurs definitions de portail. Chaque definition de portail definit une interface de portail configuree de maniere a etre affichee dans un explorateur (302) lequell accede a differents objets du web. L'ordinateur (104) de l'utilisateur transmet une identification d'abonne a un systeme de portail (102). L'identification d'abonne identifie une definition de portail stockee dans le systeme de portail (102). Ce dernier (102) recoit l'identification d'abonne transmise par l'ordinateur (104) de l'utilisateur, et extrait la definition de portail identifiee par l'identification d'abonne. Le systeme de portail (102) transmet ensuite la definition de portail extraite a l'ordinateur (104) de l'utilisateur, et l'interface de portail (304) definie par la definition de portail est affichee dans l'explorateur de l'ordinateur de l'utilisateur. Dans un mode de realisation prefere, l'interface de portail (304) est continuellement affichee dans l'explorateur (302).

Legal Status (Type, Date, Text)

Publication 20001102 A2 Without international search report and to be  
republished upon receipt of that report.

Examination 20010201 Request for preliminary examination prior to end of  
19th month from priority date

#### Fulltext Availability:

Detailed Description

#### Detailed Description

... example, components, such as, software components, object-oriented software components, class components and task components, processes, functions, attributes, procedures, subroutines, segments of program code, drivers, **firmware**, microcode, circuitry, data, **databases**, data structures, tables, arrays, and variables.

As used herein, the terms "portal system" and "content server" are to be viewed as designations of one or...

7/5,K/32 (Item 21 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00478160 \*\*Image available\*\*

IDENTIFICATION IN COMPUTER SYSTEMS USING INHERENT CHARACTERISTICS

IDENTIFICATION D'UTILISATEURS DE SYSTEMES INFORMATIQUES PAR DES  
CARACTERISTIQUES INHERENTES

Patent Applicant/Assignee:

OGILVIE John W L,

Inventor(s):

OGILVIE John W L,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9909512 A1 19990225

Application: WO 98US16877 19980813 (PCT/WO US9816877)

Priority Application: US 9756615 19970820; US 9836621 19980307

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GE GH GM HR HU ID IL IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD

MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ

VN YU ZW GH GM KE LS MW SD SZ UG ZW AM AZ BY KG KZ MD RU TJ TM AT BE CH

CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI CM GA GN GW

ML MR NE SN TD TG

Main International Patent Class: G06K-009/00

Publication Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 12888

English Abstract

Methods and systems are provided for identifying users by testing for predetermined inherent characteristics (202), and for identifying inherent characteristics suitable for use in identifying users (200). Examples include tests for visual pattern recognition tendencies, pattern completion preferences, typing rhythms, and other behavioral characteristics. The identification tests described may be used in place of conventional identification tools such as passwords and magnetic ID cards, or the conventional and novel tests may be used in combination.

French Abstract

L'invention se rapporte a des procedes et systemes permettant, d'une part, d'identifier des utilisateurs en testant des caracteristiques (202) inherentes predeterminees et, d'autre part, d'identifier des caracteristiques inherentes pouvant etre utilisees lors de l'identification d'utilisateurs (200). Par exemple, on trouve des tests concernant les tendances a la reconnaissance de motifs visuels, les preferences relatives au complement de motifs, les rythmes de frappe et d'autres caracteristiques comportementales. Les tests d'identification decrits peuvent etre utilises a la place d'outils d'identification classiques tels que des mots de passe et des cartes d'identification magnetiques, ou bien il est possible d'utiliser conjointement des tests classiques et des tests nouveaux.

Fulltext Availability:

Detailed Description

Detailed Description

... by those of skill in the pertinent art(s) using the teachings presented here and programming languages and tools such as Java. Pascal, C++, C. **database** languages. APIs. SDKs. assembly, **firmware** . microcode. and/or other languages and tools.

8/5,K/2 (Item 2 from file: 348)  
DIALOG(R) File 348:EUROPEAN PATENTS  
(c) 2002 European Patent Office. All rts. reserv.

00774397

**Electronic system, components and method for tracking files**  
**Elektronisches System, Komponenten und Verfahren zur Verfolgung von Akten**  
**Système électronique, composants et méthode de repérage de dossiers**  
PATENT ASSIGNEE:

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PATENT (CC, No, Kind, Date): EP 724227 A2 960731 (Basic)

EP 724227 A3 980527

APPLICATION (CC, No, Date): EP 96300270 960115;

PRIORITY (CC, No, Date): US 379944 950127

DESIGNATED STATES: DE; FR; GB

INTERNATIONAL PATENT CLASS: G06K-017/00;

ABSTRACT EP 724227 A2

A file tracking system includes a processor (20), which maintains and interfaces with a database, a plurality of file folder retainers (50), and a plurality of file folders (70). The file tracking system actively maintains and updates the database, which includes file information, file location, and unique file address for the plurality of files, by providing interactive communication between the processor (20), the folder retainers (50), and the file folders (70). Each file folder (70) may include an indicator light (75), such as an LED, which a user may cause to blink when trying to locating the file folder (70) at a location accessed from the database. (see image in original document)

ABSTRACT WORD COUNT: 128

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 960731 A2 Published application (Alwith Search Report ;A2without Search Report)

Search Report: 980527 A3 Separate publication of the European or International search report

Examination: 990107 A2 Date of filing of request for examination: 981106

Examination: 991229 A2 Date of dispatch of the first examination report: 19991116

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
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CLAIMS A	(English)	EPAB96	1462
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SPEC A	(English)	EPAB96	9896
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Total word count - document A	11358
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Total word count - document B	0
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Total word count - documents A + B	11358
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...SPECIFICATION an interface with the users. Further, various tasks described above as being performed by processor 20 may be allocated to embedded processor 27. For example, **embedded processor** 27 may maintain separate **databases** identifying which file folders are located on their associated branch.

Additionally, other types of annunciators or, as more broadly defined herein, file locating devices, such...



8/5,K/3 (Item 1 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
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00922137 \*\*Image available\*\*

**METHODS AND APPARATUS FOR DEEP EMBEDDED SOFTWARE DEVELOPMENT  
PROCEDES ET DISPOSITIF POUR DEVELOPPEMENT DE LOGICIEL A INTEGRATION  
PROFONDE**

Patent Applicant/Assignee:

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US, US (Residence), US (Nationality), (For all designated states  
except: US)

Patent Applicant/Inventor:

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, (Designated only for: US)

KHOO Kiak Wei, 33 Union Square #226, Union City, CA 94587, US, US  
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Legal Representative:

BEULICK John S (et al) (agent), Armstrong Teasdale LLP, Suite 2600, One  
Metropolitan Square, St. Louis, MO 63102, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200256173 A1 20020718 (WO 0256173)

Application: WO 2002US426 20020109 (PCT/WO US0200426)

Priority Application: US 2001757831 20010110

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU  
CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP  
KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO  
RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG US UZ VN YU ZA ZM ZW  
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR  
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG  
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW  
(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/44

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 6670

English Abstract

One embodiment of the present invention is a method for producing deep  
embedded software (14) suitable for a target processor (22). The method  
includes steps of: authoring a behavioral model from a specification;  
authoring a structural model using the behavioral model; authoring a  
logical model using the structural model; and authoring a physical model  
using the logical model.

French Abstract

Dans un mode de realisation, la presente invention concerne un procede  
destine a produire un logiciel a integration profonde (14) adapte a un  
processeur cible (22). Ce procede consiste a creer un modele  
comportemental a partir d'une specification, a creer un modele structural  
au moyen de ce modele comportemental, a creer un modele logique a l'aide  
du modele structural, puis a creer un modele physique au moyen dudit  
modele logique.

Legal Status (Type, Date, Text)

Publication 20020718 A1 With international search report.

Publication 20020718 A1 Before the expiration of the time limit for  
amending the claims and to be republished in the  
event of the receipt of amendments.

Fulltext Availability:

Detailed Description

Claims

Detailed Description

... macros that perform standard algorithms, such as by matching register numbers, MAC and ALU structures and pointer numbers.

Architecture stencil 92 is a pre-existing **database** containing **embedded microprocessor** or DSP core architecture information of at least one or more target processors. This information can be used to facilitate production of structural model...is also used when required to accurately reflect pipeline effects and latencies.

In one embodiment, a pre-existing database of numeric models 96 contains the **embedded microprocessor** or DSP's numeric characteristics. **Database** 96 is used in at least one embodiment to assist in building logic model 76. For example, in one embodiment, it is used by translator...

...exact verification 86 compares test vector results from physical model 78 to reference results generated from logical model 76.

In one embodiment, a pre-existing **database** of intrinsic functions 102 contains **embedded microprocessor** or DSP's special physical instruction models. Each function corresponds to an instruction in a target processor. In at least one embodiment of the present...

#### Claim

... in accordance with Claim 3 wherein said step of translating the behavioral model into a structural model using an architecturedependent description utilizes a pre-existing **database** containing **embedded microprocessor** or DSP core architecture information.

9 A method in accordance with Claim 3 further comprising the steps of producing test results using the structural model...

8/5,K/4 (Item 2 from file: 349)  
DIALOG(R)File 349:PCT FULLTEXT  
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00859463 \*\*Image available\*\*

**DISTRIBUTED MONITORING SYSTEM PROVIDING KNOWLEDGE SERVICES**  
**SYSTEME DE CONTROLE DISTRIBUE FOURNISSANT DES SERVICES DE CONNAISSANCE**  
Patent Applicant/Inventor:

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JP, JP (Residence), JP (Nationality)

Patent and Priority Information (Country, Number, Date):

Patent: WO 200193096 A2 20011206 (WO 0193096)

Application: WO 2001IB1237 20010530 (PCT/WO IB0101237)

Priority Application: US 2000208394 20000530; US 2000228519 20000828

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU

CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP

KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD

SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-017/30

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 15238

English Abstract

A de-centralized, or distributed, monitoring system provides for data

collection across a broad range of remote sources, collecting explicit data (which may be input directly by a user in the form of recommendation, comment, or vote) and/or implicit data (which may be collected by the system according to the user's browsing activity). Data may be monitored locally at the client side, and subsequently transmitted to a central database. Data may be aggregated at the server, having been collected on the client side from multiple remote sources. During the aggregation process, data collected by the distributed monitoring system are categorized and organized in a central database for convenient retrieval. Implementation of the collected data includes both transmitting explicit data on demand as well as utilizing explicit data, implicit data, or a combination of both explicit and implicit data, in an open recommendation system which facilitates customization and personalization of the information retrieval process. A user may be provided with the option of turning off, or "deselecting," the implicit data collection functionality of the system.

#### French Abstract

L'invention concerne un systeme de controle decentralise ou distribue, recueillant des donnees aupres d'une large gamme de sources distantes, les donnees recueillies etant des donnees explicites (pouvant etre saisies directement par un utilisateur sous forme de recommandation, de commentaire ou de vote), et/ou des donnees implicites (pouvant etre recueillies par le systeme en fonction d'une activite de navigation de l'utilisateur). Les donnees peuvent etre controlees localement cote client, puis transmises a une base de donnees centrale. Les donnees recueillies cote client a partir de plusieurs sources distantes peuvent etre cumulees au niveau du serveur. Durant le processus de cumul, les donnees recueillies par le systeme de controle distribue sont classees et rangees par categories dans une base de donnees centrae pour une extraction plus simple. L'implementation des donnees recueillies consiste a transmettre des donnees explicites sur demande et a utiliser les donnees explicites, les donnees implicites, ou une combinaison des donnees explicites et des donnees implicites, dans un systeme de recommandation ouvert facilitant la personnalisation du processus d'extraction d'informations. L'utilisateur peut eventuellement avoir la possibilite de couper ou de <= deselectionner >= la fonction de recueillement de donnees implicites du systeme.

Legal Status (Type, Date, Text)

Publication 20011206 A2 Without international search report and to be republished upon receipt of that report.

Examination 20020214 Request for preliminary examination prior to end of 19th month from priority date

Fulltext Availability:

Detailed Description

#### Detailed Description

... in order to take advantage of the search engine feature embedded in the browser software. Since the search engine function is included in the browser **embedded software** which accesses the **database** maintained at the server, a user need not navigate to a traditional search engine site before initiating a search.

Enhanced banner advertising functionality is also...

8/5,K/5 (Item 3 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00514130 \*\*Image available\*\*

METHOD, APPARATUS, SYSTEM, AND PROGRAM PRODUCT FOR ATTACHING FILES AND OTHER OBJECTS TO A PARTIALLY REPLICATED DATABASE

METHODE, APPAREIL, SYSTEME ET PRODUIT PROGRAMME PERMETTANT D'ANNEXER DES FICHIERS ET D'AUTRES OBJETS A UNE BASE DE DONNEES PARTIELLEMENT REPRODUITE

Patent Applicant/Assignee:

SIEBEL SYSTEMS INC,  
BRODERSEN Robert A,  
CHATTERJEE Prashant,  
COHEN Jeffrey,  
LIM Peter S,

Inventor(s):

BRODERSEN Robert A,  
CHATTERJEE Prashant,  
COHEN Jeffrey,  
LIM Peter S,

Patent and Priority Information (Country, Number, Date):

Patent: WO 9945482 A1 19990910  
Application: WO 99US4696 19990303 (PCT/WO US9904696)  
Priority Application: US 9876681 19980303

Designated States: AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES

FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU  
LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA  
UG US UZ VN YU ZW GH GM KE LS MW SD SL SZ UG ZW AM AZ BY KG KZ MD RU TJ  
TM AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE BF BJ CF CG CI  
CM GA GN GW ML MR NE SN TD TG

Main International Patent Class: G06F-017/30

Publication Language: English

Fulltext Availability:

Detailed Description  
Claims

Fulltext Word Count: 27913

English Abstract

Attaching files and other objects in a distributed computing environment. This includes adding file attachments and non-database objects, such as, text data, web file data, image file data, and other file attachment objects to databases. These objects may be retrieved at the convenience of a node to which the objects are sent. Visibility rules can be set to determine which attachments and objects are seen by a node. Distribution rules for an object determine whether a node must request the object or whether the node is forced to receive the object.

French Abstract

Cette invention a trait a la mise en annexe de fichiers et d'autres objets dans un environnement DCE. Cette operation consiste a ajouter des annexes de fichier et des objets non-base de donnees, tels que donnees textuelles, donnees de fichier web, donnees de fichiers images et d'autres objets d'annexes de fichier, a des bases de donnees. Ces objets peuvent etre extraits selon les besoins d'un noeud auxquels les objets sont envoyes. Des regles de visibilite peuvent etre etablies afin de determiner quels sont les annexes et les objets vus par un noeud. Des regles de repartition relatives a un objet determinent si un noeud doit demander l'objet ou s'il est force de le recevoir.

Fulltext Availability:

Claims

Claim

... word processing file to a Lotus "Notes" (TM) e-mail note. Smith describes aspects of sharing databases. Hogan describes a database type structure as a **repository** of real time **embedded** system **software** packages. None of the references show the "attachment" of "non-database items" to "

9/5,K/2 (Item 2 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2002 European Patent Office. All rts. reserv.

01430069

Method for mapping information from a network-based data source  
Verfahren für Einteilung von Informationen von einer Netzbasierten  
Datenquelle

Methode pour classer l'information d'un point d'emission de donnees de  
reseau

PATENT ASSIGNEE:

GENERAL ELECTRIC COMPANY, (203903), 1 River Road, Schenectady, NY 12345,  
(US), (Applicant designated States: all)

INVENTOR:

Rosenfeld, John Richard, 5536 Chester Gate Court, Mason, Ohio 45040, (US)

LEGAL REPRESENTATIVE:

Goode, Ian Roy et al (31097), GE LONDON PATENT OPERATION, Essex House,  
12/13 Essex Street, London WC2R 3AA, (GB)

PATENT (CC, No, Kind, Date): EP 1207669 A2 020522 (Basic)

APPLICATION (CC, No, Date): EP 2001309577 011113;

PRIORITY (CC, No, Date): US 713937 001116

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;

LU; MC; NL; PT; SE; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: H04L-029/06

ABSTRACT EP 1207669 A2

The disclosed invention is a method for mapping information using a network-based integration system. The method involves creating (156) custom data logic by mapping information from network-based data source to a client user interface of an internal computer application and then launching (138) a tool capable of reading (144) the network-based data source, copying (154) the information, and transferring (160) the information that was copied from the data source onto a destination client user interface form of an internal system to eliminate the need for manual input.

ABSTRACT WORD COUNT: 88

NOTE:

Figure number on first page: 4

LEGAL STATUS (Type, Pub Date, Kind, Text):

Application: 020522 A2 Published application without search report

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200221	681
SPEC A	(English)	200221	3696
Total word count - document A			4377
Total word count - document B			0
Total word count - documents A + B			4377

...SPECIFICATION 68 for displaying information, a receiving component 70 for receiving a specific query from client system 14, and an accessing component 72 for accessing centralized **database** 60. Receiving **component** 70 is **programmed** to receive a specific query from one of a plurality of users. Server system 12 further includes a processing component 76 for searching and processing...

9/5,K/3 (Item 3 from file: 348)  
DIALOG(R)File 348:EUROPEAN PATENTS  
(c) 2002 European Patent Office. All rts. reserv.

01391763

Systems and methods for managing assets using an interactive database  
Systeme und Verfahren zum Verwalten von Vermögenswerten mit Hilfe einer  
interaktiven Datenbank

Systeme et methode de gestion de valeurs utilisant une base de donnees

**interactive**

**PATENT ASSIGNEE:**

GENERAL ELECTRIC COMPANY, (203903), 1 River Road, Schenectady, NY 12345,  
(US), (Applicant designated States: all)

**INVENTOR:**

Stark, Randal Raymond, 505 Corte Cabas, Morgan Hill, California 94037,  
(US)  
Buckner, Steven Max, 291 Ronan Avenue, Gilroy, California 95020, (US)  
Chan, Ahdee Quan, 18341 Tolusa Court, Morgan Hill, California 95037, (US)  
Ranganath, Sampath, 7173 Queensbridge Way, San Jose, California 95120,  
(US)

**LEGAL REPRESENTATIVE:**

Goode, Ian Roy et al (31097), GE LONDON PATENT OPERATION, Essex House,  
12/13 Essex Street, London WC2R 3AA, (GB)

PATENT (CC, No, Kind, Date): EP 1179798 A2 020213 (Basic)

APPLICATION (CC, No, Date): EP 2001306676 010803;

PRIORITY (CC, No, Date): US 634434 000808

DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;  
LU; MC; NL; PT; SE; TR

EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI

INTERNATIONAL PATENT CLASS: G06F-017/60

**ABSTRACT EP 1179798 A2**

The disclosed invention is a system and a method utilizing a web-based interactive database (20) to automate the process for managing internal components (90,94) of a plant. The system captures all essential information and provides (80,84,88) on-line up-to-date information upon request. The system provides valuable information to a customer to analyze the plant specific problem and offers inspection methods (104), mitigation methods (110) and processes to resolve the problem (118).

ABSTRACT WORD COUNT: 71

**NOTE:**

Figure number on first page: 3

**LEGAL STATUS (Type, Pub Date, Kind, Text):**

Application: 020213 A2 Published application without search report

LANGUAGE (Publication,Procedural,Application): English; English; English

**FULLTEXT AVAILABILITY:**

Available Text	Language	Update	Word Count
CLAIMS A	(English)	200207	682
SPEC A	(English)	200207	6275
Total word count - document A			6957
Total word count - document B			0
Total word count - documents A + B			6957

...SPECIFICATION 68 to display information, a receiving component 70 to receive a specific query from client system 14, and an accessing component 72 to access centralized **database** 20. Receiving **component** 70 is **programmed** for receiving a specific query from one of a plurality of users. Server system 12 further includes a processing component 76 for searching and processing...

**9/5,K/5 (Item 5 from file: 348)**

DIALOG(R)File 348:EUROPEAN PATENTS

(c) 2002 European Patent Office. All rts. reserv.

00760737

**System for controlling the distribution and use of digital works using digital tickets**

**System zur Steuerung der Verteilung und Benutzung von Digitalwerken unter Verwendung von Digitalkarten**

**Systeme pour commander la distribution et l'utilisation d'oeuvres numeriques utilisant des tickets numeriques**

**PATENT ASSIGNEE:**

XEROX CORPORATION, (219783), Xerox Square, Rochester New York 14644, (US)  
, (applicant designated states: DE;FR;GB)

**INVENTOR:**

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(US)

LEGAL REPRESENTATIVE:

Grunecker, Kinkeldey, Stockmair & Schwanhauser Anwaltssozietat (100721)  
, Maximilianstrasse 58, 80538 Munchen, (DE)

PATENT (CC, No, Kind, Date): EP 715247 A1 960605 (Basic)

APPLICATION (CC, No, Date): EP 95308422 951123;

PRIORITY (CC, No, Date): US 344760 941123

DESIGNATED STATES: DE; FR; GB

RELATED DIVISIONAL NUMBER(S) - PN (AN):

(EP 2002022797)

(EP 2002022798)

INTERNATIONAL PATENT CLASS: G06F-001/00;

ABSTRACT EP 715247 A1

A system for controlling the distribution and use of digital works (101) using digital tickets. In the present invention, a "digital ticket" is used to entitle the ticket holder to exercise some usage right (102) with respect to a digital work. Usage rights are used to define how a digital work may be used or distributed. Each usage right may specify a digital ticket which must be present before the right may be exercised. Digital works are stored in repositories which enforce digital works usage rights (105) when usage of a digital work is requested by a requesting repository (103,104). Each repository has a "generic ticket agent" which punches tickets. In some instances only the generic ticket agent is necessary. In other instances, punching by a "special ticket agent" residing on another repository may be needed.  
(see image in original document)

ABSTRACT WORD COUNT: 157

LEGAL STATUS (Type, Pub Date, Kind, Text):

Change: 000503 A1 Legal representative(s) changed 20000315  
Application: 960605 A1 Published application (A1with Search Report  
;A2without Search Report)  
Change: 021204 A1 Application number of divisional application  
(Article 76) changed: 20021016  
Examination: 010523 A1 Date of dispatch of the first examination  
report: 20010404  
Assignee: 010620 A1 Transfer of rights to new applicant:  
ContentGuard Holdings, Inc. (3202250) 103 Foulk  
Road, Suite 205-M Wilmington, Delaware 19803 US  
Examination: 970129 A1 Date of filing of request for examination:  
961205

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	1163
SPEC A	(English)	EPAB96	19446
Total word count - document A			20609
Total word count - document B			0
Total word count - documents A + B			20609

...SPECIFICATION process. Such non-repository archive storage is considered a form of "printing" and is controlled by a print right with a specified "archive-printer." An **archive-printer device** is **programmed** to save the encrypted contents file (but not the description file) offline in such a way that it can be retrieved.

The Restore Transaction

A...

9/5,K/37 (Item 22 from file: 349)

DIALOG(R) File 349:PCT FULLTEXT

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00784123

REMOTE INSTALLATION OF COMPUTER OPERATING SYSTEMS

INSTALLATION A DISTANCE DE SYSTEMES D'EXPLOITATION INFORMATIQUES

Patent Applicant/Assignee:

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designated states except: US)

Patent Applicant/Inventor:

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PLATT Daniel James, Glaxo Wellcome Inc., Five Moore Drive, P.O. Box  
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POWELL Thomas Dwayne, Glaxo Wellcome Inc., Five Moore Drive, P.O. Box  
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Legal Representative:

LEVY David J (agent), Glaxo Wellcome Inc., Five Moore Drive, P.O. Box  
13398, Research Triangle Park, NC 27709-3398 (et al), US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200116701 A2-A3 20010308 (WO 0116701)

Application: WO 2000US23159 20000823 (PCT/WO US0023159)

Priority Application: US 99384485 19990827

Parent Application/Grant:

Related by Continuation to: US Not furnished (CIP)

Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ

DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ

LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG

SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG

(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW

(EA) AM AZ BY KG KZ MD RU TJ TM

Main International Patent Class: G06F-009/445

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description

Claims

Fulltext Word Count: 5614

English Abstract

The invention includes a method of installing a computer operating system from a network to a computer and configuring the computer for use on the network or on another network. An installation routine is run which accepts an input of a build location and a delivery location, interrogates an environment database with the build location and the delivery location to obtain one or more build specific variables and one or more delivery specific variables. The installation routine also determines the computer type and installed hardware components, accesses the network using the build specific variables, and copies a master installation script from an operating system installation source stored on the network. Still further, the installation routine modifies the copied installation script in dependence on the build specific variables, delivery specific variables, detected computer type and hardware components to create a dedicated installation script. The computer operating system is subsequently automatically installed using the dedicated installation script. The installation process may also include the step of registering the computer with the network where it is to be used upon so that it is ready for immediate use at the delivery location.

French Abstract

L'invention concerne un procede d'installation d'un systeme d'exploitation informatique a partir d'un reseau vers un ordinateur et de configuration de l'ordinateur destine a etre utilise dans le reseau ou dans un autre reseau. Un programme d'installation est execute lequel



accepte une entree d'un emplacement de constitution et d'un emplacement de remise, il interroge une base de donnees d'environnement a l'aide de l'emplacement de constitution et de l'emplacement de remise pour obtenir une ou plusieurs variables specifiques a la constitution et une ou plusieurs variables specifiques a la remise. Le programme d'installation determine egalement le type d'ordinateur et les composants materiels installes, il accede au reseau a l'aide des variables specifiques a la constitution, et il copie un script d'installation maitre a partir d'une source d'installation de systeme d'exploitation stockee dans le reseau. De plus, le programme d'installation modifie le script d'installation copie selon les variables specifiques a la constitution, les variables specifiques a la remise, le type d'ordinateur et les composants materiels detectes pour creer un script d'installation specialise. Le systeme d'exploitation informatique est ensuite installe automatiquement a l'aide du script d'installation specialise. Le processus d'installation peut egalement comprendre l'etape d'enregistrement de l'ordinateur dans le reseau dans lequel il est destine a etre utilise de maniere qu'il soit pret a etre utilise immediatement au niveau de l'emplacement de remise.

Legal Status (Type, Date, Text)

Publication 20010308 A2 Without international search report and to be republished upon receipt of that report.  
Examination 20010517 Request for preliminary examination prior to end of 19th month from priority date  
Search Rpt 20010920 Late publication of international search report  
Republication 20010920 A3 With international search report.

Fulltext Availability:  
Detailed Description

Detailed Description

... evaluating the computer's BIOS settings. Preferably, the installation routine evaluates the computer's BIOS settings by comparing the computer's BIOS settings with known BIOS settings. The storage device may hold a database containing the known BIOS settings, each of which BIOS settings being associated with one or more hardware variables, whereby the hardware variables may be used...

9/5,K/41 (Item 26 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

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00568279 \*\*Image available\*\*

**RECONFIGURABLE PROGRAMMABLE LOGIC DEVICE COMPUTER SYSTEM**  
**SYSTEME INFORMATIQUE A LOGIQUE PROGRAMMABLE RECONFIGURABLE**

Patent Applicant/Assignee:

ALTERA CORPORATION, 101 Innovation Drive, San Jose, CA 95134, US, US  
(Residence), US (Nationality)

Inventor(s):

SMITH Stephen J, Altera Corporation, 101 Innovation Drive, San Jose, CA 95134, US,  
SOUTHGATE Timothy J, Altera Corporation, 101 Innovation Drive, San Jose, CA 95134, US,

Legal Representative:

JACKSON Robert R (et al) (agent), Fish & Neave, 1251 Avenue of the Americas, New York, NY 10020, US,

Patent and Priority Information (Country, Number, Date):

Patent: WO 200031652 A2-A3 20000602 (WO 0031652)  
Application: WO 99US27485 19991119 (PCT/WO US9927485)  
Priority Application: US 98109142 19981120

Designated States: JP

(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE

Main International Patent Class: G06F-015/78

International Patent Class: G06F-009/50; G06F-009/44; G06F-017/50

Publication Language: English

Filing Language: English

Fulltext Availability:

Detailed Description  
Claims  
Fulltext Word Count: 7563

#### English Abstract

A reconfigurable computer system based on programmable logic is provided. A system design language may be used to write applications. The applications may be automatically partitioned into software components and programmable logic resource components. A virtual computer operating system may be provided to schedule and allocate system resources. The virtual computer operating system may include a virtual logic manager that may increase the capabilities of programmable logic resources in the system.

#### French Abstract

Cette invention concerne un systeme informatique reconfigurable a base de logique programmable. Un langage de conception de systeme permet d'ecrire des applications. Ces dernieres peuvent se recomposer automatiquement en composantes logicielles et en composantes de ressources logiques programmables. L'invention concerne eventuellement un systeme d'exploitation d'ordinateur virtuel permettant de planifier et d'affecter les ressources systeme. Ce systeme d'exploitation d'ordinateur virtuel peut comprendre un gestionnaire de logique virtuel, permettant d'augmenter les possibilites des ressources logiques programmables du systeme.

#### Legal Status (Type, Date, Text)

Correction 20020425 Corrected version of Pamphlet: pages 1/8-8/8, drawings, replaced by new pages 1/8-8/8; due to late transmittal by the receiving Office  
Search Rpt 20001228 Late publication of international search report  
Republication 20020425 A3 With international search report.

#### Fulltext Availability: Detailed Description

#### Detailed Description

... software methods for writing parameters to hardware functions 80 that are memory-mapped, or this could involve instantiating a hard-disk interface circuit into a **programmable** logic **device** to enable the **archive** of a file system.

It is not necessary to have both software and hardware versions of all of the functions. An application engineer may write...

File 347:JAPIO Oct 1976-2002/Aug(Updated 021203)

(c) 2002 JPO & JAPIO

File 350:Derwent WPIX 1963-2002/UD,UM &UP=200279

(c) 2002 Thomson Derwent

Set	Items	Description
S1	2984	FIRMWARE OR FIRM()WARE
S2	1	S1(5N)(DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S3	1362	EMBEDDED(1W)(CHIP? ? OR MICROCHIP? ? OR PART? ? OR ELEMENT? ? OR MODULE? ? OR HARDWARE OR SOFTWARE OR PROCESSOR? ? OR MICROPROCESSOR? ?)
S4	98481	BIOS OR CMOS OR MICROCODE OR BOOTSTRAP OR PROGRAMMABLE OR - PROGRAMMED
S5	18630	S4(3N)(CHIP? ? OR CHIPSET? ? OR MICROCHIP? ? OR BOARD? ? OR HARDWARE OR COMPONENT? ? OR PART? ? OR ROM? ? OR PROM? ? OR - EPROM? ? OR EEPROM? ? OR SEMICONDUCT??? OR SEMI(W)CONDUCT??? - OR DEVICE? ?)
S6	3	S3(5N)(DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S7	11	S5(3N)(DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S8	6	S7 AND IC=G06F

2/5/1 (Item 1 from file: 347)  
DIALOG(R)File 347:JAPIO  
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02823022 \*\*Image available\*\*  
DATA RETRIEVING SYSTEM

PUB. NO.: 01-120622 [JP 1120622 A]  
PUBLISHED: May 12, 1989 (19890512)  
INVENTOR(s): OKAMOTO TAKASHI  
APPLICANT(s): MITSUBISHI ELECTRIC CORP [000601] (A Japanese Company or Corporation), JP (Japan)  
APPL. NO.: 62-278184 [JP 87278184]  
FILED: November 05, 1987 (19871105)  
INTL CLASS: [4] G06F-007/28  
JAPIO CLASS: 45.1 (INFORMATION PROCESSING -- Arithmetic Sequence Units);  
45.2 (INFORMATION PROCESSING -- Memory Units)  
JOURNAL: Section: P, Section No. 917, Vol. 13, No. 361, Pg. 113,  
August 11, 1989 (19890811)

#### ABSTRACT

PURPOSE: To shorten the data retrieving time by transforming a **data base** control system into **firmware** and at the same time evolving the data base of a disk device onto an image operating memory and retrieving data via this memory.

CONSTITUTION: The data base of a disk device 1 is evolved onto a window memory 10 by a system program. Then a retrieving request 4 containing an interruption given from a user application 3 is sent to a graphic processing **firmware** 11 containing a **data base** control system transformed into **firmware** via an OS 7. Thus the firmware 11 performs the analysis of commands and retrieves the data base evolved onto the memory 10.

6/5/1 (Item 1 from file: 347)  
DIALOG(R)File 347:JAPIO  
(c) 2002 JPO & JAPIO. All rts. reserv.

03256104 \*\*Image available\*\*  
PROGRAM PRODUCING DEVICE

PUB. NO.: 02-231604 [JP 2231604 A]  
PUBLISHED: September 13, 1990 (19900913)  
INVENTOR(s): TANADA SEIICHI  
APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP  
(Japan)  
APPL. NO.: 01-052009 [JP 8952009]  
FILED: March 06, 1989 (19890306)  
INTL CLASS: [5] G05B-015/02; G06F-009/06  
JAPIO CLASS: 22.3 (MACHINERY -- Control & Regulation); 45.1 (INFORMATION  
PROCESSING -- Arithmetic Sequence Units)  
JOURNAL: Section: P, Section No. 1137, Vol. 14, No. 542, Pg. 157,  
November 30, 1990 (19901130)

#### ABSTRACT

PURPOSE: To decrease the number of retrieving circuits for embedded parts by registering previously the embedded parts forming a program of system specifications and producing a program for each embedded parts.  
CONSTITUTION: A function selection processing part 2 sorts the specification data 1 for each function and extracts the function data corresponding to each function out of a function selection knowledge base 3. These extracted function data are supplied to an embedded parts selection part 4 for selection and extraction of the **embedded parts** out of an **embedded parts data base** 5. The extracted **embedded parts** data are written into an embedded parts storage part 6. Then a frame parts selection part 7 receives the embedded parts data and selects the necessary frame parts. Then the part 7 supplies a read request signal to a frame parts data base 8. The select frame parts data are read out of the base 8 and supplied to a program generating part 9. The embedded parts data are read out of the part 6 and undergo a connection processing via the frame parts data. Thus the retrieving frequency is reduced for the storage data.

6/5/2 (Item 1 from file: 350)  
DIALOG(R)File 350:Derwent WPIX  
(c) 2002 Thomson Derwent. All rts. reserv.

014902840 \*\*Image available\*\*  
WPI Acc No: 2002-723546/200278  
XRPX Acc No: N02-570427

#### **Software development and testing method for Real-time Operating Systems has host computer, embedded processor and database with interface descriptions**

Patent Assignee: S2 TECHNOLOGIES INC (STWO-N)  
Inventor: FERTITTA K; HANDA B; HOWELL R; TRAN P; UNDERSETH M; VALOIS H  
Number of Countries: 099 Number of Patents: 001  
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200277813	A2	20021003	WO 2002US8810	A	20020322	200278 B

Priority Applications (No Type Date): US 2002363436 P 20020311; US  
2001278212 P 20010323; US 2001299555 P 20010619

#### Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
WO 200277813	A2	E 104	G06F-009/46	

Designated States (National): AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA  
CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN  
IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ  
OM PH PL PT RO RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA  
ZM ZW

Designated States (Regional): AT BE CH CY DE DK EA ES FI FR GB GH GM GR  
IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TR TZ UG ZM ZW

Abstract (Basic): WO 200277813 A2

NOVELTY - Development and Test (DAT) system (100) automatically formats block of data that is transmitted between embedded computer (104) and host computer (108) and details machine specific enumeration sizes, machine specific pointer sizes, machine specific structure alignment boundaries, machine specific integer sizes, and machine specific byte ordering.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (1) a system for transmitting data in host computer - embedded processor system,
- (2) a method of transmitting data,
- (3) a method of building a database,
- (4) a system for building a database,
- (5) a database,
- (6) a system for providing a communication interface,
- (7) a system for providing communication between a host computer and an embedded computer,
- (8) a system for providing communication with a thread,
- (9) a method of providing communication with a thread,
- (10) a method of providing access to a communication system,
- (11) a system for providing access to a communication,
- (12) an executable communication object,
- (13) a system for testing a thread executing on an embedded computer,
- (14) a method of testing a thread,
- (15) a system for testing a thread,
- (16) a method of initiating an application programming interface,
- (17) a system for initiating an application programming interface,
- (18) a method of generating code,
- (19) a system of generating code.

USE - For developing and testing real-time operating systems (RTOS).

ADVANTAGE - The software development system enables a programmer to simulate and test seamless communication with threads executing on an embedded computer via either a scripting program, any COM-compliant program, or a graphical test utility.

DESCRIPTION OF DRAWING(S) - The block diagram represents a DAT system for testing an embedded computer.

DAT system (100)

Embedded computer (104)

Host computer (108)

pp; 104 DwgNo 1/33

Title Terms: SOFTWARE; DEVELOP; TEST; METHOD; REAL; TIME; OPERATE; SYSTEM; HOST; COMPUTER; EMBED; PROCESSOR; DATABASE; INTERFACE; DESCRIBE

Derwent Class: T01

International Patent Class (Main): G06F-009/46

File Segment: EPI

6/5/3 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011981658 \*\*Image available\*\*

WPI Acc No: 1998-398568/199834

XRPX Acc No: N98-310125

**Repository apparatus for storage and retrieval of real-time embedded software - includes parent repository server storage with search device which responds to client query generator to search of software based on attributes of software**

Patent Assignee: TELOGY NETWORKS INC (TELO-N)

Inventor: HOGAN K; SCHOLL T H; WITOWSKY W E

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5778368	A	19980707	US 96642060	A	19960503	199834 B

Priority Applications (No Type Date): US 96642060 A 19960503

Patent Details:

Patent No	Kind	Lang	Pg	Main IPC	Filing Notes
US 5778368	A		25	G06F-015/76	

*printed*

Abstract (Basic): US 5778368 A

The repository system for storage and retrieval of real-time **embedded software** involves a parent **repository**, with a storage, and a repository client with a storage. A device generates a query to the parent repository server, and is resident on the repository client to search for attributes of real-time embedded software stored on the parent.

Real-time **embedded software** is stored on the parent **repository** server storage. A search device resident on the parent repository server responds to the client query generator to search for the real-time embedded software based on the attributes of the real-time embedded software. A multimedia display displays the results of the client query made to the parent repository server. A communications link joins the repository client and server.

ADVANTAGE - Fully characterises, evaluates, and reuses real-time embedded software that is placed or stored in repository database.

Dwg.10/11

Title Terms: REPOSITORY; APPARATUS; STORAGE; RETRIEVAL; REAL; TIME; EMBED;  
SOFTWARE; PARENT; REPOSITORY; SERVE; STORAGE; SEARCH; DEVICE; RESPOND;  
CLIENT; QUERY; GENERATOR; SEARCH; SOFTWARE; BASED; ATTRIBUTE; SOFTWARE

Derwent Class: T01

International Patent Class (Main): G06F-015/76

File Segment: EPI

8/5/1 (Item 1 from file: 347)

DIALOG(R)File 347:JAPIO

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03315580 \*\*Image available\*\*

METHOD FOR CONVERTING REAL CIRCUIT OF LOGIC CIRCUIT

PUB. NO.: 02-291080 [JP 2291080 A]

PUBLISHED: November 30, 1990 (19901130)

INVENTOR(s): MARUI TOMOTAKA  
ISHIDA YOSHIHIRO  
OKA HIROYUKI

APPLICANT(s): KAWASAKI STEEL CORP [000125] (A Japanese Company or Corporation), JP (Japan)

APPL. NO.: 01-112588 [JP 89112588]

FILED: May 01, 1989 (19890501)

INTL CLASS: [5] G06F-015/60

JAPIO CLASS: 45.4 (INFORMATION PROCESSING -- Computer Applications); 42.2 (ELECTRONICS -- Solid State Components)

JAPIO KEYWORD:R131 (INFORMATION PROCESSING -- Microcomputers & Microprocessors)

JOURNAL: Section: P, Section No. 1167, Vol. 15, No. 67, Pg. 129, February 18, 1991 (19910218)

#### ABSTRACT

PURPOSE: To perform design by utilizing an existing design resource by storing the combination of the real circuit of a designed **programmable logic device** (PLD) in a **data base**, and finding the real circuit corresponding to divided partial circuits by utilizing the data base.

CONSTITUTION: When a designed logic circuit is converted to the real circuit including the PLD, the logic circuit is divided into partial circuits A, B, C, D, and E, and the real circuit Da of the PLD corresponding to the partial circuit is found by utilizing the data base in which a large number of real circuits of the PLD corresponding to a designed partial circuit are stored, and by allocating a found real circuit to, for example, a one-board computer 30, the entire logic circuit can be converted to the real circuit

8/5/2 (Item 1 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014696636 \*\*Image available\*\*

WPI Acc No: 2002-517340/200255

XRPX Acc No: N02-409293

#### Ethernet frame filtering method for computer networks using pre-loaded filter information

Patent Assignee: INT BUSINESS MACHINES CORP (IBM )

Inventor: BROBERG R N C; KUBISTA P B; MILLER G D

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 6381648	B1	20020430	US 99306231	A	19990506	200255 B

Priority Applications (No Type Date): US 99306231 A 19990506

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 6381648	B1	11	G06F-015/16	

Abstract (Basic): US 6381648 B1

NOVELTY - The communication node low level **hardware** is **programmed** with filter **data based** on the MAC address to accept broadcast frames .A high level hardware checks the incoming frame header information against pre-defined comparison data and ignores frames not matching the comparison data.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are included for



- (1) a system for filtering communications data.
- (2) a computer program product for filtering communications data.

USE - For filtering broadcast Ethernet frames in a computer network.

ADVANTAGE - By filtering the Ethernet frame the load on the host processor is reduced as broadcast frame rejection takes place with the hardware level, not the software level.

DESCRIPTION OF DRAWING(S) - The drawing shows a flow diagram of the frame filtering method.

pp; 11 DwgNo 5/5

Title Terms: FRAME; FILTER; METHOD; COMPUTER; NETWORK; PRE; LOAD; FILTER; INFORMATION

Derwent Class: T01; W01

International Patent Class (Main): G06F-015/16

File Segment: EPI

8/5/3 (Item 2 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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014561845 \*\*Image available\*\*

WPI Acc No: 2002-382548/200241

Related WPI Acc No: 2001-024340; 2001-416523; 2001-457133; 2002-009859;

2002-279826; 2002-350700

XRPX Acc No: N02-299464

**Data broadcast method for wireless communication, involves broadcasting extracted data from SQL database to receiving device programmed to reconstruct data into SQL database**

Patent Assignee: RADIOSCAPE LTD (RADI-N)

Inventor: FERRIS G R

Number of Countries: 021 Number of Patents: 002

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 200213058	A2	20020214	WO 2001GB3500	A	20010803	200241 B
GB 2370727	A	20020703	GB 200119017	A	20010803	200251

Priority Applications (No Type Date): GB 200019011 A 20000803

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
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WO 200213058	A2	E	19	G06F-017/30	
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Designated States (National): JP US

Designated States (Regional): AT BE CH CY DE DK ES FI FR GB GR IE IT LU

MC NL PT SE TR

GB 2370727	A			H04Q-003/00	
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Abstract (Basic): WO 200213058 A2

NOVELTY - A data is extracted from a source structured queried language (SQL) database broadcast to a receiving device programmed to reconstruct the data into a SQL database.

DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

- (a) Broadcast data receiving method using wireless communication;
- (b) Data broadcasting apparatus;
- (c) Data receiving device;
- (d) Database application adapted to use broadcast data;
- (e) Database application adapted to use received data;
- (f) Data broadcast program;
- (g) Data receiving program;
- (h) E-commerce transaction system

USE - For broadcast of news, stock quotes, EPGs through wireless communication e.g. Eureka-147 digital audio broadcasting, Internet broadcasting, DVB, ISDB-T, DRM, IBOC, 802.11 Bluetooth, to car and PC receivers.

ADVANTAGE - Provides a scalable approach to the receiver-side implementation, allowing simple receivers to decode individual tables and perform simple select queries, while at the same time facilitates the use of complex execution engines to utilize full power of SQL.

Provides an appropriate platform for implementation of certain key higher-level applications that are primarily driven by structured data such as electronic program guide (EPG).

DESCRIPTION OF DRAWING(S) - The figure shows a schematic illustration of broadcast database (BDB) implementation.

pp; 19 DwgNo 1/1

Title Terms: DATA; BROADCAST; METHOD; WIRELESS; COMMUNICATE; BROADCAST; EXTRACT; DATA; SQL; DATABASE; RECEIVE; DEVICE; PROGRAM; RECONSTRUCT; DATA; SQL; DATABASE

Derwent Class: T01; W02; W03

International Patent Class (Main): G06F-017/30 ; H04Q-003/00

File Segment: EPI

8/5/4 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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012020007 \*\*Image available\*\*

WPI Acc No: 1998-436917/199837

XRPX Acc No: N98-340457

**Program scheduling method for on-board vehicle computer - reprograms software and programmable on-board vehicle computer when deciphered data values match with data values stored in computer**

Patent Assignee: CHRYSLER CORP (CHRY )

Inventor: BERRA C J

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5787367	A	19980728	US 96675754	A	19960703	199837 B

Priority Applications (No Type Date): US 96675754 A 19960703

Patent Details:

Patent No	Kind	Lan Pg	Main IPC	Filing Notes
US 5787367	A		9 H04L-009/32	

Abstract (Basic): US 5787367 A

The system has a flash memory (38) in programmable on-board vehicle computer, in which an identification code, first and second password messages (50,52) are stored respectively. A database containing a programming software provides first and second password messages corresponding to the identification code. An encryption unit is used for encrypting communication data based on series of data values and values provided by second password message.

An interface unit is provided for communicating between the **database** and the **programmable on-board vehicle computers**. A controller within the computer deciphers the received encrypted messages and generates deciphered data values as a function of password message. The re-programming of software in the computer is carried out when the deciphered data values match with the data values stored in the computers.

ADVANTAGE - Ensures security. Allows authorised reprogramming of software.

Dwg.3/5

Title Terms: PROGRAM; SCHEDULE; METHOD; BOARD; VEHICLE; COMPUTER; SOFTWARE; PROGRAM; BOARD; VEHICLE; COMPUTER; DATA; VALUE; MATCH; DATA; VALUE; STORAGE; COMPUTER

Derwent Class: T01; W01

International Patent Class (Main): H04L-009/32

International Patent Class (Additional): G06F-012/14

File Segment: EPI

8/5/5 (Item 4 from file: 350)

DIALOG(R)File 350:Derwent WPIX

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011439619 \*\*Image available\*\*

WPI Acc No: 1997-417526/199739

XRFX Acc No: N97-347702

**Multimedia presentation equipment for information on public transport - decodes and reproduces or stores video and audio information for passengers in accordance with priorities and requirements indicated by code**

Patent Assignee: INFOSCREEN GES STADTINFORMATIONSANLAGEN (INFO-N)

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
DE 29708460	U1	19970821	DE 97U2008460	U	19970513	199739 B

Priority Applications (No Type Date): DE 97U2008460 U 19970513

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
DE 29708460	U1		13		

Abstract (Basic): DE 29708460 U

The mobile equipment is connected to a display screen (10) in the passenger accommodation. It is based on a central processor (5) linked to a code recognition **device** (6), a preferably **programmable** sequence **database** (2), a program memory (3) and an image memory (4).

The components are connected in parallel with a video decoder (8) between a transceiver (7) and the display driver (9) which includes an image rate convertor. The code signifies priorities assigned to video programmes stored in the memory, and indicates whether the currently received images and/or audio data are to be reproduced immediately or stored.

ADVANTAGE - Supplies passengers with up-to-date information throughout journey, provides information during any break in data transmission.

Dwg.1/1

Title Terms: PRESENT; EQUIPMENT; INFORMATION; PUBLIC; TRANSPORT; DECODE; REPRODUCE; STORAGE; VIDEO; AUDIO; INFORMATION; PASSENGER; ACCORD; PRIORITY; REQUIRE; INDICATE; CODE

Derwent Class: P85; T01; W04; X22

International Patent Class (Main): G09G-003/00

International Patent Class (Additional): **G06F-017/30**

File Segment: EPI; EngPI

**8/5/6 (Item 5 from file: 350)**

DIALOG(R)File 350:Derwent WPIX

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011247870 \*\*Image available\*\*

WPI Acc No: 1997-225773/199720

XRFX Acc No: N97-186858

**Programming write-only registers using database and pre-processor e.g. for test instrument - storing database for defining hardware registers and bit fields of each hardware register, receiving bit field write commands comprising commands for programming registers and names identifying bit fields in registers**

Patent Assignee: NAT INSTR CORP (NAIN-N)

Inventor: ILIC K

Number of Countries: 001 Number of Patents: 001

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
US 5619702	A	19970408	US 94249171	A	19940525	199720 B
			US 96695968	A	19960813	

Priority Applications (No Type Date): US 94249171 A 19940525; US 96695968 A 19960813

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5619702	A		19	G06F-009/45	Cont of application US 94249171

Abstract (Basic): US 5619702 A

Hardware registers are programmed using a database defining each hardware register and associated bit fields of the registers. The input code includes bit field write (BFW) commands identifying bit fields corresponding values to program into the bit fields and defining an array of software copies of the hardware registers. A preprocessor writes the appropriate code to program the hardware registers. The database provides the name, size and address of each register and the names and sizes of associated bit fields within each register.

The pre-processor generates output source code by replacing the BFW commands with code to manipulate software copies of the hardware registers and to write the software copies to the hardware registers. The output source code identifies each of the affected registers and writing appropriate code to access the register only once per BFW command.

USE/ADVANTAGE - Database definitions and BFW commands allow superior organisation and simplification to reduce programming time, cost and potential for error. E.g for testing electrical machinery.

Dwg.1/3

Title Terms: PROGRAM; WRITING; REGISTER; DATABASE; PRE; PROCESSOR; TEST; INSTRUMENT; STORAGE; DATABASE; DEFINE; HARDWARE; REGISTER; BIT; FIELD; HARDWARE; REGISTER; RECEIVE; BIT; FIELD; WRITING; COMMAND; COMPRISE; COMMAND; PROGRAM; REGISTER; NAME; IDENTIFY; BIT; FIELD; REGISTER

Derwent Class: T01

International Patent Class (Main): G06F-009/45

File Segment: EPI

File 275:Gale Group Computer DB(TM) 1983-2002/Dec 11  
     (c) 2002 The Gale Group  
 File 47:Gale Group Magazine DB(TM) 1959-2002/Dec 06  
     (c) 2002 The Gale group  
 File 621:Gale Group New Prod.Annou.(R) 1985-2002/Dec 10  
     (c) 2002 The Gale Group  
 File 636:Gale Group Newsletter DB(TM) 1987-2002/Dec 11  
     (c) 2002 The Gale Group  
 File 16:Gale Group PROMT(R) 1990-2002/Dec 11  
     (c) 2002 The Gale Group  
 File 160:Gale Group PROMT(R) 1972-1989  
     (c) 1999 The Gale Group  
 File 148:Gale Group Trade & Industry DB 1976-2002/Dec 10  
     (c)2002 The Gale Group  
 File 624:McGraw-Hill Publications 1985-2002/Dec 11  
     (c) 2002 McGraw-Hill Co. Inc  
 File 98:General Sci Abs/Full-Text 1984-2002/Oct  
     (c) 2002 The HW Wilson Co.  
 File 553:Wilson Bus. Abs. FullText 1982-2002/Oct  
     (c) 2002 The HW Wilson Co  
 File 88:Gale Group Business A.R.T.S. 1976-2002/Dec 05  
     (c) 2002 The Gale Group  
 File 15:ABI/Inform(R) 1971-2002/Dec 11  
     (c) 2002 ProQuest Info&Learning  
 File 635:Business Dateline(R) 1985-2002/Dec 11  
     (c) 2002 ProQuest Info&Learning  
 File 9:Business & Industry(R) Jul/1994-2002/Dec 10  
     (c) 2002 Resp. DB Svcs.  
 File 810:Business Wire 1986-1999/Feb 28  
     (c) 1999 Business Wire  
 File 647:CMP Computer Fulltext 1988-2002/Nov W3  
     (c) 2002 CMP Media, LLC  
 File 674:Computer News Fulltext 1989-2002/Dec W2  
     (c) 2002 IDG Communications  
 File 696:DIALOG Telecom. Newsletters 1995-2002/Dec 10  
     (c) 2002 The Dialog Corp.  
 File 369:New Scientist 1994-2002/Dec W1  
     (c) 2002 Reed Business Information Ltd.  
 File 813:PR Newswire 1987-1999/Apr 30  
     (c) 1999 PR Newswire Association Inc  
 File 634:San Jose Mercury Jun 1985-2002/Dec 10  
     (c) 2002 San Jose Mercury News  
 File 370:Science 1996-1999/Jul W3  
     (c) 1999 AAAS  
 File 613:PR Newswire 1999-2002/Dec 11  
     (c) 2002 PR Newswire Association Inc  
 File 610:Business Wire 1999-2002/Dec 11  
     (c) 2002 Business Wire.

Set	Items	Description
S1	42244	FIRMWARE OR FIRM()WARE
S2	117	S1(5N)(DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S3	107124	EMBEDDED(1W)(CHIP? ? OR MICROCHIP? ? OR PART? ? OR ELEMENT? ? OR MODULE? ? OR HARDWARE OR SOFTWARE OR SYSTEM? ? OR PROCESSOR? ? OR MICROPROCESSOR? ?)
S4	490808	BIOS OR CMOS OR MICROCODE OR BOOTSTRAP OR PROGRAMMABLE OR - PROGRAMMED
S5	76652	S4(3N)(CHIP? ? OR CHIPSET? ? OR MICROCHIP? ? OR BOARD? ? OR HARDWARE OR COMPONENT? ? OR PART? ? OR ROM? ? OR PROM? ? OR - EPROM? ? OR EEPROM? ? OR SEMICONDUCT??? OR SEMI(W)CONDUCT??? - OR IC OR ASIC)
S6	61	RD S2 (unique items)
S7	616	S3(3N)(DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S8	266	RD (unique items)
S9	165	S8 NOT PD>19990927
S10	3	(DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARCHIV???-

	) (3N) (EMBEDDED(1W) (CHIP? ? OR MICROCHIP? ? OR HARDWARE))
S11	2 RD (unique items)
S12	31 (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARCHIV???- ) (3N) (EMBEDDED()SOFTWARE)
S13	11 RD (unique items)
S14	68 S5(5N) (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC- HIV???)
S15	45 RD (unique items)

6/9/11 (Item 11 from file: 275)  
DIALOG(R) File 275:Gale Group Computer DB(TM)  
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01454292 SUPPLIER NUMBER: 11400873 (THIS IS THE FULL TEXT)  
**Calculating the real cost of software defects. (technical)**  
Ward, William T.  
Hewlett-Packard Journal, v42, n4, p55(4)  
Oct, 1991  
DOCUMENT TYPE: technical ISSN: 0018-1153 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 2028 LINE COUNT: 00163

ABSTRACT: HP's Waltham Div projects provide examples for creating a model to calculate software defect cost data for firmware and software products, proving there is a strong financial motivation for improving software quality. HP's corporatewide 10x software quality improvement initiative is intended to improve HP's software quality. In addition to the compelling financial motivation behind software improvement is the promise of better customer satisfaction and the potential to place HP in a leadership position in quality software development. A software quality metrics data base applicable to software defect cost calculations collects and stores pertinent data. In the included example, software defect costs are detected, equated and calculated. The results show that there is the potential for losing \$10,500 per defect and \$1,165,00 per project, which can be countered by implementing remedial actions, including CASE tools and methodologies to improve software quality.

TEXT:

Using data from a well-established software metrics database and an industry profit loss model, a method is developed that computes the real cost of dealing with software defects.

In response to the HP corporate-wide 10 x software quality improvement initiative, much attention has been focused on improving the quality of software products developed throughout HR. The motivation for software quality improvement is most often expressed in terms of increased customer satisfaction with higher product quality, or more generally, as a need to position HP as a leader in quality software development.

A more fundamental motivation to support the initiative for higher software quality can be developed when software defect cost data is considered. The data presented in this paper is drawn from an extensive software project database maintained at the HP Waltham Division for product releases over the past five years. When software defect cost calculations are performed on this data, a very compelling bottom line perspective on software quality emerges; software defects are very expensive and early defect prevention and removal techniques can substantially enhance the profit realized on software products. This paper will present a general model that can be used to calculate software defect cost data for any software or firmware product. Data from actual HP Waltham projects will be used to provide examples of software cost calculations.

The Need for Metrics

As an example of the need for substantive software quality cost data, consider the situation a project manager might encounter when attempting to justify the purchase and use of a new software development tool such as a static code analyzer. If the cost of the tool is \$20,000 and if there is reliable data to suggest that the tool will uncover 50/o of the total number of software defects during typical use, is the project manager justified in acquiring and using the tool?

To provide answers to this type of question, it is important to have access to a reliable database of software quality metrics. Such a database is maintained by the software quality engineering group at the clinical systems business unit of HP's Waltham Division. This database has become an essential component of software quality activities at HP Waltham and is invaluable for such tasks as project scheduling, resource planning, project and product quality status reporting, and software defect cost calculations.

In addition to maintaining the metrics database, the software quality engineering group works with R&D in testing and process improvement

activities.

#### Software Quality Metrics Database

Fig. 1 indicates the development phases of a typical software project, with the phases indicated in which metrics are collected and stored into the software quality database. Data is gathered from a variety of sources including software defect logging, product comparison studies, project post-mortem studies, code complexity and size analysis, and project schedule and resource plans. The physical data resides mainly in a standard HP STARS\* database, which has been augmented with additional fields, files, and reporting utilities. All of the products represented in the metrics database are **firmware**-based medical devices such as critical care monitors, arrhythmia analysis computers, and clinical databases.

Figs. 2, 3, and 4 represent various types of useful data that can be extracted from the database. Fig. 2 documents the steps that are typically required to find, fix, and retest a defect discovered by the software quality engineering group during integration and system or acceptance testing. The engineering effort for this activity, which is shown as 20 hours, represents the average effort for finding and fixing one typical software defect. This value has been calculated using hundreds of data points from multiple software projects that have been tracked with the software quality database. Fig. 3 is an example of how an accurate schedule for the integration through the release phases can be developed using historical project data from the database. In this case, it is clear that a stable and linear relationship exists between product code size and resultant calendar time. Finally, Fig. 4 tabulates various software metrics from multiple software projects. This data can be very useful for developing project comparison studies.

The data presented in these figures is a small subset of the data that exists in the database. This specific data has been presented because of its applicability to software defect cost calculations.

#### Looking for Software Defect Costs

Software defect costs can be investigated using a variety of different approaches. For example, costs can be calculated on a prerelease or a postrelease basis, or costs can be determined per defect or per project phase, or costs can be weighted based on code size or programmer productivity. The software defect cost data developed in this paper focuses on the cost per prerelease software defect that is found and fixed during the integration through the release phases of project development. This approach is used because of the abundance of reliable data points available for study and because of the potential utility of the results.

#### The Software Defect Cost Equation

The calculation of prerelease software defect cost proposed here is based on the formula:

$$\text{Software Defect Cost} = \text{Software Defect Rework Cost} + \text{Profit Loss}$$

Software defect rework cost is determined by the amount of effort and expense required to find and fix software defects during the integration through release phases of a software project. Profit loss is the revenue loss that is caused by lower product sales throughout the entire postrelease lifetime of the product. The lower sales factor is caused directly by the lengthy find and fix cycle of prerelease defects that force a schedule slip and result in a loss of market-window opportunity.

Many other factors could probably be used to determine the software defect cost but our data shows that the rework cost and profit loss factors have a major impact on the result and will supply a close first approximation of the final value. Table I lists a set of product and project software factors that will be used to calculate a software defect cost value. All of these factors represent typical values derived from our database.

Table I

#### Typical Values in the Metrics Database

Code size	75 KNCSS
Calendar time for pre-release testing	6 months
Number of prerelease defects found and fixed	110 defects
Prerelease defect density	1.5 defects/KNCSS

#### Software Defect Rework Calculation

This calculation is very simple and is based on data presented in



Figs. 2 and 4 and Table 1. A typical product will have 110 software defects found and fixed during the project test phase. Each of these defects will require 20 engineering hours to find and fix. The total prerelease software rework effort then is:

Software Defect Rework Effort =  $110 \times 20 = 2200$  engineering hours.

To convert this effort value to dollars requires the \$/hour software engineer factor. As a close approximation of an industry standard value, we will use \$75/hour as the standard charge for the services of a software engineer. This includes basic salary + administration overhead of 750/6).

Software Defect Rework Cost =  $2200 \text{ hours} \times \$75/\text{hour} = \$165,000$ .

On a per-defect basis, rework cost can be determined as:

Rework Cost per Software Defect =  $20 \text{ hours} \times \$75/\text{hour} = \$1500$ .

These calculations are useful in highlighting the true waste factor of poor software quality. Each software defect is responsible for \$1500 of unnecessary expense, and for a typical project \$165,000 is required for software rework.

#### Software Defect Profit Loss Calculation

The other major factor contributing to software defect cost is product profit loss because of missed market-window opportunities and the resultant loss of product sales. In other words, if a product release date slips because the software defect find and fix cycle is unnecessarily long, then potential product sales are irretrievably lost and overall lifetime profit dollars will be less. Such factors as rapidly obsolete technology and the availability of competitive products also contribute to the potential loss of sales.

Several industry models 1, 2 have been proposed that can be used to quantify the profit loss factor. Fig. 5 presents one of these models and will serve as the basis for our calculations. For the following calculations we assume a 1000-unit customer base of a \$20,000 product with a 150/o profit margin. This will yield \$3,000,000 in lifetime profit. Assuming a six-month slip in product release because of the software defect find and fix cycle, Fig. 5 suggests a 33% loss in profit.

Profit Loss =  $\$3,000,000 \times 33\% = \$1,000,000$

Using the data on the number of prerelease defects given in Table 1, on a per-defect basis, profit-loss can be determined as:  $\$1,000,000/110$  defects - \$9000 per defect.

It may seem extreme to say that every prerelease defect causes a product to be late to market. However, because of the nature of our business, it is important that our products perform reliably in the critical-care medical environment. This means that each defect of a high enough severity level that is found during prerelease tests must be fixed and retested before final release. It is this test, fix, and retest cycle that delays product release and contributes to the cost of poor software quality.

#### The \$1,000,000 Opportunity

Fig. 6 summarizes the software defect cost data calculated in this paper. The variables used in these calculations will vary from one organization to another, but the fundamental algorithm for computing software defect cost is applicable to most cases. Although the product cost and profit margin numbers used here are for illustrative purposes, they are typical for large software systems. Therefore, with the potential for a cost of 10,500 per defect and \$1,165,000 per project, there is ample financial basis for a number of potential remedial actions.

Quality Awareness. Most software engineers probably have no idea about the cost of reworking software to find and fix a defect once the code enters the integration and test phases. They should be made aware of the savings possible if more defect detection could be done in the early stages of product development.

CASE Investment. There are a large number of CASE tools and methodologies available to augment the software development process. Examples of modern CASE technology include static code analyzers, debuggers, execution profilers, formal inspections of design and code, structured analysis and design, and so on. Most of these technologies can be acquired for a financial investment of \$10,000 to \$30,000. If each software defect has a \$10,500 cost, then it is clearly appropriate to consider the use of CASE to improve software quality. Software 10 x Program: When it becomes clear that software quality improvements can yield substantial financial rewards, then the goal of a 10 x gain in software quality assumes

additional impetus. Consider that a 10 x improvement of the number of prerelease software defects for the typical software project presented in this paper would yield almost an additional \$1,000,000 in profit. That figure is a powerful bottom line motivator.

#### Conclusion

This paper has presented a technique that can be used to calculate software defect cost values. Historical HP Waltham software quality and project data has been applied to cost calculations so that realistic results might be obtained. Although additional investigations, such as a determination of postrelease software defect cost, might provide a more detailed analysis of cost, the data presented in this paper is accurate and provides compelling financial motivation for improved software quality.

#### References

1. B.C. Cole, "Getting To The Market On Time." Electronics, Vol. 62, no. 4, April 1989, pp. 62-67.

2. D. G. Reinertsen, "Whodunit? The Search for the New-product Killers." McKinsey and Company Report, 1983, pp. 35-37.

CAPTIONS: Software defect find and fix cycle. (chart); Typical software metrics for projects in the software quality database. (table); Percentage of profit loss associated with product release problems. (graph)

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SPECIAL FEATURES: illustration; chart; table; graph

DESCRIPTORS: Software Quality; Software Design; Defects; Performance/Cost Relationship; Models of Computation; Firmware; Measurement; Software Validation

SIC CODES: 3571 Electronic computers; 3825 Instruments to measure electricity

FILE SEGMENT: CD File 275

6/9/21 (Item 6 from file: 621)

DIALOG(R)File 621:Gale Group New Prod.Annou.(R)

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01672296 Supplier Number: 50145000 (THIS IS THE FULLTEXT)

**Lexmark's New, Easy-To-Use Print Server Pulls Jobs Off the Network Quickly With Fast Ethernet.**

Business Wire, p07061255

July 6, 1998

Language: English Record Type: Fulltext

Article Type: Article

Document Type: Newswire; Trade

Word Count: 715

TEXT:

LEXINGTON, Ky.--(BUSINESS WIRE)--July 6, 1998--

High-performance MarkNet Pro 1 print server can be configured with a web browser and has custom web links --

To give customers a super-fast external adapter that reduces network traffic and speeds printer throughput, Lexmark International, Inc. (NYSE:LXK) today announced the MarkNet(TM) Pro 1 10/100BaseTX print server. The adapter delivers performance up to two times faster than the leading competitor.

Customers can use Lexmark's external print servers to attach virtually any printer to all major network operating systems. The dual-speed MarkNet Pro 1 10/100BaseTX adapter helps customers reduce their total information systems costs by protecting their printer investment. Customers can use the MarkNet Pro 1 10/100BaseTX for connectivity to 10BaseT today and 100BaseTX in the future.

"Lexmark customers consistently recognize value and performance," said Glenn Hudson, vice president and general manager of attachment products atLexmark. "This product gives maximum flexibility for attaching printers to a network and adds a rich set of features to make configuration, updating and maintenance easy from the desktop or workstation."

This new print server incorporates a fast 32Bit PCI local bus, optimized firmware and a powerful Hitachi SH2 27 MHz RISC processor to help empty print queues faster, reducing the wait for print jobs.

The new MarkNet Pro 1 10/100BaseTX is equipped with an ECP IEEE 1284

compliant bi-directional parallel port delivering 800Kbps and a twisted pair (RJ-45) connection delivering today's 10Mbps and tomorrow's 100Mbps speeds. Lexmark's family of print servers are Web-ready and can be configured through your favorite browser.

Embedded links to technical support, printer driver and **firmware** update **databases** and online product registration on the Lexmark Web Site make maintenance easy. The links can be customized to point to customers or reseller intranet sites or online purchasing systems. The MarkNet Pro home page also includes a Java applet that displays status information for the last four jobs printed when an Optra(TM) printer is attached.

#### Key features

The MarkNet Pro print servers include many features that make them easy to setup, configure and manage including:

-- Automatic speed detection and media detection eliminate the need to set these parameters during installation -- Automatic assignment of IP addresses and intuitive naming of printers with DHCP and WINS/DNS support to make setup and operation easier -- Testing with a wide variety of dot matrix, inkjet and laser printers to ensure compatibility -- Simultaneous support for all major network protocols - TCP/IP, IPX, DLC/LLC, EtherTalk and TokenTalk -- Flash memory firmware for quick and easy upgrades -- MarkVision(TM), a powerful tool with an intuitive, graphical user interface for setup and configuration of the MarkNet Pro print servers and a host of useful printer management features for network administrators when used with an Optra laser printer

#### Pricing and availability

The MarkNet Pro 1 10/100BaseTX Ethernet print server has an estimated street price of \$225 (U.S.). The new MarkNet Pro 1 model is available now through Lexmark authorized resellers and partners.

For more information about Lexmark products visit the Lexmark home page at [www.lexmark.com](http://www.lexmark.com) on the Internet. Customers may also call 1-800-LEXMARK (1-800-539-6275), use the Lexmark bulletin board service at (606) 232-5238 or the CompuServe forum "Go Lexmark."

Lexmark International, Inc. is a global developer, manufacturer and supplier of printer solutions and products, including laser, inkjet and dot matrix printers and associated consumable supplies for the office and home markets. The company is a wholly owned subsidiary of Lexmark International Group, Inc. (NYSE: LXX). Lexmark, which had sales of \$2.5 billion in 1997, has executive offices and a manufacturing center in Lexington, Ky.; other manufacturing centers are in Boulder, Colo.; Juarez, Mexico; Rosyth, Scotland; Orleans, France; and Sydney, Australia. For more information on Lexmark products, including easy-to-use instructions on choosing the right printer, visit the Lexmark home page at [www.lexmark.com](http://www.lexmark.com) on the Internet. Customers may also call 1-800-LEXMARK (1-800-539-6275), use the Lexmark bulletin board service at (606) 232-5238 or the CompuServe forum "Go Lexmark."

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Street prices are estimates; actual prices may vary. Lexmark, MarkVision, Optra and MarkNet are trademarks of Lexmark International, Inc., registered in the U.S. and/or other countries. All other trademarks are the property of their respective holders.

CONTACT: Lexmark International Inc., Lexington  
Media Contact: Chris Snapp, 606/232-5484,  
[csnapp@lexmark.com](mailto:csnapp@lexmark.com)

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INDUSTRY NAMES: BUS (Business, General); BUSN (Any type of business)

NAICS CODES: 334111 (Electronic Computer Manufacturing)

09751854 SUPPLIER NUMBER: 19792572 (THIS IS THE FULL TEXT)  
**Lexmark's New, easy-to-use Print Servers Help customers print much faster  
than the leading competitors' products.**  
Business Wire, p9291230  
Sep 29, 1997  
LANGUAGE: English RECORD TYPE: Fulltext  
WORD COUNT: 661 LINE COUNT: 00061

TEXT:

LEXINGTON, Ky.--(BUSINESS WIRE)--Sept. 29, 1997--

High-performance MarkNet Pro print servers are also Web-ready with customizable links to support information --

Lexmark International, Inc. today announces four new MarkNet (TM) Pro print servers that deliver performance up to three times faster than the top two competitors' print servers. These new print servers incorporate a fast PCI interface, optimized firmware and a powerful Hitachi SH2 27 MHz RISC processor to help empty print queues faster, reducing waiting for print jobs.

The MarkNet Pro print servers are Web-ready with embedded links to technical support, printer driver and **firmware** update **databases** and on-line product registration on the Lexmark(TM) Web site. The links can be customized to point to customers' or resellers' intranet information databases or on-line purchasing systems. The MarkNet Pro home page also includes a Java applet that displays status information for the last four jobs printed when an Optra(TM) printer is attached.

The MarkNet Pro 3 print servers can be combined with a low-cost modem to print incoming faxes on any PostScript Level 2 compatible printer. The incoming fax information can also be displayed in the Java applet status information and can be tracked in MarkVision(TM) using the job statistics feature.

The MarkNet Pro print servers include many features that make them easy to setup, configure and manage including:

- Automatic speed detection and media detection eliminate the need to set these parameters during installation
- Automatic assignment of IP addresses and intuitive naming of printers with DHCP and WINS/DNS support to make setup and operation easier
- Testing with a wide variety of dot matrix, inkjet and laser printers to ensure compatibility
- Simultaneous support for all major network protocols - TCP/IP, IPX, DLC/LLC, EtherTalk and TokenTalk
- Flash memory firmware for quick and easy upgrades
- MarkVision, a powerful tool with an intuitive, graphical user interface for setup and configuration of the MarkNet Pro print servers and a host of useful printer management features for network administrators when used with an Optra laser printer

The single-port MarkNet Pro 1 print servers have one ECP IEEE 1284 compliant parallel port and are available in 10BaseT Ethernet or 10Base2/10BaseT Ethernet models. The three-port MarkNet Pro 3 print servers have two ECP IEEE 1284 compliant parallel ports and one serial port and are available in 10/100BaseTX Ethernet or 4Mb/16Mb Token-Ring models.

Pricing and availability

The MarkNet Pro 1 10BaseT Ethernet and 10Base2/10BaseT Ethernet print servers have street prices of \$225 and \$250 (U.S.) (a), respectively. The MarkNet Pro 3 10/100BaseTX Ethernet and 4 Mb/16Mb Token-Ring print servers have street prices of \$325 and \$500 (U.S.) (a), respectively. The new MarkNet Pro models are available now through Lexmark authorized resellers and partners. For more information about Lexmark products visit the Lexmark home page at [www.lexmark.com](http://www.lexmark.com) on the Internet. Customers may also call 1-800-LEXMARK (1-800-539-6275), use the Lexmark bulletin board service at (606) 232-5238 or the CompuServe forum "Go Lexmark."

Lexmark International, Inc., is a global developer, manufacturer and supplier of printing solutions and products, including laser, inkjet and dot matrix printers and associated consumable supplies for the office and home markets. The company is a wholly owned subsidiary of Lexmark International Group, Inc. (NYSE: LXX). Lexmark, which had sales of \$2.4 billion in 1996, has executive offices and its largest manufacturing center

in Lexington, Ky.; other manufacturing centers are in Boulder, Colo.;  
Juarez, Mexico; Rosyth, Scotland; Orleans, France and Sydney, Australia.  
-0-

(a) Street prices are estimates; actual prices may vary.

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other trademarks are the property of their respective holders.

6/3,K/1 (Item 1 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02269960 SUPPLIER NUMBER: 53877380 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Personal Computers and the Year 2000. (Industry Trend or Event)**

Lips, Michael D.

Enterprise Systems Journal, 14, 2, 50(1)

Feb, 1999

ISSN: 1053-6566 LANGUAGE: English RECORD TYPE: Fulltext; Abstract

WORD COUNT: 4092 LINE COUNT: 00309

... on the shelf for months. Another serious problem is that a lot of old PCs were recycled rather than scrapped or given away.

Spreadsheet and Database Software. Exacerbating the hardware and firmware problems, are those related to faulty applications software. When spreadsheets and databases are involved, the risk may depend on the usage of the date.

In...

6/3,K/2 (Item 2 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02254095 SUPPLIER NUMBER: 53422423 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Advanced tool package administers complete physical to PC. (Software Review) (Evaluation)**

BYRNE, JASON

Government Computer News, 17, 40, 29(1)

Dec 14, 1998

DOCUMENT TYPE: Evaluation ISSN: 0738-4300 LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 655 LINE COUNT: 00054

... in some of the GCN Lab's older, more troublesome machines and found that, after identifying a component's manufacturer, I could access the industry database stored in the card's firmware to obtain technical support numbers and Web sites. This is great help for technicians who work in the field and don't have vendor contact...

6/3,K/3 (Item 3 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02141821 SUPPLIER NUMBER: 20163882 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Upwardly mobile. (notebook computers) (includes related article on the Psion 5) (Buyers Guide)**

Shim, Richard S.

Computer Shopper, v17, nguide, p164(4)

Dec 15, 1997

DOCUMENT TYPE: Buyers Guide ISSN: 0886-0556 LANGUAGE: English

RECORD TYPE: Fulltext; Abstract

WORD COUNT: 2832 LINE COUNT: 00225

... life, at about \$500. The new Psion Series 5 (about \$700), meanwhile, is perhaps the most elegant PDA, combining powerful word processing, spreadsheet, scheduling, and database firmware with a surprisingly roomy slide-out keyboard. Its Win 95 desktop link is slick, but the British import requires an optional modem or PC Card...

6/3,K/4 (Item 4 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02108210 SUPPLIER NUMBER: 19844607 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Editorial - Year 2000 Compliance: Tactics Or Strategy?**

Newsbytes, pNEW09110035

Sep 11, 1997

LANGUAGE: English      RECORD TYPE: Fulltext

WORD COUNT: 752      LINE COUNT: 00063

... understand that years can and do include values of 2000 and above.

However, the magnitude of the challenge is extreme because applications encompass almost everything: **databases**, networks, software, hardware, **firmware**. Compliance concerns extend into our everyday lives. From the computers in our cars to the BIOS in our PCs.

For reasons that extend beyond the...

**6/3,K/5      (Item 5 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02068471      SUPPLIER NUMBER: 19342487      (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Server management. (five network management products) (Product Information)**

Network VAR, v5, n4, p60(2)

April, 1997

ISSN: 1082-8818      LANGUAGE: English      RECORD TYPE: Fulltext; Abstract

WORD COUNT: 1684      LINE COUNT: 00142

... are being utilized. The product is based on SNMP, providing integration with other management products. Insight Version Control allows administrators to monitor versions of server **firmware**, drivers, and utilities. **Database** Export allows users to export an Insight Manager database to their preferred database or spreadsheet. The Compaq Remote Insight board and ProLiant Integrated Remote Console...

**6/3,K/6      (Item 6 from file: 275)**

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01925238      SUPPLIER NUMBER: 18182301      (USE FORMAT 7 OR 9 FOR FULL TEXT)

**HP, Informix, GemPlus join to promote smart cards. (Company Business and Marketing) (Brief Article)**

Nicolaisen, Nancy

Computer Shopper, v16, n5, p93(1)

May, 1996

DOCUMENT TYPE: Brief Article      ISSN: 0886-0556      LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 299      LINE COUNT: 00028

... in the card will provide access and record-keeping for users' credit-card, banking, and other accounts.

As transactions are made, the smart card's **database firmware** --Informix's part of the package--will automatically update records in the appropriate cell. The alliance's long-term plans include making smart-card readers...

**6/3,K/7      (Item 7 from file: 275)**

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01720922      SUPPLIER NUMBER: 15944371      (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Sub-subnotebook search; memo to PC makers: let's get small. (Working On It) (Column)**

Grevstad, Eric

Computer Shopper, v15, n1, p53(2)

Jan, 1995

DOCUMENT TYPE: Column      ISSN: 0886-0556      LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 909      LINE COUNT: 00065

...ABSTRACT: a system that has a very comfortable or easy-to-use keyboard. Sharp Electronics' Wizard PDA offers users a very good touch-screen interface, good **database firmware**, scheduling and a fax/modem, but the keyboard is so small it is barely operable. Psion Inc's Series 3a PDA is a more expensive...

... Twinkie.

My latest near-miss was the newest Sharp Wizard. The Wizard OZ-9520FX has an elegant touch-screen interface, decent scheduling and nicely customizable **database firmware**, and a fax/modem the size of a headless Pez dispenser. With the modem clipped to its side, the Wizard fits perfectly into your jacket...

6/3,K/8 (Item 8 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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01710846 SUPPLIER NUMBER: 16182379 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Virtual remote: the centralized expert. (monitoring remote sites from a central office)**

Butler, Hamish

Hewlett-Packard Journal, v45, n5, p75(8)

Oct, 1994

ISSN: 0018-1153 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 4648 LINE COUNT: 00376

... using the ISS language

- \* Simulate the instrument operation using the ISS simulator
- \* Use the ISS compiler to generate an embedded instrument database
- \* Compile the instrument **database** into embedded instrument code
- \* Test **firmware** operation in the target instrument.

The ISS simulator and the target instrument share the core components, mainly the ISS database processing engine. The development process...

6/3,K/9 (Item 9 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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01601498 SUPPLIER NUMBER: 13924545 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Letters. (Letter to the Editor)**

Kaminer, Evan M.; Allen, Jack; Dean, Malcolm; Brodie, Michael; Fleming, Mark; Perrell, David; Simmons, Michael; Seabourn, Roy; Silbiger, Herman R.; Uptain, Allyn; Waters, Geoff; Nelson, A. James; Matthews, Michael J., Jr.  
PC Magazine, v12, n12, p19(2)

June 29, 1993

DOCUMENT TYPE: Letter to the Editor ISSN: 0888-8507 LANGUAGE:

ENGLISH RECORD TYPE: FULLTEXT

WORD COUNT: 1475 LINE COUNT: 00114

... most profitable product line, generates revenue for IBM that exceeds Digital Equipment Corp.'s entire corporate revenue, and has such advanced features as a relational **database** system implemented in **firmware**.

Some slug. Some joke.

Michael Brodie

Port Credit, Ontario, Canada

The only statement Mr. Dvorak makes in his April 13 Inside Track that demonstrates the...

6/3,K/10 (Item 10 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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01474424 SUPPLIER NUMBER: 13314435



**CrossComm feature lets routers handle inventory. (Network Inventory feature takes advantage of ILAN Universal Router Architecture)**

MacAskill, Skip

Network World, v9, n49, p17(2)

Dec 7, 1992

ISSN: 0887-7661

LANGUAGE: ENGLISH

RECORD TYPE: ABSTRACT

...ABSTRACT: compiling information on remote network devices. The software utilizes the ILAN's Universal Router Architecture (URA) to maintain a high-speed Address Processor and Directory **database**, a hardware/ **firmware** combination that stores information on up to 64,000 devices. Network managers can use a centrally located workstation running IMS 5.1 to obtain information...

**6/3,K/11 (Item 11 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)

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01454292 SUPPLIER NUMBER: 11400873 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Calculating the real cost of software defects. (technical)**

Ward, William T.

Hewlett-Packard Journal, v42, n4, p55(4)

Oct, 1991

DOCUMENT TYPE: technical

ISSN: 0018-1153

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 2028 LINE COUNT: 00163

... in a standard HP STARS\* database, which has been augmented with additional fields, files, and reporting utilities. All of the products represented in the metrics **database** are **firmware**-based medical devices such as critical care monitors, arrhythmia analysis computers, and clinical databases.

Figs, 2, 3, and 4 represent various types of useful data...

**6/3,K/12 (Item 12 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)

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01315952 SUPPLIER NUMBER: 07577964 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Use of structured methods for real-time peripheral firmware.**

Bartlett, Paul F.; Robinson, Paul F.; Hains, Tracey A.; Simms, Mark J.

Hewlett-Packard Journal, v40, n4, p79(8)

August, 1989

ISSN: 0018-1153

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 3481 LINE COUNT: 00273

... actually contains 24 control flows and 46 data flows between the processes. HP Teamwork/SA was used to create and maintain a central data dictionary **data base** for the whole **firmware** system. A data dictionary is a method for defining every data flow, control flow, and data store used in a system. The central data base...

**6/3,K/13 (Item 13 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)

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01044944 SUPPLIER NUMBER: 00662785

**Software Escrow and the Security Practitioner.**

Choney, Liliane

Computer Security Journal, v3, n1, p67-76

Summer, 1984

ISSN: 0277-0865

LANGUAGE: ENGLISH

RECORD TYPE: ABSTRACT

...ABSTRACT: contract security to both the software developer and the purchaser, software escrow can establish a third-party agent to preserve

the source code, development tools, **database**, **firmware** and other materials. The form, content and stability of the software must be addressed in escrow provisions, and commitments must be made by both parties...

6/3,K/14 (Item 1 from file: 47)  
DIALOG(R)File 47:Gale Group Magazine DB(TM)  
(c) 2002 The Gale group. All rts. reserv.

03509201 SUPPLIER NUMBER: 11230362  
**Operating system basics. (overview of hardware, software, firmware , database management systems, standards, etc.) (Operating Systems for Automated Library Systems)**  
Boss, Richard W.; Casey, Mary H.  
Library Technology Reports, v27, n2, p129(20)  
March-April, 1991  
CODEN: LTRPA ISSN: 0024-2586 LANGUAGE: ENGLISH RECORD TYPE:  
CITATION

**Operating system basics. (overview of hardware, software, firmware , database management systems, standards, etc.) (Operating Systems for Automated Library Systems)**

6/3,K/15 (Item 2 from file: 47)  
DIALOG(R)File 47:Gale Group Magazine DB(TM)  
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03160178 SUPPLIER NUMBER: 07124635 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Database Machines: the least-cost route?**  
Gregory, Ed  
Datamation, v34, n21, p85(3)  
Nov 1, 1988  
ISSN: 1062-8363 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 2069 LINE COUNT: 00163

... at the University of Minnesota Medical School.  
Britton Lee Inc., Los Gatos, Calif., and Teradata Inc., Los Angeles, are the two major suppliers of these **database** machines, the hardware-**firmware** -software successors to resource-hungry general purpose computer databases. Budd recalls being impressed by a benchmark that showed the Britton Lee would perform three-and...

6/3,K/16 (Item 1 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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03309109 Supplier Number: 94202327 (USE FORMAT 7 FOR FULLTEXT)  
**Sea View Technologies Acquires www.56k.Com.**  
PR Newswire, pNEW02513112002  
Nov 13, 2002  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 313

Computer enthusiasts to engineers have relied on <http://www.56k.com/> for several years as a **repository** for detailed information, data sheets, **firmware** downloads, upgrades and buyer's guide for modems. The success of the site as a meeting place for those seeking technical advice attracted the attention...

6/3,K/17 (Item 2 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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03247937 Supplier Number: 90825787 (USE FORMAT 7 FOR FULLTEXT)  
**Diamond IP Technologies Announces ImageControl(TM) For Cable Version 2.0.**  
PR Newswire, pPHTH03215082002  
August 15, 2002  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 432

... ModelMatching(TM) technology, ImageControl can automatically discover, categorize and inventory all the DOCSIS devices on an MSO's network. They are then stored in a **database** for reporting and **firmware** update scheduling, which can be performed on-demand or scheduled in the future. This provides complete flexibility to MSOs. Firmware binaries are also registered and...

6/3,K/18 (Item 3 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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02751331 Supplier Number: 67681094 (USE FORMAT 7 FOR FULLTEXT)  
**Extended Systems Names Andrew Terry Chief Technology Officer.**  
Business Wire, p0094  
Dec 7, 2000  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 467

... engineering graduate, Terry joined Extended Systems through the acquisition of Advanced Systems. He has focused on a wide range of software technologies, including real-time **firmware**, optimization techniques and relational **databases**, throughout his career. In recent years, he has focused exclusively on software solutions for the mobile computing segment. In his role as Chief Technology Officer...

6/3,K/19 (Item 4 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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02687466 Supplier Number: 66165810 (USE FORMAT 7 FOR FULLTEXT)  
**McDATA Launches First Enterprise-Level SAN Management Software.**  
PR Newswire, pNA  
April 25, 2000  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 871

... NT operating system. The EFC Server supports up to four concurrent remote workstation users and one dial-in connection. It centralizes log files, and configures **databases** and non-disruptive **firmware** distribution while centralizing call home, e-mail and support facilities. Java-based EFC Manager gives administrators a single interface and management tool for multiple McDATA...

6/3,K/20 (Item 5 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2002 The Gale Group. All rts. reserv.

01819409 Supplier Number: 53991323 (USE FORMAT 7 FOR FULLTEXT)  
**MediaCom Makes Industry Leading Least Cost Call Routing Technology Available On an OEM Basis.**  
Business Wire, pl534  
March 2, 1999  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 659

... Information Appliance technology, for OEMs of telephony and consumer electronics products.

At CT Expo in Los Angeles, MediaCom introduced its newest generation of application software, **firmware**, custom chipsets and **database** updating technologies that enable product vendors to easily add high-value, least cost routing capabilities to their products. MediaCom's telephony products include the Intelligent...

6/3,K/21 (Item 6 from file: 621)

DIALOG(R)File 621:Gale Group New Prod.Annou.(R)

(c) 2002 The Gale Group. All rts. reserv.

01672296 Supplier Number: 50145000 (USE FORMAT 7 FOR FULLTEXT)

**Lexmark's New, Easy-To-Use Print Server Pulls Jobs Off the Network Quickly With Fast Ethernet.**

Business Wire, p07061255

July 6, 1998

Language: English Record Type: Fulltext

Article Type: Article

Document Type: Newswire; Trade

Word Count: 715

... Lexmark's family of print servers are Web-ready and can be configured through your favorite browser.

Embedded links to technical support, printer driver and **firmware** update **databases** and online product registration on the Lexmark Web Site make maintenance easy. The links can be customized to point to customers or reseller intranet sites...

6/3,K/22 (Item 7 from file: 621)

DIALOG(R)File 621:Gale Group New Prod.Annou.(R)

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01565433 Supplier Number: 47934143 (USE FORMAT 7 FOR FULLTEXT)

**HP and Database Excelleration Systems Deliver Industry-Leading I/O**

**Performance on HP 9000 T600; HP and DES Combine Technologies to**

**Demonstrate Industry's Best I/O Throughput Performance.**

Business Wire, p08260214

August 26, 1997

Language: English Record Type: Fulltext

Document Type: Newswire; Trade

Word Count: 851

... solutions for high-availability, secured-Web-server, distributed-computing, server-consolidation and mainframe-alternative applications.

By combining solid-state memory enhanced with DES-developed DirectAddressing **firmware**, the DES **Database** Excellerator dramatically increases the performance of relational database (RDBMS) applications. By moving key RDBMS "hot files," temporary work areas, transaction logs or indices onto a...

6/3,K/23 (Item 8 from file: 621)

DIALOG(R)File 621:Gale Group New Prod.Annou.(R)

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01433375 Supplier Number: 46758880 (USE FORMAT 7 FOR FULLTEXT)

**Database Excelleration Systems and Kenan Systems Achieve Mainframe**

**Performance on Client/Server Systems with France Telecom Benchmark;**

**Companies Agree to Cooperatively Market Client/Server Billing Systems**

**Solutions to the Telecommunications Industry.**

Business Wire, p10011031

Oct 1, 1996

Language: English Record Type: Fulltext

Document Type: Newswire; Trade  
Word Count: 852

... acceleration products and professional services for very high transaction and query-intensive client/server applications. DES products combine solid state memory and advanced DES DirectAddressing **firmware** to dramatically accelerate **database** application performance ranges of Unix or NT servers. The company also provides professional diagnostic and evaluation services to assist users in optimizing Unix and NT...

6/3,K/24 (Item 9 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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01391399 Supplier Number: 46447984 (USE FORMAT 7 FOR FULLTEXT)  
**Database Excelleration Systems Receives Notice of Allowance of Patent Claims from U.S. Patent and Trademark Office.**  
Business Wire, p06071259  
June 7, 1996  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 251

... patent will augment DES' existing portfolio of patents directed to solid state devices.

DES products combine solid state memory and advanced DES Direct Addressing(tm) **firmware** to greatly accelerate **database** application performance. The company also provides professional diagnostic and evaluation services to assist users in optimizing UNIX and NT-based applications that are experiencing resource...

6/3,K/25 (Item 10 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2002 The Gale Group. All rts. reserv.

01161049 Supplier Number: 42024985 (USE FORMAT 7 FOR FULLTEXT)  
**HONEYWELL AND MEASUREX JOINTLY DEVELOP INTEGRATION BETWEEN SYSTEMS**  
News Release, p1  
April 23, 1991  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 567

... productivity  
\* Heightened flexibility

#### Communication Architecture

The Honeywell interface to the Measurex DIALOG is accomplished through the Communication Link Module developed for Measurex, and includes:

- \* User **data base**
- \* User program
- \* Communication **firmware**
- \* Gauging system interface device

Complementing the CLM, the Measurex Communication Module provides a:

- \* Distributed control system interface device
- \* DIALOG Data Directory handler to simplify link...

6/3,K/26 (Item 11 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2002 The Gale Group. All rts. reserv.

01081630 Supplier Number: 40467473 (USE FORMAT 7 FOR FULLTEXT)  
MCDONNELL DOUGLAS EXPANDS SUPERMINICOMPUTER LINE: ADDS MID-RANGE SYSTEM  
News Release, p1  
August 3, 1988  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 865

... require. The 18/600 is designed to solve complex business problems utilizing the proprietary McDonnell Douglas REALITY" Operating System that contains performance sensitive portions in **firmware** to maximize the efficiency of **data base** operations. The REALITY Operating System on the 18/600 comes bundled with a recently announced operating system shell, REALITY Integrated System Management (REALISM.), to provide...

6/3,K/27 (Item 12 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2002 The Gale Group. All rts. reserv.

01038754 Supplier Number: 40026650 (USE FORMAT 7 FOR FULLTEXT)  
WORLD'S FIRST RELATIONAL PROCESSOR  
PR Newswire, pN/A  
April 14, 1987  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 524

... real-time operation. It provides an exceptionally elegant, low cost solution to the problem of extracting high throughput with real-time performance from a relational **database**. FRP is centred on specialised **firmware** dedicated to **database** handling. Consequently it offers a much higher performance than general purpose machines such as those based on 32 bit microprocessors, transputers and array processors in...

6/3,K/28 (Item 13 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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01036413 Supplier Number: 40003089 (USE FORMAT 7 FOR FULLTEXT)  
MCDONNELL DOUGLAS EXTENDS HIGH END OF COMPUTER LINE: TOPS INDUSTRY STANDARDS FOR SPEED, TRANSACTION SUPPORT  
PR Newswire, pN/A  
March 23, 1987  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 760

... predictably higher throughput even at the maximum number of terminal users.

The proprietary REALITY System is designed to allow performance-sensitive portions to reside in **firmware**, thus providing synergistic efficiency of **data base** operations.

These attributes are combined to make the Series 18 both a "database engine" and a fully integrated business system -- hardware, software, **firmware**, networking capability -- optimized for **database**

intensive applications such as transaction processing.

Brinkley added, "The Series 18 is our first step in offering systems that not only offer unparalleled price/performance...

6/3,K/29 (Item 1 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

03919668 Supplier Number: 50148992 (USE FORMAT 7 FOR FULLTEXT)  
-LEXMARK: Lexmark's new, easy-to-use print server pulls jobs off the  
network quickly with fast Ethernet  
M2 Presswire, pN/A  
July 9, 1998  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 694

(USE FORMAT 7 FOR FULLTEXT)  
TEXT:

...Lexmark's family of print servers are Web-ready and can be configured through your favorite browser. Embedded links to technical support, printer driver and **firmware** update **databases** and online product registration on the Lexmark Web Site make maintenance easy. The links can be customized to point to customers or reseller intranet sites...

6/3,K/30 (Item 2 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

03296335 Supplier Number: 46768311 (USE FORMAT 7 FOR FULLTEXT)  
**BASE EXCELLERATION: Database Excelleration & Kenan Systems achieve on client/server systems**  
M2 Presswire, pN/A  
Oct 2, 1996  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 884

... acceleration products and professional services for very high transaction and query-intensive client/server applications. DES products combine solid state memory and advanced DES DirectAddressing **firmware** to dramatically accelerate **database** application performance ranges of Unix or NT servers. The company also provides professional diagnostic and evaluation services to assist users in optimizing Unix and NT...

6/3,K/31 (Item 3 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
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03172202 Supplier Number: 46498721 (USE FORMAT 7 FOR FULLTEXT)  
**3COM: 3COM announces breakthrough in ISDN call management**  
M2 Presswire, pN/A  
June 28, 1996  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 407

... of hung calls and faulty ports, allowing service affecting problems to be resolved quickly.

Other features include detailed call billing information, a centrally held configuration **database**, and **firmware** download capabilities, specifically designed for 3Com's Remote Access ISDN products to simplify the customer's management task.

"This software is an industry first in...

6/3,K/32 (Item 4 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)

(c) 2002 The Gale Group. All rts. reserv.

02749169 Supplier Number: 45579473 (USE FORMAT 7 FOR FULLTEXT)  
**DECSWITCH 400 ETHERNET/ATM SWITCH AVAILABLE FROM DIGITAL EQUIPMENT**  
LAN Product News, v7, n6, pN/A  
June, 1995  
Language: English Record Type: Fulltext  
Document Type: Newsletter; Trade  
Word Count: 1129

... supplemental services with this product. These services include unlimited 7 day a week, 24 hours a day access to toll-free technical support and information **databases**, access to the latest **firmware**, troubleshooting, and onsite repair of the DECswitch 400. Users can select an onsite service level -- from next-day onsite to 7 day a week, 24...

**6/3,K/33 (Item 5 from file: 636)**  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

02609045 Supplier Number: 45277903 (USE FORMAT 7 FOR FULLTEXT)  
**VITRO RECEIVES \$15.8 MLN CONTRACT FROM U.S. NAVY (JAN 20/1733 GMT)**  
Periscope Daily Defense News Capsules, pN/A  
Jan 20, 1995  
Language: English Record Type: Fulltext  
Document Type: Newsletter; Trade  
Word Count: 82

... will provide test and evaluation/quality assurance engineering services for the AN/SQQ-89(V) Surface Antisubmarine Warfare Combat system and subsystem software, hardware, and **firmware**.

(COPYRIGHT 1995, PERISCOPE/USNI MILITARY **DATABASE** . FOR MORE INFORMATION CALL RICH KOCHMAN, 1-800-929-4824, EXT 288.)  
Copyright 1995 United Communications Group

**6/3,K/34 (Item 6 from file: 636)**  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

02550409 Supplier Number: 45144398 (USE FORMAT 7 FOR FULLTEXT)  
**DIGITAL'S NEW ALPHA 21066A CHIP WITH PCI PUTS MORE SPEED INTO EMBEDDED APPLICATIONS, DESKTOP PCs**  
M2 Presswire, pN/A  
Nov 17, 1994  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 931

... a platform for parallel development of hardware and software applications. The board is packaged with a user's manual, full layout and mechanical information, design **database**, material lists, debug monitor, and **firmware** development tools.

Prices, Availability The Alpha 21066A microprocessor is offered at two clock speeds: 100MHz and 233MHz. At 233 MHz it is estimated to deliver...

**6/3,K/35 (Item 7 from file: 636)**  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

02100241 Supplier Number: 43883540 (USE FORMAT 7 FOR FULLTEXT)  
**New MATIMOP Projects**  
Israel Business Today, v8, n330, pN/A  
June 4, 1993  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; General Trade



Word Count: 278

... developed. The system can scan and digitize high quality photographs, maps, drawings, and other gray scale or full color image information. It provides an efficient **firmware** management for large photographic **archives**, and has a very short search and display. The digitized images are stored on erasable or optical discs to be used only once. They can...

6/3,K/36 (Item 8 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

01593436 Supplier Number: 42401858 (USE FORMAT 7 FOR FULLTEXT)  
**1ST 2.1-MEGABYTE 1/4-IN. CARTRIDGE TAPE DRIVE BOWS**  
Data Storage Report, pN/A  
Oct, 1991  
Language: English Record Type: Fulltext  
Document Type: Newsletter; Trade  
Word Count: 627

... memory control and block and frame formatting in hardware. This minimizes system overhead. Firmware in the drive ensures plug-and-play compatibility with older version **Archive** drives. Also the **firmware** is contained in EEPROM memory, thus it can be field upgraded as new features are added to the drive. It also contains the full implementation...

6/3,K/37 (Item 1 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2002 The Gale Group. All rts. reserv.

04816081 Supplier Number: 47085158 (USE FORMAT 7 FOR FULLTEXT)  
**BMC maintains pole position**  
UNIX News, p42  
Feb, 1997  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 252

... it to embed Patrol agents into Java applets.  
BMC's gradually moving Patrol agents from outside the system into its guts, managing applications first, then **databases**, operating systems and **firmware** and now middleware.  
Patrol business is growing at 300%, while mainframe database tools business is growing at 50%.

6/3,K/38 (Item 1 from file: 160)  
DIALOG(R)File 160:Gale Group PROMT(R)  
(c) 1999 The Gale Group. All rts. reserv.

01623005  
**Solid-state database engine optimized for realtime.**  
ELECTRONIC ENGINEERING TIMES May 4, 1987 p. 42

... offers a low-cost solution to the problem of extracting high throughput with realtime performance from a relational database. The processor is based on specialized **firmware** dedicated to **database** handling. ...

6/3,K/39 (Item 2 from file: 160)  
DIALOG(R)File 160:Gale Group PROMT(R)  
(c) 1999 The Gale Group. All rts. reserv.

01219488

Probbase Acquired.

OFFICE SYSTEMS 84 June, 1985 p. 22

Probbase has been acquired by **Firmware**. Probbase develops relational **data base** products.

...

6/3,K/40 (Item 3 from file: 160)

DIALOG(R)File 160:Gale Group PROMT(R)

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00776770

**Amperif** introduced a relational data base machine that enables **Sperry Univac 1100** series users to increase computer system performance and reduce application development costs.

MIS Week June 2, 1982 p. 15

The Relational Database Machine (RDM 1100) combines specially-designed hardware, relational **database** management system **firmware** and 1100 Series host software in an integrated package. By offloading database management tasks from the host computer, RDM 1100 increases the speed of both...

6/3,K/41 (Item 4 from file: 160)

DIALOG(R)File 160:Gale Group PROMT(R)

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00622469

The most interesting data base machines currently announced will be available only to OEMs.

Computerworld January 5, 1981 p. 8-12

A data base machine is a computer or computer-like device whose sole purpose is to execute DBMS ( **data base** management systems) software or **firmware** relative to a coherent data file. A data base machine is not intended to be an independent computer, although it can be. It reacts to...

6/3,K/42 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB

(c)2002 The Gale Group. All rts. reserv.

10460136 SUPPLIER NUMBER: 21129853 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Mylex and Lucent Technologies to Provide RAID Technology for Next-Generation Voice Messaging System.**

Business Wire, p9150182

Sept 15, 1998

LANGUAGE: English RECORD TYPE: Fulltext

WORD COUNT: 772 LINE COUNT: 00074

... Walter Fant, systems architect at the Lucent Technologies Octel Messaging Division.

"Mylex stepped up to the plate and invested significant engineering resources to develop custom **firmware** that supports real-time **archiving** and playback of digitized voice messages. Mylex also developed special technology which allows customers of our popular Sierra family of voice messaging products to migrate...

6/3,K/43 (Item 2 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB

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09994601 SUPPLIER NUMBER: 20185983 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Tegic eyes a new concept for key input. (Tegic Communications' T9 linguistic database software) (Company Business and Marketing)**

Wirbel, Loring  
Electronic Engineering Times, n989, p59(1)  
Jan 19, 1998  
ISSN: 0192-1541      LANGUAGE: English      RECORD TYPE: Fulltext  
WORD COUNT: 483      LINE COUNT: 00041

... times Tegic will approach markets other than OEMs is for a downloadable aftermarket, where companies or individuals want to add more languages to the linguistic **database** through **firmware** upgrades. Vertical linguistic **databases** for vertical PDA markets also are possible. For these, Tegic is exploring a range of sales channels.

Because small input devices are so common, Tegic...

6/3,K/44      (Item 3 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

09751854      SUPPLIER NUMBER: 19792572      (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Lexmark's New, easy-to-use Print Servers Help customers print much faster than the leading competitors' products.**  
Business Wire, p9291230  
Sep 29, 1997  
LANGUAGE: English      RECORD TYPE: Fulltext  
WORD COUNT: 661      LINE COUNT: 00061

... print queues faster, reducing waiting for print jobs.  
The MarkNet Pro print servers are Web-ready with embedded links to technical support, printer driver and **firmware** update **databases** and on-line product registration on the Lexmark(TM) Web site. The links can be customized to point to customers' or resellers' intranet information databases...

6/3,K/45      (Item 4 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

09735605      SUPPLIER NUMBER: 19724040      (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Novell checking its figures as millennium date nears. (Novell's Year 2000 compliance project) (White Paper: Novell Inc.) (Company Operations)**  
Computer Reseller News, n751, p83(3)  
August 25, 1997  
ISSN: 0893-8377      LANGUAGE: English      RECORD TYPE: Fulltext; Abstract  
WORD COUNT: 2841      LINE COUNT: 00237

... year 2000-compliant.  
\* Creating the compliant test bed: In most cases, creating a compliant environment will require coordination in building compliant versions of hardware system, **firmware**, operating system, **database** version, third-party application software, in-house developed soft-ware and utilities (e.g., backup systems).  
Organization Of Novell's Year 2000 Effort  
Project 2000...

6/3,K/46      (Item 5 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

07941492      SUPPLIER NUMBER: 17093448      (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**NetWorth announces HubView second-generation network management product line; easy-to-use SNMP-based network management software.**  
Business Wire, p6211071  
June 21, 1995  
LANGUAGE: ENGLISH      RECORD TYPE: FULLTEXT  
WORD COUNT: 801      LINE COUNT: 00087

... Set NetWorth hub IP addresses  
o MIB Browser -- View and edit MIB objects  
o TFTP Server -- Trivial File Transfer Protocol, used for updating NetWorth hub **firmware**  
o **Database** backup/restore facilities  
-- Windows 3.11 version includes Novell's proven TCP/IP protocol stack  
-- WINSOCK compliant for compatibility with other TCP/IP stacks  
Release...

6/3,K/47 (Item 6 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

05471882 SUPPLIER NUMBER: 11407537 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Mismatched media don't protect data. (using rewritable media in a write-once mode will not maintain unalterable data in multifunction drives) (Spotlight on Optical Storage) (Brief Article)**  
Hoy, John J.  
Government Computer News, v10, n19, p75(1)  
Sept 16, 1991  
DOCUMENT TYPE: Brief Article ISSN: 0738-4300 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT  
WORD COUNT: 323 LINE COUNT: 00025

... features to detect and set write-protect flags.  
What happens when this new type of media is put into older drives that lack the proper **firmware** ? The **archived** data can be overwritten easily.  
Industry analysts estimate that about 80,000 optical drives have been sold, only a few of which eventually will receive...

6/3,K/48 (Item 7 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

05409804 SUPPLIER NUMBER: 10998958 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Trusted system packages do much of agencies' work. (a report on secure government data systems and the Trusted Computer System Evaluation Criteria, or Orange Book) (Open File) (column)**  
Taft, Darryl K.  
Government Computer News, v10, n14, p44(2)  
July 8, 1991  
DOCUMENT TYPE: column ISSN: 0738-4300 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 673 LINE COUNT: 00053

... on which the protection is based."  
The TCB may include key elements or all of the operating system and other software -- such as a secure **database** management system -- as well as **firmware** and hardware.  
A commercial TCB, such as a trusted operating system, provides a secure foundation to build upon, saving the system developer both time and  
...

6/3,K/49 (Item 1 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
(c) 2002 ProQuest Info&Learning. All rts. reserv.

01797779 04-48770  
**France: Emmanuel Michau**  
Anonymous  
International Tax Review Guide to the World's Leading Information Technology Advisers Supplement PP: 18 Feb 1999  
ISSN: 0958-7594 JRNL CODE: ITR  
WORD COUNT: 201

...TEXT: and other on-line services, advertising and communication, trade mark and domain name, copyright, design and intellectual property generally, cryptography, security, fraud, audiovisual, radio, multimedia, **data bases**, personal information, directories, EDI, software, **firmware**, hardware and computer technologies at large, consumer protection, privacy, research and development, transfer of technologies, marketing and distribution, outsourcing, commercial and financial ICT transactions, joint ...

6/3,K/50 (Item 2 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
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01421118 00-72105  
**Whether volatile or non-volatile, SSD offers blazing speeds**  
Murphy, Robert  
Computer Technology Review PP: 92-95 Spring 1997  
ISSN: 0278-9647 JRNL CODE: CTN  
WORD COUNT: 1147

...TEXT: conventional RAID systems feature as little as 64MB of cache, and lack the capacity for the high read rates demanded by Internet pages or news **databases**. With an easy **firmware** change, SSD drives can be used to expand this read cache and significantly boost RAID performance in lowwrite/high-read traffic patterns. SSDs allow both...

6/3,K/51 (Item 3 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
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01342559 99-91955  
**Date 2000: A practical guide**  
Barnes, Roger  
Management Accounting-London v74n11 PP: 38-39 Dec 1996  
ISSN: 0025-1682 JRNL CODE: MAC  
WORD COUNT: 2022

...TEXT: encompasses both systems software and application software in use on mainframes, minis, file-servers and PCs. Systems software includes operating systems, compilers, utilities, development tools, **database** management and **firmware** in use in telecommunications and computers. Application software includes packages, bespoke software and macros.

To start with, categorise systems, i.e. corporate systems under the...

6/3,K/52 (Item 1 from file: 635)  
DIALOG(R)File 635:Business Dateline(R)  
(c) 2002 ProQuest Info&Learning. All rts. reserv.

0610688 95-66983  
**NetWorth announces HubView second-generation network management product line**  
Oshman, Tamye  
Business Wire (San Francisco, CA, US) s1 p1  
PUBL DATE: 950621  
WORD COUNT: 1,060  
DATELINE: Irving, TX, US

TEXT:

- ...Set NetWorth hub IP addresses
- o MIB Browser -- View and edit MIB objects

- o TFTP Server -- Trivial File Transfer Protocol, used for updating NetWorth hub **firmware**
- o **Database** backup/restore facilities
- Windows 3.11 version includes Novell's proven TCP/IP protocol stack
- WINSOCK compliant for compatibility with other TCP/IP stacks

Release...

6/3,K/53 (Item 2 from file: 635)  
DIALOG(R)File 635:Business Dateline(R)  
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0544359 95-01245

**Digital's new Alpha 21066A chip with PCI puts more speed into embedded applications, desktop PCs**

McGloin, Patricia  
PR Newswire (New York, NY, US) s1 p1  
PUBL DATE: 941114  
WORD COUNT: 850  
DATELINE: Maynard, MA, US

TEXT:

...a platform for parallel development of hardware and software applications. The board is packaged with a user's manual, full layout and mechanical information, design **database**, material lists, debug monitor, and **firmware** development tools.

Prices, Availability

The Alpha 21066A microprocessor is offered at two clock speeds: 100MHz and 233MHz. At 233 MHz it is estimated to deliver...

6/3,K/54 (Item 3 from file: 635)  
DIALOG(R)File 635:Business Dateline(R)  
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0063376 88-21542

**McDonnell Douglas Expands Superminicomputer Line; Adds Mid-Range System**

Goodrich, S.  
Business Wire (San Francisco, CA, US) s1 p1  
PUBL DATE: 880803  
WORD COUNT: 792  
DATELINE: Irvine, CA, US

TEXT:

...require. The 18/600 is designed to solve complex business problems utilizing the proprietary McDonnell Douglas REALITY Operating System that contains performance sensitive portions in **firmware** to maximize the efficiency of **database** operations.

The REALITY Operating System on the 18/600 comes bundled with a recently announced operating system shell, REALITY Integrated System Management (REALISM), to provide...

6/3,K/55 (Item 1 from file: 9)  
DIALOG(R)File 9:Business & Industry(R)  
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02042804 (USE FORMAT 7 OR 9 FOR FULLTEXT)

Text-Recognition Software -- Tegic eyes a new concept for key input  
(Tegic Communications developed linguistic predictive database package that  
allows text input from a 9-key keypad)  
Electronic Engineering Times, p 59  
January 19, 1998  
DOCUMENT TYPE: Journal ISSN: 0192-1541 (United States)  
LANGUAGE: English RECORD TYPE: Fulltext  
WORD COUNT: 435

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:

...times Tegic will approach markets other than OEMs is for a downloadable aftermarket, where companies or individuals want to add more languages to the linguistic **database** through **firmware** upgrades. Vertical linguistic **databases** for vertical PDA markets also are possible. For these, Tegic is exploring a range of sales channels.

Because small input devices are so common, Tegic...

6/3,K/56 (Item 1 from file: 810)  
DIALOG(R)File 810:Business Wire  
(c) 1999 Business Wire . All rts. reserv.

0739329 BW0207

CQN HEWLETT PACKARD 4: Hewlett-Packard corrects and replaces BW1327 -- HP and Database Excelleration Systems Deliver Industry-Leading I/O Performance on HP 9000 T600; HP and DES Combine Technologies to Demonstrate Industry's Best I/O Throughput Performance

August 26, 1997

Byline: Business Editors/Computer Writers

...solutions for  
high-availability, secured-Web-server, distributed-computing,  
server-consolidation and mainframe-alternative applications.  
By combining solid-state memory enhanced with DES-developed  
DirectAddressing **firmware**, the DES **Database** Excellerator dramatically  
increases the performance of relational database (RDBMS)  
applications. By moving key RDBMS "hot files," temporary work  
areas, transaction logs or indices onto a...

6/3,K/57 (Item 2 from file: 810)  
DIALOG(R)File 810:Business Wire  
(c) 1999 Business Wire . All rts. reserv.

0065590 BW084

MCDONNELLDUGLAS: McDonnell Douglas new Series 18 Superminicomputer now  
operational at customer sites

September 30, 1987

Byline: Business Editors

...throughput even with the maximum number of terminal users.  
The new series incorporates the company's proprietary REALITY  
Operating System with performance-sensitive portions in **firmware**,  
providing synergistic efficiency of **database** operations and more  
efficient utilization of memory.

The Series 18 is described as both a "database engine" and a  
fully integrated business system -- hardware, software, **firmware**,  
networking capability -- optimized for **database** intensive applications  
such as transaction processing. With the addition of the new series,

the company now has the ability to support small business,  
departmental and...

6/3,K/58 (Item 1 from file: 647)  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

01150600 CMP ACCESSION NUMBER: EET19980119S0070  
**Text-Recognition Software - Tegic eyes a new concept for key input**  
Loring Wirbel  
ELECTRONIC ENGINEERING TIMES, 1998, n 989, PG59  
PUBLICATION DATE: 980119  
JOURNAL CODE: EET LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: Design  
WORD COUNT: 453

... times Tegic will approach markets other than OEMs is for a  
downloadable aftermarket, where companies or individuals want to add more  
languages to the linguistic **database** through **firmware** upgrades.  
Vertical linguistic **databases** for vertical PDA markets also are  
possible. For these, Tegic is exploring a range of sales channels.  
Because small input devices are so common, Tegic...

6/3,K/59 (Item 2 from file: 647)  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

01135760 CMP ACCESSION NUMBER: CRN19970825S0095  
**Novell Checking Its Figures As Millennium Date Nears**  
COMPUTER RESELLER NEWS, 1997, n 751, PG83  
PUBLICATION DATE: 970825  
JOURNAL CODE: CRN LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: White Paper: Novell Inc.  
WORD COUNT: 2694

... year 2000-compliant.  
- Creating the compliant test bed: In most cases, creating a  
compliant environment will require coordination in building compliant  
versions of hardware system, **firmware**, operating system, **database**  
version, third-party application software, in-house developed soft- ware  
and utilities (e.g., backup systems)  
Organization Of Novell's Year 2000 Effort  
Project 2000...

6/3,K/60 (Item 3 from file: 647)  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

01042125 CMP ACCESSION NUMBER: VAR19950101S0059  
**Selling Fax on Demand (Q&)**  
Ron Levine  
VARBUSINESS, 1995, n 11, PG127  
PUBLICATION DATE: 950101  
JOURNAL CODE: VAR LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: Computer Telephony  
WORD COUNT: 1414

... basic components for building a fax-on-demand system are:  
A computer with plenty of hard disk space to accommodate image  
storage and the information **database**.  
A voice board (with associated **firmware** and software) to provide a  
voice interface to the caller and to function as the attendant and call



director.

A fax board (with associated firmware...

6/3,K/61 (Item 4 from file: 647)

DIALOG(R)File 647:CMP Computer Fulltext

(c) 2002 CMP Media, LLC. All rts. reserv.

00532912 CMP ACCESSION NUMBER: EBN19930621S3200

**4-Gbyte DAT Drives From Conner Ship In Volume**

Ann R. Thryft

ELECTRONIC BUYERS' NEWS, 1993, n 859, 18

PUBLICATION DATE: 930621

JOURNAL CODE: EBN LANGUAGE: English

RECORD TYPE: Fulltext

SECTION HEADING: Computer Systems and Peripherals

WORD COUNT: 341

... available in either 3.5- or 5.25-in. form factors, can operate with either 90- or 120-m tape cartridges, and use the same **firmware** as the **Archive** Turbo Python drive, which had a 2- Gbyte capacity but a 366-Kbyte/s transfer rate, said Dan Sullivan, product manager for Conner Tape Products...

11/9/1 (Item 1 from file: 47)  
DIALOG(R)File 47:Gale Group Magazine DB(TM)  
(c) 2002 The Gale group. All rts. reserv.

05426776 SUPPLIER NUMBER: 55500485 (THIS IS THE FULL TEXT)  
**Lubbock Searches for Embedded Chips.(Year 2000 computer transition problem  
disaster drill in Lubbock, Texas) (Brief Article)**  
Hernandez, Victor; Glass, Sandy  
Nation's Cities Weekly, 22, 32, 5  
August 9, 1999  
DOCUMENT TYPE: Brief Article ISSN: 0164-5935 LANGUAGE: English  
RECORD TYPE: Fulltext  
WORD COUNT: 648 LINE COUNT: 00054

TEXT:

When the city of Lubbock, Tex., conducted one of the nation's first Year 2000 disaster drills last September, the city was besieged with worldwide media attention.

ABC's World News Tonight filmed the drill. Reporters from the Wall Street Journal and other national, regional and local media reported on the show.

In the days following the drill, radio stations, magazines and newspapers called for more information, city manager Bob Cass testified before the Senate on Y2K issues, and the city sent out more than 500 packets of information to other municipalities and counties in the U.S. and abroad.

Since the drill, Lubbock has continued with its Y2K preparedness by identifying and testing its embedded chips. This painstaking process doesn't attract any media attention, but it is the backbone of a successful transition into the year 2000.

Lubbock began the process in late 1997. A committee of employees from every department was formed and examined all the city's equipment and identified its embedded chips. This information was put into a database and prioritized as to what equipment was mission-critical and what was not. Since the city owns an electric company, that embedded chip equipment went right to the top of the priority list.

Knowing that additional expertise was needed, the city hired the information technology consulting firm Strategia to continue work with embedded chips. "We knew we had gone as far as we could," said assistant city manager Richard Burdine. "We called in the experts for the next phase of our Y2K preparation."

Strategia consultants came in and walked through every city facility to verify the embedded chips in the inventory and physical infrastructure. The company compiled a second **database of embedded chips** and then began to survey vendors regarding compliance.

Ron Lewis, assistant to the city manager, said, "We started out with about 1,100 items that contained embedded chips, but we narrowed it down to approximately 900 by taking out fax machines and copiers, and other non-critical equipment."

Strategia contacted each vendor and requested compliance information. Those compliance letters filled six huge notebooks. Those notebooks, in turn, became the responsibility of Dan Krejci, a member of the city manager's staff.

"We had information from vendors that affected 47 cost centers," Krejci said. "I have spent the last few months visiting each cost center and going over each letter. We are checking the equipment to make sure we agree with the vendors. If the vendor says a piece of equipment is Y2K compliant, we want to follow-up to make sure. If the vendor says it is not compliant, we evaluate the function it serves and replace it if needed."

Lubbock will continue to test its embedded chips until all of the identified chips have been deemed compliant or have been replaced. Even with this meticulous testing, there is still an element of uncertainty. No one knows exactly what will happen at 12:01 a.m. January 1, 2000. With that in mind, the city is now putting together a Personnel Staffing Plan for the transition period.

This plan includes who will be in the Emergency Operations Center on December 31, 1999, and January 1, 2000, and who will be on call. Names, addresses, phone numbers, pager numbers, radio capability--it's all in the

plan.

"If we need someone and we can't raise them on the phone," Burdine said, "we'll drive out to their house and get them. We are going to be as prepared as we can possibly be."

As the Year 2000 transition approaches, Lubbock's motto is: Preparation, not panic.

Victor Hernandez is a councilmember in Lubbock and Sandy Glass is the city's public information officer. Hernandez can be reached at [vhernandez@mail.ci.lubbock.tx.us](mailto:vhernandez@mail.ci.lubbock.tx.us) and Glass at [sglass@mail.ci.lubbock.tx.us](mailto:sglass@mail.ci.lubbock.tx.us).

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11/3,K/1 (Item 1 from file: 47)  
DIALOG(R)File 47:Gale Group Magazine DB(TM)  
(c) 2002 The Gale group. All rts. reserv.

05426776 SUPPLIER NUMBER: 55500485 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Lubbock Searches for Embedded Chips.** (Year 2000 computer transition problem  
disaster drill in Lubbock, Texas) (Brief Article)  
Hernandez, Victor; Glass, Sandy  
Nation's Cities Weekly, 22, 32, 5  
August 9, 1999  
DOCUMENT TYPE: Brief Article ISSN: 0164-5935 LANGUAGE: English  
RECORD TYPE: Fulltext  
WORD COUNT: 648 LINE COUNT: 00054

... consultants came in and walked through every city facility to  
verify the embedded chips in the inventory and physical infrastructure. The  
company compiled a second **database** of **embedded chips** and then began  
to survey vendors regarding compliance.

Ron Lewis, assistant to the city manager, said, "We started out with  
about 1,100 items that...

11/3,K/2 (Item 1 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

11188106 SUPPLIER NUMBER: 55083900 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Embedded systems open door for database.**  
Landriault, Gabriel  
Computer Dealer News, 15, 23, 39(1)  
June 11, 1999  
ISSN: 1184-2369 LANGUAGE: English RECORD TYPE: Fulltext  
WORD COUNT: 852 LINE COUNT: 00069

TEXT:

Sybase is promising the ability to keep track of every link in your  
chain through tiny **databases** on **embedded chips** .

13/3,K/1 (Item 1 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

01636867 SUPPLIER NUMBER: 14098599 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Jack of all trades. (open systems staff expected to provide a wide variety of skills)**  
Lang, Mark  
IBM System User, v14, n7, p41(2)  
July, 1993  
ISSN: 0950-303X LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 1827 LINE COUNT: 00146

... employees lack critical skills at the leading edge of technology. The report, entitled 'IT Skills in the 90s', identifies the key skills shortages.

- \* C Programmers.
- \* **Embedded software** .
- \* **Database** design.
- \* End user technical support.
- \* Unix systems.
- \* 4 GLs:
- \* Application development.
- \* Systems integration.
- \* Telecommunications--voice/data.

The report also asked respondents what they thought were...

13/3,K/2 (Item 2 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

01350243 SUPPLIER NUMBER: 08214978 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**DEC enters fault-tolerant market. (DEC's VAXft 3000 310, minicomputer)**  
**(product announcement)**  
Stedman, Craig  
Electronic News, v36, n1799, p18(1)  
March 5, 1990  
DOCUMENT TYPE: product announcement ISSN: 0013-4937 LANGUAGE:  
ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 730 LINE COUNT: 00055

... to keep the system running for up to 15 minutes during power failures.

The new VMS release, which supports all VAX and MicroVAX machines, includes **embedded software** for linking multiple **databases** in OLTP applications, DEC said. The firm also introduced new releases of its VAX Rdb/VMS and VAs DBMS database packages supporting VMS Version 5...

13/3,K/3 (Item 1 from file: 621)  
DIALOG(R)File 621:Gale Group New Prod. Annou. (R)  
(c) 2002 The Gale Group. All rts. reserv.

03209077 Supplier Number: 87711929 (USE FORMAT 7 FOR FULLTEXT)  
**IBM Alliance With Narad Networks to Enable Data Services Platform For Cable Industry; Strategic Alliance Combines Companies' Technology and Services To Give Cable Operators Faster Access for Small and Mid-Sized Businesses.**  
PR Newswire, pATM01724062002  
June 24, 2002  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 943

... Services Delivery Platform (NSDP). The solutions will include a host of IBM technology, including WebSphere Applications Server, WebSphere Business Integrator, WebSphere Everyplace Server, WebSphere Everyplace **Embedded Software**, DB2 **database** software, advanced storage systems,

PowerPC chips, and IBM eServer pSeries and xSeries systems.

The alliance between Narad and IBM is another example of IBM's...

**13/3,K/4 (Item 2 from file: 621)**  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2002 The Gale Group. All rts. reserv.

03208374 Supplier Number: 87689620 (USE FORMAT 7 FOR FULLTEXT)  
**IBM Alliance with Narad Networks to Enable Data Services Platform for Cable Industry.**  
Business Wire, p2380  
June 24, 2002  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 900

... Services Delivery Platform (NSDP). The solutions will include a host of IBM technology, including WebSphere Applications Server, WebSphere Business Integrator, WebSphere Everyplace Server, WebSphere Everyplace **Embedded Software**, DB2 **database** software, advanced storage systems, PowerPC chips, and IBM eServer pSeries and xSeries systems.  
The alliance between Narad and IBM is another example of IBM's...

**13/3,K/5 (Item 3 from file: 621)**  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
(c) 2002 The Gale Group. All rts. reserv.

02299493 Supplier Number: 59088940 (USE FORMAT 7 FOR FULLTEXT)  
**Red Hat Brings Open Source to Post-PC, Embedded Devices.**  
Business Wire, pl599  
Jan 31, 2000  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 1461

... Tools also provide sophisticated source browsing functionality, build and project management tools, and remote debugging of embedded Linux targets.  
-- Priority access to Red Hat's **embedded software repository** with new ports and tools enhancements.  
-- An unmodified version of the x86 2.2.12 Linux kernel, which is currently shipping with Red Hat Linux...

**13/3,K/6 (Item 4 from file: 621)**  
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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01493133 Supplier Number: 47141128 (USE FORMAT 7 FOR FULLTEXT)  
**CommVault Systems Announces New Center for 24-Hour Technical Assistance; 10-Minute Response Time Guaranteed.**  
Business Wire, p02201314  
Feb 20, 1997  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 539

... of four severity levels. Customer Engineers proactively handle the requests within one hour, although the average response time is less than 10 minutes. CTAC's **database** has **embedded software** that defines steps and escalation processes to guarantee response time and resolution time are tracked.  
"We know that our customers depend on CommVault to make...

**13/3,K/7 (Item 5 from file: 621)**

DIALOG(R)File 621:Gale Group New Prod.Annou.(R)  
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01320049 Supplier Number: 45930729 (USE FORMAT 7 FOR FULLTEXT)  
**Microrim to release R:BASE 5.5 for Windows and OS/2; 32-bit Windows relational database software now offers new graphical interface and design tools.**  
Business Wire, p11130161  
Nov 13, 1995  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 1061

... ANSI SQL.

Level 2 SQL standards are embedded in the R:BASE Windows and OS/2 software, rather than an SQL shell typical of competitive **database** software programs. The **embedded software** enables users to use R:BASE and SQL commands interchangeably, and work nearly seamlessly between different database programs. All R:BASE features and database tools...

13/3,K/8 (Item 1 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

05026765 Supplier Number: 76411694 (USE FORMAT 7 FOR FULLTEXT)  
**FastObject's j2 and e7 database solutions deliver superior database functionality to the embedded market; FastObjects solution brings real-time database to next-generation systems.**  
M2 Presswire, pNA  
July 9, 2001  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 835

... extend product life, j2 is optimised for diskless devices, reducing the wear on flash memory-an industry first for embedded database providers.

"The FastObjects embedded **database** solutions meet **embedded software** developers exactly at their pain points," noted Dirk Bartels, president and CEO of Poet Software. "Most applications in that space today are still using arcane

13/3,K/9 (Item 2 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

03778595 Supplier Number: 48181608 (USE FORMAT 7 FOR FULLTEXT)  
**CEO INTERVIEW- PETER J. BONI, PRESIDENT AND CEO, DISCUSSES THE OUTLOOK FOR CAYENNE SOFTWARE (CAYN, SIC7372)**  
Wall Street Transcript Digest, v27, n3, pN/A  
Dec 15, 1997  
Language: English Record Type: Fulltext  
Document Type: Newsletter; Trade  
Word Count: 154

(USE FORMAT 7 FOR FULLTEXT)

TEXT:

...Peter J. Boni, President and CEO of Cayenne Software, Burlington, Massachusetts, depicts firm's manufacture software tools for programmers. Result merger Bachman Information Systems with **data base** designs, Cadre Technologies; **embedded software**. Size potential market. Reworking global distribution; partnerships abroad; proportion revenue international sales. Five-part strategy company transition; product mix shift; education, consulting services. "Very brisk...

13/3,K/10 (Item 3 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

03688195 Supplier Number: 47955350 (USE FORMAT 7 FOR FULLTEXT)  
**EMBEDDED COMMUNICATIONS SOFTWARE APPROACH COULD LOWER DEVICE COSTS**  
Wireless Data News, v5, n18, pN/A  
Sept 3, 1997  
Language: English Record Type: Fulltext  
Document Type: Newsletter; Trade  
Word Count: 462

... What we're seeing today is that the main application is voice,"  
Salamoff said.

Salamoff predicted that Telogy will be more heavily involved in  
producing **embedded software** for **data - based** devices in about five  
years. In the meantime, Telogy intends to monitor the various standards  
bodies that are developing packet data and high-speed aggregated...

13/3,K/11 (Item 1 from file: 613)  
DIALOG(R)File 613:PR Newswire  
(c) 2002 PR Newswire Association Inc. All rts. reserv.

00786040 20020624ATM017 (USE FORMAT 7 FOR FULLTEXT)  
**IBM Alliance With Narad Networks to Enable Data Services**  
PR Newswire  
Monday, June 24, 2002 10:35 EDT  
JOURNAL CODE: PR LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT  
DOCUMENT TYPE: NEWSWIRE  
WORD COUNT: 877

TEXT:

...Services Delivery Platform  
(NSDP). The solutions will include a host of IBM technology, including  
WebSphere Applications Server, WebSphere Business Integrator, WebSphere  
Everyplace Server, WebSphere Everyplace **Embedded Software**, DB2  
**database**  
software, advanced storage systems, PowerPC chips, and IBM eServer pSeries  
and  
xSeries systems.

The alliance between Narad and IBM is another example of IBM's...



15/9/13 (Item 2 from file: 621)  
DIALOG(R) File 621: Gale Group New Prod. Annou. (R)  
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01061501 Supplier Number: 40265724 (THIS IS THE FULLTEXT)  
**HUGHES RADAR SYSTEMS GROUP AUTOMATES DESIGN CONTROL WITH SHERPA (TM) DMS**  
News Release, p1  
Jan 12, 1988

Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 761  
TEXT:

Contact: Kate Quackenbush  
Hi-Tech Public Relations, Inc. SHP-2330  
(415) 864-5600

Art Fluter  
Sherpa Corporation  
(408) 433-0455

HUGHES RADAR SYSTEMS GROUP AUTOMATES DESIGN CONTROL WITH SHERPA (TM)  
DMS

Projected savings from engineering data management system  
top \$500,000 a year

EL SEGUNDO, Calif., Jan. 12, 1988 -- The Radar Systems Group (RSG) of Hughes Aircraft Company, a subsidiary of GM Hughes Electronics, has put its extensive engineering design databases and processes under the central control of Sherpa Corporation's Design Management System (DMS).

The Sherpa (TM) DMS system is being used to manage and control the design process for components of Hughes' on-board radar systems in the F-14, F-15, and F/A-18 fighter aircraft. By tracking and monitoring design files and associated data from conception through release to manufacturing -- and thus streamlining the entire complex design process -- the Sherpa system is expected to save Hughes more than \$1,000 per database, per year in labor costs that would occur through normal paper tracking and control.

"By installing the Design Management System, we've taken the initial steps to consolidate the management of our multi-level design database, which is spread over many engineering project teams," said Jim Veronica, manager of RSG's integrated CAD/CAM systems. "We estimate that the system's communications functions alone -- the ability to electronically alert team members to developments in the project -- will shave at least a half a million dollars a year off our design costs.

The system saves us much of the labor costs of tracking and maintaining comprehensive design files, and is providing us with a return on total investment of less than two years."

#### Controlling PCB build designs

The Sherpa DMS software runs on Hughes' Digital Equipment Corporation VAX (TM) 8800 system -- with which some 500 or more engineers are linked via terminals or PCs running terminal emulation software.

Sherpa DMS was installed to fulfill Hughes' long-term plans to build a central source of control for all engineering files -- comprising more than 3500 design databases -- throughout the Radar Systems Group Engineering Division. As a result of consolidating design control, the Sherpa system will permit all project team members to:

- o find the latest electronic revision of a file;

- o access DMS to approve and promote designs;
- o customize management of the design process for each project;
- o instantly communicate design changes to other engineers on a design team.
- o and utilize Sherpa Alerts to trigger an automatic check program for alerting team members of a design's progress.

Currently, Sherpa DMS is being used by database administrators to monitor and track net lists and build designs for printed circuit boards (PCBs) and mechanical chassis assemblies for Hughes airborne radars. In particular, the system controls approximately 200 **databases** containing **programmable** read only memory ( **PROM** ) and **programmable** logic array (PLA) instructions for Hughes radar systems PCBs.

*for radar*

By the end of 1988, Sherpa DMS will control design documentation, wire harness designs, wire lists, PCB assemblies, and machine parts for the RSG radars. Within two years, Veronica expects Hughes engineers and managers to work directly with the system to monitor database processing and project status.

"We plan for Sherpa DMS to be the cornerstone of our integrated CAD/CAM system," said Veronica. "Sherpa delivered a comprehensive, flexible design management solution that was ready to implement immediately."

Electronic system provides accessibility, ease of use

According to Veronica, the Sherpa system has proven highly accessible and easy to use. Hughes' program administrators can now track the progress of a project by logging on to the Sherpa system. Previously, they had to call or route memos to project engineers and database managers to receive such status information, or hold weekly meetings to gather the information.

In addition, the ready availability of design status information from the Sherpa DMS system allows manufacturing engineers to review design databases before the design is officially released. As a result, problems can be pinpointed and corrected earlier in the design's iteration.

Future plans for the system include adding new areas of control and expanding the system across multiple DEC VAX computers using the Sherpa Distributed Access DMS software. In addition, Hughes RSG plans to have the DMS system control files from Computervision, Sun, and other workstations.

Sherpa Corporation is headquartered in San Jose, Calif. The company specializes in software tools and services to improve the design management process across diverse engineering environments, CAD/CAE applications, and hardware platforms.

For more information, contact Sherpa Corporation at 611 River Oaks Parkway, San Jose, CA 95134. Phone (408) 433-0455.

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VAX is a registered trademark of Digital Equipment Corporation.

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PUBLISHER NAME: Various

COMPANY NAMES: \*GM Hughes Electronics Corp.; Hughes Aircraft Co.; Sherpa Corp.

EVENT NAMES: \*330 (Product information); 460 (Use of materials & supplies)

GEOGRAPHIC NAMES: \*1USA (United States); 1U9CA (California)  
PRODUCT NAMES: \*7372431 (CAD/CAM/CIM/CAE Software)  
INDUSTRY NAMES: BUS (Business, General); BUSN (Any type of business)  
NAICS CODES: 51121 (Software Publishers)  
TICKER SYMBOLS: GMH  
TRADE NAMES: Sherpa DMS

15/9/21 (Item 1 from file: 160)  
DIALOG(R) File 160:Gale Group PROMT(R)  
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01956912

**CAD Files Controlled**

Computer Decisions June, 1988 p. 40A  
ISSN: 0010-4558

Hughes Aircraft's Radar Systems Group is using Sherpa's (San Jose, CA) Design Management System to manage 200 **databases** containing designs for **programmable** ROM and logic array instructions used on printed circuit boards for Hughes's on-board radar systems for the F-14, F-15, and F/A-18 fighter aircraft. DMS is used to track and monitor design files and associated data from conception through release to mfg. Additionally, DMS manages the entire design and approval process.

COMPANY:

\*Hughes Aircraft  
Sherpa

PRODUCT: \*Missiles, Space Vehicles & Parts (3760000); Graphics Software  
Pkgs (Micro) (7372440)  
EVENT: \*Use of Materials & Supplies (46); Product Design & Development  
(33)  
COUNTRY: \*United States (1USA)

15/3,K/1 (Item 1 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02500685 SUPPLIER NUMBER: 74225183 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Component data for OEMs. (Company Business and Marketing)**

Goering, Richard

Electronic Engineering Times, 64

May 7, 2001

ISSN: 0192-1541 LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 478 LINE COUNT: 00043

... the ability to go in and search components, and they can set up communities where they share information with each other."

SiliconExpert has developed a **database** of some 300,000 **components**, including microprocessors, microcontrollers, **programmable** -logic devices, analog and communications chips. It promises to have 800,000 active components in the database by this fall.

Other active component databases exist...

15/3,K/2 (Item 2 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

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02361282 SUPPLIER NUMBER: 58448465 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Infoliant does your Y2K legwork. (Infoliant Year 2000 Network**

**Advisor) (online database) (Company Business and Marketing)**

Apicella, Mario

InfoWorld, 21, 52, 55

Dec 27, 1999

ISSN: 0199-6649 LANGUAGE: English

RECORD TYPE: Fulltext; Abstract

WORD COUNT: 692 LINE COUNT: 00059

...ABSTRACT: a large database of year 2000 patches and fixes for a myriad of applications from the home user level to the enterprise level. The searchable **database** finds software, **hardware**, and **BIOS** information by product or manufacturer. The user can tag the products to track, and Infoliant sends emails as those manufacturers issue year 2000 updates. It

... for the InfoWorld Test Center.

THE BOTTOM LINE: VERY GOOD

Infoliant Year 2000 Network Advisor

Summary: Infoliant Year 2000 Network Advisor is a Web-accessible **database** that aggregates Y2K-related **hardware**, software, and **BIOS** compliance information on numerous enterprise platforms including mainframes, Unix, and Windows.

Business Case: As a repository of information on most or all of the hardware...

15/3,K/3 (Item 3 from file: 275)

DIALOG(R)File 275:Gale Group Computer DB(TM)

(c) 2002 The Gale Group. All rts. reserv.

02326765 SUPPLIER NUMBER: 55613147

**Microsoft strengthens management tool. (Microsoft System Management Server 2.0 software) (Software Review) (Evaluation)**

Symoens, Jeff

Federal Computer Week, 13, 27, 32(2)

August 9, 1999

DOCUMENT TYPE: Evaluation

ISSN: 0893-052X

LANGUAGE: English

RECORD TYPE: Abstract

...ABSTRACT: system inventory capabilities. It would be helpful if some SMS 2.0 features were taken further. For example, Year 2000 compliance-checking does not accommodate **hardware** and **BIOS** verification checking, and the initialproduct **database** only includes entries for

Microsoft products.

**15/3,K/4 (Item 4 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)  
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02189151 SUPPLIER NUMBER: 20841225 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Tomorrow's data centres will move to the big box. (Industry Trend or Event)**  
Daniel, Dianne  
Computing Canada, v24, n24, p29(2)  
June 22, 1998  
ISSN: 0319-0161 LANGUAGE: English RECORD TYPE: Fulltext  
WORD COUNT: 1374 LINE COUNT: 00109

... it was to our benefit to get a much larger system and put all our users on one."

In addition to the Domino server, the **board** houses two other **CMOS** machines. DB2 is the primary **database** technology, storing a total of 1.6 TB of data.

Meta Group's Greiner says the S/390 architecture is now a cost-effective alternative...

**15/3,K/5 (Item 5 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)  
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02086985 SUPPLIER NUMBER: 19576792 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Visualizing a new world. (Microsoft's Visual Studio 97 application development software) (Software Review) (Evaluation)**  
Whipple, Larry C.  
Databased Web Advisor, v15, n7, p60(5)  
July, 1997  
DOCUMENT TYPE: Evaluation ISSN: 1090-6436 LANGUAGE: English  
RECORD TYPE: Fulltext; Abstract  
WORD COUNT: 3109 LINE COUNT: 00245

... end database) that can be used by any web browser, on any platform.

VI, one of the three tools included in the integrated IDE, has **database** connectivity and debugging tools, **programmable components**, web site management and publication abilities, a personal web server, content creation tools (including a stripped-down version of FrontPage 97), and a lot of...

**15/3,K/6 (Item 6 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)  
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01663812 SUPPLIER NUMBER: 15003458 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**CheckIt PRO: Analyst 1.0 for Windows. (TouchStone Software) (Software Review) (one of nine evaluations of diagnostic software in 'New Solutions for Old Conflicts') (Evaluation)**  
Chong, Herb  
Windows Sources, v2, n2, p130(2)  
Feb, 1994  
DOCUMENT TYPE: Evaluation ISSN: 1065-9641 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 736 LINE COUNT: 00065

...ABSTRACT: technology, collecting data and testing hardware under DOS and analyzing captured data in a Windows program. It is designed for systems management and provides a **database** of common adapter cards with **ROM BIOS**, IRQ, DMA channel and I/O port requirements. The package is especially useful for network administrators because it can collect data from multiple machines. The...

15/3,K/7 (Item 7 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
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01523055 SUPPLIER NUMBER: 12310204 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Industrial Engineering's 1992 automatic identification buyer's guide.**  
Industrial Engineering, v24, n6, pBG1(16)  
June, 1992  
ISSN: 0019-8234 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 2479 LINE COUNT: 00201

... replace some bar code applications as hardware becomes more affordable.

Smart cards

Smart cards are credit-card size plastic cards embedded with one or more **microchips**. The cards are **programmable** and carry large **databases**. They can also have their own keyboards, liquid crystal

15/3,K/8 (Item 8 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

01451409 SUPPLIER NUMBER: 11238045 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Roundup: our annual buyers' guide of major PBX players and their systems.**  
(short reviews of 45 products) (buyers guide)  
Teleconnect, v9, n9, p60(23)  
Sept, 1991  
DOCUMENT TYPE: buyers guide ISSN: 0740-9354 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 9613 LINE COUNT: 00790

... so you can add high-end features only when you truly crave them. Over the past year, Tadiran has boosted EKT expansion to 144 user-**programmable** buttons. Flash **EPROM** memory for nonvolatile **database** storage is another recent addition to the Coral reef

Tadiran

Emerald CCS

Min/Max stations: 4; 24

Min/Max trunks: 2; 16

Technology: Analog, Space...

15/3,K/9 (Item 9 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

01402131 SUPPLIER NUMBER: 09779647  
**Multuser programmable data bases . ( part 2) (Software Review)**  
(Overview of a comparison of three database products) (evaluation)  
Duncan, Judy; Banapour, Zoreh  
InfoWorld, v13, n2, p45(10)  
Jan 14, 1991  
DOCUMENT TYPE: evaluation ISSN: 0199-6649 LANGUAGE: ENGLISH  
RECORD TYPE: ABSTRACT

**Multuser programmable data bases . ( part 2) (Software Review)**  
(Overview of a comparison of three database products) (evaluation)

15/3,K/10 (Item 10 from file: 275)  
DIALOG(R)File 275:Gale Group Computer DB(TM)  
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01334879 SUPPLIER NUMBER: 09605069  
**Data handlers. (evaluation of five multi-user programmable databases ) (**  
**Hardware Review) (includes related articles on how products are tested**

and summary of test scores) (evaluation)

Duncan, Judy; Banapour, Zoreh; Petreley, Nicholas; Slovic, Linda

InfoWorld, v12, n46, p115(16)

Nov 12, 1990

DOCUMENT TYPE: evaluation

ISSN: 0199-6649

LANGUAGE: ENGLISH

RECORD TYPE: ABSTRACT

**Data handlers. (evaluation of five multi-user programmable databases ) ( Hardware Review) (includes related articles on how products are tested and summary of test scores) (evaluation)**

**15/3,K/11 (Item 11 from file: 275)**

DIALOG(R)File 275:Gale Group Computer DB(TM)

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01240746 SUPPLIER NUMBER: 06546205 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Programmable databases: dBASE and its challengers. (Project database 3)**

**(overview to evaluations of 43 programmable database managers) ( part 3 of four parts) (evaluation)**

Seymour, Jim

PC Magazine, v7, n9, p93(85)

May 17, 1988

DOCUMENT TYPE: evaluation

ISSN: 0888-8507

LANGUAGE: ENGLISH

RECORD TYPE: FULLTEXT; ABSTRACT

WORD COUNT: 3401 LINE COUNT: 00269

**Programmable databases: dBASE and its challengers. (Project database 3) (overview to evaluations of 43 programmable database managers) ( part 3 of four parts) (evaluation)**

**15/3,K/12 (Item 1 from file: 621)**

DIALOG(R)File 621:Gale Group New Prod.Annou.(R)

(c) 2002 The Gale Group. All rts. reserv.

01651486 Supplier Number: 48479171 (USE FORMAT 7 FOR FULLTEXT)

**Computer Horizons Adds Viasoft OnMark 2000 Technology to Its Distributed Desktop Offering.**

Business Wire, p05121393

May 12, 1998

Language: English Record Type: Fulltext

Document Type: Newswire; Trade

Word Count: 551

... 2000 Knowledge Base.

--Spreadsheet and Database Assessment - leverages OnMark 2000 suite to assess spreadsheets and desktop databases.

--Desktop Remediation - quick, cost-effective option to fix **hardware BIOS** and resolve spreadsheet, **database** and other software issues.

--Client/Server Software Remediation - leverages Viasoft's OnMark 2000 and Computer Horizons' Signature 2000 process and tools to assess, remediate, and...

**15/3,K/13 (Item 2 from file: 621)**

DIALOG(R)File 621:Gale Group New Prod.Annou.(R)

(c) 2002 The Gale Group. All rts. reserv..

01061501 Supplier Number: 40265724 (USE FORMAT 7 FOR FULLTEXT)

**HUGHES RADAR SYSTEMS GROUP AUTOMATES DESIGN CONTROL WITH SHERPA (TM) DMS**

News Release, p1

Jan 12, 1988

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 761

... net lists and build designs for printed circuit

boards (PCBs) and mechanical chassis assemblies for Hughes airborne radars. In particular, the system controls approximately 200 **databases** containing **programmable** read only memory ( **PROM** ) and **programmable** logic array (PLA) instructions for Hughes radar systems PCBs.

By the end of 1988, Sherpa DMS will control design documentation, wire harness designs, wire lists...

15/3,K/14 (Item 1 from file: 636)  
DIALOG(R)File 636:Gale Group Newsletter DB(TM)  
(c) 2002 The Gale Group. All rts. reserv.

05029454 Supplier Number: 76467884 (USE FORMAT 7 FOR FULLTEXT)  
**Warner Bros International Theatres calls in Foundation Systems to bring down curtain on systems 'nightmare'.**  
M2 Presswire, pNA  
July 11, 2001  
Language: English Record Type: Fulltext  
Document Type: Newswire; Trade  
Word Count: 589

... s London office to keep track of their time and carry out the complex billing process with minimum effort.

The solution will replace a custom- **programmed database** and will **part** -automate the time-consuming process of compiling timesheets, then billing for time spent.

15/3,K/15 (Item 1 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2002 The Gale Group. All rts. reserv.

06415862 Supplier Number: 54898702 (USE FORMAT 7 FOR FULLTEXT)  
**Bar Code.**  
Automatic I.D. News, v15, n7, ps6  
June, 1999  
Language: English Record Type: Fulltext  
Document Type: Magazine/Journal; Trade  
Word Count: 6125

... Smart Cards

The smart card uses a credit card-sized plastic card with one or more microchips embedded in it. Strictly speaking, smart cards are **programmable**, contain a microprocessor **chip** and carry a large **database**. The microprocessor manages the security entry to one or more application databases. Smart cards normally have a microprocessor, though some have memory only.

The term...

15/3,K/16 (Item 2 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2002 The Gale Group. All rts. reserv.

06382945 Supplier Number: 54779097 (USE FORMAT 7 FOR FULLTEXT)  
**TEST CENTER COMPARISON : Year-2000 assessment tools Time waits for no one. (Centennial International's Centennial 2000 Pro Enterprise Edition 2.1, Greenwich Mean Time-UTA's GMT Check 2000 Client Server 3.11, Symantec's Norton 2000 Enterprise Edition, WRQ's Express 2000 Software Manager) (Software Review) (Evaluation)**  
InfoWorld, v21, n22, pNA  
May 31, 1999  
Language: English Record Type: Fulltext Abstract  
Article Type: Evaluation  
Document Type: Magazine/Journal; Trade  
Word Count: 9493



... software is on each PC, but unfortunately, it does not specify year-2000 compliance.

Pros

(1) Inexpensive solution (2) Automated update of PC data to repository (3) Consolidated hardware / BIOS , applications, and data repository

Cons

(1) The data scanner does not provide ODBC access (2) Lacks a year-2000-compliant application database

Symantec solution Bottom Line: 6.7

Had capacity (3) No reporting tools from central repository for BIOS /applications

WRQ solution

Components Express 2000 Software Manager 4.0 Adapt/PC 2000 Bottom Line: 7.5

Express 2000 Software Manager 4.0 from WRQ does not limit its...

15/3,K/17 (Item 3 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2002 The Gale Group. All rts. reserv.

05429400 Supplier Number: 48234200 (USE FORMAT 7 FOR FULLTEXT)

**Cleaning up LANs for year 2000**

Dryden, Patrick

Computerworld, p053

Jan 19, 1998

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Tabloid; Trade

Word Count: 513

... H., tracks specific configuration information such as the type of processor and support chips. After lab tests identified noncompliant configurations, NetCensus spotted them in its database . A simple upgrade to BIOS chips can fix 120 of these PCs, Swarthout said, but modifying the rest will be too costly. So, labels and warning flags in NetCensus will have...

15/3,K/18 (Item 4 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2002 The Gale Group. All rts. reserv.

04903786 Supplier Number: 47211049 (USE FORMAT 7 FOR FULLTEXT)

**Smart Cards**

Automatic I.D. News, p16

March 15, 1997

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 237

(USE FORMAT 7 FOR FULLTEXT)

TEXT:

The smart card uses a credit card-sized plastic card with one or more microchips embedded in it. Strictly speaking, smart cards are programmable , contain a microprocessor chip and carry a large database . The microprocessor manages the security entry to one or more application databases. Smart cards normally have a microprocessor, though some have memory only.

15/3,K/19 (Item 5 from file: 16)  
DIALOG(R)File 16:Gale Group PROMT(R)  
(c) 2002 The Gale Group. All rts. reserv.

04029753 Supplier Number: 45858940 (USE FORMAT 7 FOR FULLTEXT)

**Consolidating Your File Servers: A Look Back**

Network Computing, p110

Oct 15, 1995

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 2737

... from server hardware vendors and Novell to make network managers' lives easier. From the hardware vendors, I would like a version control and change management **database** facility that tracks **microcode** and **hardware** driver changes. Compaq's Insight Manager does track microcode updates and that is a very progressive start. Well done! From Novell, I would like to ...

15/3,K/20 (Item 6 from file: 16)

DIALOG(R)File 16:Gale Group PROMT(R)

(c) 2002 The Gale Group. All rts. reserv.

01116411 Supplier Number: 41256505 (USE FORMAT 7 FOR FULLTEXT)

**CD-ROMs Are Coming**

VARbusiness, p76

April, 1990

Language: English Record Type: Fulltext

Document Type: Magazine/Journal; Trade

Word Count: 880

... jump into the market. The Scotts Valley, Calif., company's software offerings include a program called Desktop CD Publisher that helps VARs design a CD- ROM **database** . It is **programmed** to work with retrieval software programs already on the market.

Michael Prussian, executive vice president, thinks it is a great opportunity for VARs to build...

15/3,K/21 (Item 1 from file: 160)

DIALOG(R)File 160:Gale Group PROMT(R)

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01956912

**CAD Files Controlled**

Computer Decisions June, 1988 p. 40A

ISSN: 0010-4558

Hughes Aircraft's Radar Systems Group is using Sherpa's (San Jose, CA) Design Management System to manage 200 **databases** containing designs for **programmable** ROM and logic array instructions used on printed circuit boards for Hughes's on-board radar systems for the F-14, F-15, and F/A...

15/3,K/22 (Item 1 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB

(c)2002 The Gale Group. All rts. reserv.

09918836 SUPPLIER NUMBER: 19992307 (USE FORMAT 7 OR 9 FOR FULL TEXT)

**Year 2000 Problem: Strategies and Solutions from the Fortune 100. (book reviews)**

Scheier, Robert L.

Computerworld, v31, n45, p71(1)

Nov 10, 1997

DOCUMENT TYPE: Review ISSN: 0010-4841 LANGUAGE: English

RECORD TYPE: Fulltext

WORD COUNT: 217 LINE COUNT: 00019

... legal threats posed by the year 2000 problem. There are also useful case studies, as well as a CD-ROM with a business risk-assessment **database** , utilities to check PC **BIOS** **chips** for year 2000 flaws and sample survey letters to ask vendors about the year 2000 status of their products.

Bottom line: This can be a...

15/3,K/23 (Item 2 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

09742124 SUPPLIER NUMBER: 19744803 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Software development suite is less than sum of its parts. (Microsoft's Visual Studio 97 Enterprise Edition) (Software Review) (Evaluation)**  
Graves, Steve  
Government Computer News, v16, n26, p1(2)  
Sep 1, 1997  
DOCUMENT TYPE: Evaluation ISSN: 0738-4300 LANGUAGE: English  
RECORD TYPE: Fulltext; Abstract  
WORD COUNT: 1134 LINE COUNT: 00096

... Database Connectivity-compliant databases through Data Access Objects 3.5, Remote Data Objects 2.0 or Active Data Objects. Tools such as DataView, Query Designer, **Database Designer**, **programmable access components** and wizards make it easier to write database-driven Web applications.  
Shared environments  
Visual J++, Visual C++ and Visual InterDev share a common environment and...

15/3,K/24 (Item 3 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

06722330 SUPPLIER NUMBER: 14445840 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Alpha Four handles dBase without programming. (Alpha Software's Alpha Four 3.0 nonprogrammable DOS database) (Software Review) (Evaluation)**  
Frayer, Alan  
InfoWorld, v15, n41, p84(2)  
Oct 11, 1993  
DOCUMENT TYPE: Evaluation ISSN: 0199-6649 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 2121 LINE COUNT: 00163

... of RAM and 110MB hard drives as client workstations. The network was Novell NetWare 3.11.  
Features:  
Alpha Four applications can rival applications built with **programmable databases**, due in part to its relational capabilities, variety of functions, and scripting capability. As many as 10 databases can be grouped to form a set, and applications can...

15/3,K/25 (Item 4 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

06223328 SUPPLIER NUMBER: 14330544 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Tool set joins synthesis with simulation for reliable ASIC design. (application specific integrated circuit) (Mentor Graphics Corp.'s Top Down Design-Solver) (Product Announcement)**  
Napier, John C.  
EDN, v37, n24, p72(1)  
Nov 26, 1992  
DOCUMENT TYPE: Product Announcement ISSN: 0012-7515 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 450 LINE COUNT: 00036

... calculator, one VHDL (VHSIC-hardware-description-language) compiler for both synthesis and simulation, one simulator for both behavioral and gate-level work, and a common **database** for all tools.  
Five **ASIC** and FPGA (field- **programmable** -gate-array) vendors

(Fujitsu, LSI Logic, Mitsubishi, VLSI Technology, and Xilinx) will deliver design kits during the fourth quarter of 1992 for both Sun and...

15/3,K/26 (Item 5 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

06216167 SUPPLIER NUMBER: 13813145 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Rebel with a cause: Steve Jobs' NeXT challenge. (entrepreneur Steve Jobs) (includes related article) (Cover Story)**  
Burke, Gibbons  
Futures (Cedar Falls, Iowa), v21, n12, p78(3)  
Oct, 1992  
DOCUMENT TYPE: Cover Story ISSN: 0746-2468 LANGUAGE: ENGLISH  
RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 2184 LINE COUNT: 00169

... 0 of NeXTSTEP, soon to be released, also has many features NeXT hopes will broaden its appeal. One is DB Kit, a set of pre- **programmed database** -handling software **components** or objects enabling users to access databases. Adapters for the widely used Oracle and Sybase SQL databases are included, and NeXT feels third-party vendors...

15/3,K/27 (Item 6 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
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06092984 SUPPLIER NUMBER: 12504139 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Allen-Bradley unveils new CIM plant, strategy; automated conveying, self-directed work teams featured in advanced circuit-board plant. (computer-integrated manufacturing)**  
Modern Materials Handling, v47, n8, p12(2)  
July, 1992  
ISSN: 0026-8038 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT  
WORD COUNT: 610 LINE COUNT: 00053

... are produced with one to three new board designs added weekly. Once the engineering file on a board has been electronically downloaded into the EMS1 **data base** and the assembly equipment automatically **programmed** for that design, **boards** can be introduced into the production process in a matter of minutes. A group of 15 employees, working in self-directed teams, is involved in...

15/3,K/28 (Item 7 from file: 148)  
DIALOG(R)File 148:Gale Group Trade & Industry DB  
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05851924 SUPPLIER NUMBER: 12136290 (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**COMPETITIVENESS AMERICAN STYLE -- A WORLD-LEADING MANUFACTURING CENTER DEBUTS IN MILWAUKEE**  
PR Newswire, 0514A0085  
May 14, 1992  
LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT  
WORD COUNT: 1004 LINE COUNT: 00083

... are used to improve the process further on up the line. Once the engineering file on a board has been electronically downloaded into the EMS1 **data base** and the assembly equipment automatically **programmed** for that design, **boards** can be introduced into the production process in a matter of minutes. A group of 15 men and women, who work in self-directed teams...

15/3,K/29 (Item 8 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB  
(c)2002 The Gale Group. All rts. reserv.

04524149      SUPPLIER NUMBER: 08546461      (USE FORMAT 7 OR 9 FOR FULL TEXT)  
**Project management: pulling the data together.**  
Deitz, Daniel  
Mechanical Engineering-CIME, v112, n2, p56(2)  
Feb, 1990  
ISSN: 0025-6501      LANGUAGE: ENGLISH      RECORD TYPE: FULLTEXT  
WORD COUNT:    1071      LINE COUNT:    00083

...      communicated to design team members working on related files.  
Tracking Data  
When it was installed about three years ago, Hughes used the system to control **data bases** containing **programmable** read only memory ( **PROM** ) and **programmable** logic array (PLA) instructions for the printed wiring boards used in RSG's airborne radar systems. Today, the system helps manage the spectrum of engineering...

15/3,K/30      (Item 1 from file: 624)  
DIALOG(R)File 624:McGraw-Hill Publications  
(c) 2002 McGraw-Hill Co. Inc. All rts. reserv.

0245996  
**Lockheed Develops Pilot Aid Under NASA Contract**  
Aviation Daily October 5, 1990; Pg 45; Vol. 302, No. 5  
Journal Code:    AD      ISSN: 0193-4597  
Word Count:      151      \*Full text available in Formats 5, 7 and 9\*

TEXT:  
... a rationale. It also can assess an air traffic controller's recommendations or a pilot decision, but the pilot remains the final authority." The on- **board database** can be **programmed** to suit the needs of the customer and may include information about route structures and schedules and other carrier-specific policy.

15/3,K/31      (Item 1 from file: 553)  
DIALOG(R)File 553:Wilson Bus. Abs. FullText  
(c) 2002 The HW Wilson Co. All rts. reserv.

03851503      H.W. WILSON RECORD NUMBER: BWBA98101503      (USE FORMAT 7 FOR FULLTEXT)  
**Safety net.**  
AUGMENTED TITLE: technology buyer's guide; with table of product ratings  
Fortune (Fortune) special issue (Winter '99) p. 211-14+  
LANGUAGE:    English  
WORD COUNT:    4569

(USE FORMAT 7 FOR FULLTEXT)

TEXT:  
...      technology that Network Associates built into its Y2K product for large networks, this simpler, streamlined offering looks at individual desktops, reformatting two-digit spreadsheet and **database** fields, fixing **hardware** and **BIOS** problems, and scanning for viruses. Guarding against the Y2K problem may not be foolproof, so just in case, 2000 Toolbox includes several additional tools: an...

15/3,K/32      (Item 2 from file: 553)  
DIALOG(R)File 553:Wilson Bus. Abs. FullText  
(c) 2002 The HW Wilson Co. All rts. reserv.

03552746      H.W. WILSON RECORD NUMBER: BWBA97052746  
**Marketing services versus marketing efforts.**  
AUGMENTED TITLE: interactive voice response strategies

Culpepper, Kenneth M  
Direct Marketing (Direct Mark) v. 59 (Apr. '97) p. 22-3+  
LANGUAGE: English

...ABSTRACT: initiate multiple interactive marketing endeavors by linking communications and database marketing technologies. For retailers to employ IVR technology effectively, it is necessary to interface three **programmable components** : telephone system, computer system (customer **database** information), and point-of-sale system (any source-data strategy software that collects transactional information that enables the customer to be identified). The benefits that...

15/3,K/33 (Item 1 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
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01851390 05-02382

**Card technologies**

Anonymous

Automatic I.D. News v15n6 (1999 Buyer's Guide Supplement) PP: 10-12 1999  
ISSN: 0890-9768 JRNL CODE: AIN  
WORD COUNT: 986

...TEXT: Smart Cards

The smart card uses a credit card-sized plastic card with one or more microchips embedded in it. Strictly speaking, smart cards are **programmable** , contain a microprocessor **chip** and carry a large **database** . The microprocessor manages the security entry to one or more application databases. Smart cards normally have a microprocessor, though some have memory only.

The term...

15/3,K/34 (Item 2 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
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01673368 03-24358

**Symantec addresses Y2K compliance**

Cornetto, Jon

InfoWorld v20n29 PP: 12 Jul 20, 1998  
ISSN: 0199-6649 JRNL CODE: IFW  
WORD COUNT: 346

...TEXT: an all-in-one, year-2000 compliance toolkit for the desktop and PC networks, at the end of this month.

The toolkit will test and fix **hardware BIOS** issues, scan **databases** , and analyze applications themselves for compliance.

Like many of its competitors, Norton 2000 checks databases to see if there are problems related to dates, but...

15/3,K/35 (Item 3 from file: 15)  
DIALOG(R)File 15:ABI/Inform(R)  
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01528086 01-79074

**Year 2000 capacity and performance issues**

Friedman, Ellen M; Rosenberg, Jerry L

Capacity Management Review v25n9 PP: 1, 14+ Sep 1997  
ISSN: 1049-2194 JRNL CODE: PPR  
WORD COUNT: 6489

...TEXT: and performance analysts.

The scope of the Year 2000 challenge spans the entire Information Technology industry. A data mismatch can exist in any level of **hardware** or software, from **microcode** to applications, in files and **databases**, and can be present on any platform.

There are some general misunderstandings and myths regarding the Year 2000 challenge. These include:

(1) This is a...

**15/3,K/36 (Item 4 from file: 15)**  
DIALOG(R)File 15:ABI/Inform(R)  
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00582273 91-56620  
**Front-to-Back Tool Integration**  
Waddell, Pete  
Printed Circuit Design v8n11 PP: 10-14 Nov 1991  
ISSN: 1047-5567 JRNL CODE: PCC

...ABSTRACT: person uses, is involved with tool integration. Integration can be defined as the ability to transfer data from one process-specific software tool to another. **Database** capture for integrated circuits ( IC ), **programmable** devices, **boards**, and systems begins with computer-aided engineering (CAE) tools. The translator segment of the industry is receiving much attention. Vendors, particularly value-added resellers, have ...

**15/3,K/37 (Item 5 from file: 15)**  
DIALOG(R)File 15:ABI/Inform(R)  
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00537292 91-11636  
**Product Analysis - Paradox: The SQL (Part 1)**  
Petreley, Nicholas  
InfoWorld v13n7 PP: 53-57 Feb 18, 1991  
ISSN: 0199-6649 JRNL CODE: IFW  
WORD COUNT: 4213

...TEXT: how the two products work as a team.

To test Paradox SQL Link we ran the same multiuser benchmark trials we used in our two- **part** multiuser **programmable database** product comparison (November 12, 1990, Page 115, and January 14, Page 45), with a slightly different test bed to accommodate the needs of SQL Server...

**15/3,K/38 (Item 1 from file: 9)**  
DIALOG(R)File 9:Business & Industry(R)  
(c) 2002 Resp. DB Svcs. All rts. reserv.

03135228 (USE FORMAT 7 OR 9 FOR FULLTEXT)  
**Component data for OEMs**  
(SiliconExpert Technologies Inc will market its component database and parametric search capabilities to device vendors and distributors, eventually targeting large OEMs)  
Electronic Engineering Times, p 64  
May 07, 2001  
DOCUMENT TYPE: Journal ISSN: 0192-1541 (United States)  
LANGUAGE: English RECORD TYPE: Fulltext  
WORD COUNT: 428

(USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:

...the ability to go in and search components, and they can set up communities where they share information with each other."

SiliconExpert has developed a **database** of some 300,000 **components**, including microprocessors, microcontrollers, **programmable** -logic devices, analog and communications chips. It promises to have 800,000 active components in the database by this fall.  
Other active component databases exist...

15/3,K/39 (Item 1 from file: 647)  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

01236173 CMP ACCESSION NUMBER: EET20010507S0048  
**Component data for OEMs**  
Richard Goering  
ELECTRONIC ENGINEERING TIMES, 2001, n 1165, PG64  
PUBLICATION DATE: 010507  
JOURNAL CODE: EET LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: DESIGN AUTOMATION  
WORD COUNT: 446

... the ability to go in and search components, and they can set up communities where they share information with each other."

SiliconExpert has developed a **database** of some 300,000 **components**, including microprocessors, microcontrollers, **programmable** -logic devices, analog and communications chips. It promises to have 800,000 active components in the database by this fall.

Other active component databases exist...

15/3,K/40 (Item 2 from file: 647)  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

01077482 CMP ACCESSION NUMBER: WIN19960101S0153  
**Enhancing Your Hard Drive - You may need more stuff to make it all work.**  
(Winlab Product Comparisons)  
John Gartner  
WINDOWS MAGAZINE, 1996, n 701, PG261  
PUBLICATION DATE: 960101  
JOURNAL CODE: WIN LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: Winlab Product Comparisons  
WORD COUNT: 408

... PIO Modes 3 and 4.

The 528MB limitation (see Power Windows, February 1995) is a result of the way the operating system (DOS) and the **hardware** (through the system **BIOS**) store and communicate **data** based on the sector, head and cylinder location. New systems that include an updated BIOS and EIDE drive controllers no longer have this limitation. To work...

15/3,K/41 (Item 3 from file: 647)  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

01068623 CMP ACCESSION NUMBER: NWC19951015S0041  
**Consolidating Your File Servers: A Look Back** (Infrastructure)  
Shane R. Yamkowy  
NETWORK COMPUTING, 1995, n 613, PG110  
PUBLICATION DATE: 951015



JOURNAL CODE: NWC      LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: Workshops  
WORD COUNT: 2713

... from server hardware vendors and Novell to make network managers' lives easier. From the hardware vendors, I would like a version control and change management **database** facility that tracks **microcode** and **hardware** driver changes. Compaq's Insight Manager does track microcode updates and that is a very progressive start. Well done! From Novell, I would like to...

**15/3,K/42      (Item 4 from file: 647)**  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

00628830    CMP ACCESSION NUMBER: EET19890515S1602  
**Take note: a new computer category**  
ELECTRONIC ENGINEERING TIMES, 1989, n 538, 22  
PUBLICATION DATE: 890515  
JOURNAL CODE: EET      LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: NEWS  
WORD COUNT: 717

... and cannot be connected to external units.  
. Sharp's Brain, a popular notebook computer, comes loaded with word-processing software, schedule program and address-book **database** -stored in a 1-Mbit **CMOS** mask **ROM**. It can be fitted with an optional floppy disk and tied to an optional thermal printer. By buying some (expensive) printer-driver software, it can...

**15/3,K/43      (Item 5 from file: 647)**  
DIALOG(R)File 647:CMP Computer Fulltext  
(c) 2002 CMP Media, LLC. All rts. reserv.

00574652    CMP ACCESSION NUMBER: VAR19900305S2476  
**CD-ROMs Are Coming - Wise VARs Will Let Them In**  
VARBUSINESS, 1990, n 46  
PUBLICATION DATE: 900305  
JOURNAL CODE: VAR      LANGUAGE: English  
RECORD TYPE: Fulltext  
SECTION HEADING: MARKET REPORT  
WORD COUNT: 890

... jump into the market. The Scotts Valley, Calif., company's software offerings include a program called Desktop CD Publisher that helps VARs design a **CD-ROM database**. It is **programmed** to work with retrieval software programs already on the market.

Michael Prussian, executive vice president, thinks it is a great opportunity for VARs to build...

**15/3,K/44      (Item 1 from file: 674)**  
DIALOG(R)File 674:Computer News Fulltext  
(c) 2002 IDG Communications. All rts. reserv.

063084  
**Swatting the Y2K bug**  
**Managing, Resources: IS manager's bookshelf**  
Byline: Robert L. Scheier and Rick Saia  
Journal: Computerworld      Page Number: 71  
Publication Date: November 10, 1997  
Word Count: 491      Line Count: 43

Text:

'... legal threats posed by the year 2000 problem. There are also useful case studies, as well as a CD-ROM with a business risk-assessment **database**, utilities to check PC **BIOS** **chips** for year 2000 flaws and sample survey letters to ask vendors about the year 2000 status of their products. Bottom line: This can be a...

15/3,K/45 (Item 2 from file: 674)  
DIALOG(R) File 674:Computer News Fulltext  
(c) 2002 IDG Communications. All rts. reserv.

034364

**MAXIMIZE**

Buy a new mainframe? Probably not. Squeeze what you've got for all it's worth? Very Likely. Tools for automation, performance and optimization are selling madly, as users find no letup in mainframe use.

Byline: Susan Gannon Middleton; Middleton is an analyst at International Data Corp. in Framingham, Mass.

Journal: Computerworld Page Number: 93

Publication Date: November 29, 1993

Word Count: 819 Line Count: 59

Text:

...in need of a large single-image machine.

A highly parallel query system --- dubbed PQS --- for DB2. This is a read-only, channel-attached DB2 **database** machine built from **CMOS** **chips**

The PQS is the first of IBM's parallel CMOS offerings, but it will not replace the mainframe. Rather, it lets customers process DB2 queries...

File 8: Ei Compendex(R) 1970-2002/Dec W1  
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File 202: Information Science Abs. 1966-2002/Oct 29  
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File 2: INSPEC 1969-2002/Dec W2  
(c) 2002 Institution of Electrical Engineers

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(c) 2002 Info. Today Inc.

File 94: JICST-EPlus 1985-2002/Oct W1  
(c) 2002 Japan Science and Tech Corp(JST)

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(c) 2002 The Gale Group

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File 434: SciSearch(R) Cited Ref Sci 1974-1989/Dec  
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File 34: SciSearch(R) Cited Ref Sci 1990-2002/Dec W2  
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File 99: Wilson Appl. Sci & Tech Abs 1983-2002/Oct  
(c) 2002 The HW Wilson Co.

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(c) 2002 The Gale Group

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(c) 2002 FIZ TECHNIK

File 62: SPIN(R) 1975-2002/Nov W1  
(c) 2002 American Institute of Physics

File 438: Library Literature 1984-2002/Oct  
(c) 2002 The HW Wilson Co

Set	Items	Description
S1	5004	FIRMWARE OR FIRM()WARE
S2	8	S1(5N) (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S3	6785	EMBEDDED(1W) (CHIP? ? OR MICROCHIP? ? OR PART? ? OR ELEMENT? ? OR MODULE? ? OR HARDWARE OR SOFTWARE OR PROCESSOR? ? OR MICROPROCESSOR? ?)
S4	366360	BIOS OR CMOS OR MICROCODE OR BOOTSTRAP OR PROGRAMMABLE OR - PROGRAMMED
S5	46517	S4(3N) (CHIP? ? OR CHIPSET? ? OR MICROCHIP? ? OR BOARD? ? OR HARDWARE OR COMPONENT? ? OR PART? ? OR ROM? ? OR PROM? ? OR - EPROM? ? OR EEPROM? ? OR SEMICONDUCT??? OR SEMI(W)CONDUCT??? - OR DEVICE? ?)
S6	8	RD S2 (unique items)
S7	12	S3(3N) (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S8	9	RD (unique items)
S9	31	S5(3N) (DATABASE? ? OR DATA()BASE? ? OR REPOSITOR??? OR ARC-HIV???)
S10	26	RD (unique items)

6/5/1 (Item 1 from file: 202)  
DIALOG(R)File 202:Information Science Abs.  
(c) Information Today, Inc. All rts. reserv.

0902017

**Some problems in associative processor applications to data base management.**

Book Title: In American Federation Of Information Processing Societies. Afips Conference Proceedings, Volume 43. 1974 National Computer Conferenc And Exposition, May 6-10, 1974, Chicago. P. 1-5. 1 Illus. 25 Ref. See Isa 74-1635/y.

Author(s): Berra, P Bruce  
Corporate Source: Syracuse University, New York.  
Publication Date: 1974  
Language: English  
Document Type: Book Chapter  
Record Type: Abstract  
Journal Announcement: 0900

Background is given on the application of associative devices to data base management. This is followed by a review of existing literature in the field. Searching, a most important capability of an associative device, is then considered. It is shown that several data base management functions such as retrieval and update have searching at their core and therefore are well suited to these devices. Furthermore, due to the simplicity of the associative storage structure, increased performance can be obtained in some of the other functions of data base management. It is concluded that these devices have a place in the solution of data base management problems but represent only a step on the way to more sophisticated hardware/software/ **firmware** devices designed especially for **data base** management problems.

Classification Codes and Description: 5.11 (Searching and Retrieval)  
Main Heading: Information Processing and Control

6/5/2 (Item 1 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2002 Institution of Electrical Engineers. All rts. reserv.

03299110 INSPEC Abstract Number: C89014884

**Title: Database machines: the least-cost route?**

Author(s): Gregory, E.  
Journal: Datamation vol.34, no.21 p.85-6, 90  
Publication Date: 1 Nov. 1988 Country of Publication: USA  
CODEN: DTMNAT ISSN: 0011-6963  
Language: English Document Type: Journal Paper (JP)  
Treatment: Practical (P); Product Review (R)

Abstract: Relational database machines may never completely supersede host-resident hierarchical or relational databases, but users and analysts say the affordable muscle of these dedicated data servers delivers new power to those who want to make strategic use of information. Britton Lee Inc. and Teradata Inc., are the two major suppliers of these **database** machines, the hardware- **firmware** -software successors to resource-hungry general purpose computer databases. Britton Lee machines-like Teradata systems and any software-based relational database that offers portability-work with ANSI-standard SQI tools, including PC/SQL Link, and fourth generation languages, such as Focus. (0 Refs)

Subfile: C

Descriptors: computer evaluation; relational databases; software portability; special purpose computers

Identifiers: relational database machines; hardware-optimized database solutions; least-cost route; dedicated data servers; Teradata; major suppliers; hardware-firmware-software successors; software-based relational database; portability; ANSI-standard SQI tools; PC/SQL Link; fourth generation languages; Focus

Class Codes: C6160D (Relational DBMS); C5430 (Microcomputers)

6/5/3 (Item 2 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2002 Institution of Electrical Engineers. All rts. reserv.

02476674 INSPEC Abstract Number: C85033158

**Title: MSX: A Bit of a Hit (Sony Hit Bit)**

Author(s): Newman, J.

Journal: Micro Decision no.43 p.93-4

Publication Date: May 1985 Country of Publication: UK

CODEN: MIDEEDG ISSN: 0261-5142

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P); Product Review (R)

Abstract: The Sony Hit Bit home computer, which conforms to the MSX standard, costs about Pounds 300. Its software and peripherals are fully interchangeable with those of other MSX computers. This article reviews the machine and its integral Personal Data Bank **firmware**; this program is a simple **database** which holds names and addresses, memos and dates for a diary. (0 Refs)

Subfile: C

Descriptors: computer evaluation; database management systems; firmware; microcomputers

Identifiers: personal database; ROM software; Sony Hit Bit; home computer; MSX standard; Pounds 300; MSX computers; Personal Data Bank firmware

Class Codes: C5140 (Firmware); C5430 (Microcomputers); C6160 (Database management systems (DBMS))

6/5/4 (Item 3 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2002 Institution of Electrical Engineers. All rts. reserv.

02406797 INSPEC Abstract Number: C85016003

**Title: Let's talk DBMS**

Author(s): Snyders, J.

Journal: Infosystems vol.31, no.12 p.36-44

Publication Date: Dec. 1984 Country of Publication: USA

CODEN: IFSYAF ISSN: 0364-5533

Language: English Document Type: Journal Paper (JP)

Treatment: General, Review (G)

Abstract: You cannot discuss database management systems (DBMS) without a good working definition of a database. A database is a representation or model of the things, descriptions and relationships that define the business and the environment in which that business operates. It is the raw material that supports the production of information. The database therefore becomes a resource that belongs to the entire company as opposed to any one specific functional area or application. A DBMS integrates data files into a database and provides different views to different users. Therefore, the software, hardware, **firmware** and procedures that manage that **database** make up the DBMS. (0 Refs)

Subfile: C

Descriptors: administrative data processing; database management systems

Identifiers: information production; database management systems; business; DBMS; data files

Class Codes: C6160 (Database management systems (DBMS)); C7100 (Business and administration)

6/5/5 (Item 4 from file: 2)  
DIALOG(R)File 2:INSPEC  
(c) 2002 Institution of Electrical Engineers. All rts. reserv.

00809600 INSPEC Abstract Number: C75021817

**Title: The end of programming**

Author(s): Berger, P.

Journal: Informatique et Gestion vol.67 p.57-61

Publication Date: May 1975 Country of Publication: France

CODEN: IFQGAJ ISSN: 0020-062X

Language: French Document Type: Journal Paper (JP)

Treatment: General, Review (G)

Abstract: In five or ten years it is likely that programs will no longer be written. Not because of the use of 'super-languages' or natural language translators, but because of the disappearance of a method of communication ill-suited both to the user and to the machines of today. With modern developments in for example **data - base firmware**, the program, as a long sequence of instructions addressed to an empty machine, is an anachronism. The basic mechanism for the change already exists; the problem is now the maintenance and development of existing techniques and systems.

Subfile: C

Descriptors: programming

Identifiers: programming; natural language translators; super languages;  
**data base firmware**

Class Codes: C6110 (Systems analysis and programming)

6/5/6 (Item 1 from file: 6)

DIALOG(R)File 6:NTIS

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1794930 NTIS Accession Number: N94-22547/1

**VLBA Correlator: Real-Time in the Distributed Era**

(Abstract Only)

Wells, D. C.

National Radio Astronomy Observatory, Green Bank, WV.

Corp. Source Codes: 888888888; NK868302

Sponsor: National Aeronautics and Space Administration, Washington, DC.

1992 1p

Languages: English

Journal Announcement: GRAI9411; STAR3205

In NASA, Washington, Second Annual Conference on Astronomical Data Analysis Software and Systems. Abstracts p 116.

NTIS Prices: (Order as N94-22438/3, PC A07/MF A02)

Country of Publication: United States

The correlator is the signal processing engine of the Very Long Baseline Array (VLBA). Radio signals are recorded on special wideband (128 Mb/s) digital recorders at the 10 telescopes, with sampling times controlled by hydrogen maser clocks. The magnetic tapes are shipped to the Array Operations Center in Socorro, New Mexico, where they are played back simultaneously into the correlator. Real-time software and firmware controls the playback drives to achieve synchronization, compute models of the wavefront delay, control the numerous modules of the correlator, and record FITS files of the fringe visibilities at the back-end of the correlator. In addition to the more than 3000 custom VLSI chips which handle the massive data flow of the signal processing, the correlator contains a total of more than 100 programmable computers, 8-, 16- and 32-bit CPUs. Code is downloaded into front-end CPU's dependent on operating mode. Low-level code is assembly language, high-level code is C running under a RT OS. We use VxWorks on Motorola MVME147 CPU's. Code development is on a complex of SPARC workstations connected to the RT CPU's by Ethernet. The overall management of the correlation process is dependent on a database management system. We use Ingres running on a Sparcstation-2. We transfer logging information from the database of the VLBA Monitor and Control System to our database using Ingres/NET. Job scripts are computed and are transferred to the real-time computers using NFS, and correlation job execution logs and status flow back by the route. Operator status and control displays use windows on workstations, interfaced to the real-time processes by network protocols. The extensive network protocol support provided by VxWorks is invaluable. The VLBA Correlator's dependence on network protocols is an example of the radical transformation of the real-time world over the past five years. Real-time is becoming more like conventional computing. Paradoxically, 'conventional' computing is also adopting practices from the real-time world: semaphores, shared memory, light-weight threads, and concurrency. This appears to be a convergence of thinking.

Descriptors: Correlators; \*Data recording; \*Radio signals; \*Signal processing; Chips; Digital systems; Real time operation; Telescopes; Very

large scale integration; Assembly language; Broadband; Computer networks;  
**Data base** management systems; **Data bases** ; **Firmware** ; Hydrogen  
masers; Information flow; Magnetic tapes; Wave fronts

Identifiers: NTISNASA

Section Headings: 54C (Astronomy and Astrophysics--Astrophysics)

6/5/7 (Item 2 from file: 6)

DIALOG(R) File 6:NTIS

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0569564 NTIS Accession Number: AD-912 632/7/XAB

**Analysis of Hardware and Software Storage and Retrieval Functions**  
(Technical document)

Shen, J. T.

Naval Electronics Lab Center San Diego Calif

Corp. Source Codes: 403940

Report No.: NELC-TD-259

10 Jul 73 52p

Journal Announcement: GRAI7622

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NTIS Prices: PC A04/MF A01

Contract No.: NELC-Z401

This report presents the results of an analysis of the information  
storage and retrieval (ISAR) functions of several Navy ISAR systems. The  
analysis was performed to provide further support to the Advanced Software  
Technology Division for Project 2175. In brief, the guiding philosophy of  
Project 2175 is to determine the feasibility of mechanizing storage and  
retrieval functions in modular building blocks by combinations of hardware,  
firmware and software. Recent studies have borne witness to the growing  
concern with a number of problems incident to the acquisition and operation  
of computer-based storage and retrieval systems. In essence, these problems  
include the following: (1)increasing system development costs; (2)  
extensive time required to acquire new systems; (3)the variety of  
incompatible systems and components; (4)evolving requirements precipitated  
by the growing complexity of modern warfare; (5)increasing costs of  
maintaining multiple systems and components; (6)limited reliability of  
system software and (7)the trend toward reduced funding resources. Of all  
these problems, the software aspect is one of the most vexing. In  
particular there is convincing evidence to indicate that software is the  
most expensive, least reliable element of contemporary computer-based  
systems.

Descriptors: \*Information retrieval; \*Data storage systems; \*Computer  
programs; Memory devices; Digital computers; Data processing; Costs;  
Electromagnetic compatibility; Maintenance; Reliability(Electronics); Data;  
Algorithms; Computer logic; Sequences(Mathematics); Statistical analysis;  
Inequalities; Disks; Simulation; Compilers

Identifiers: An/uyk-7; Computer hardware; Computer files; Computerized  
simulation; Data acquisition; **Data bases** ; Delay time; File structure;  
**Firmware** ; Magnetic disk storage; Magnetic drum st  
control systems; Navy; Shipboard; Tactical intel  
processing; NTISDODXD

Section Headings: 88B (Library and Information Sci  
Systems); 62A (Computers, Control, and Information  
Hardware)

Have Shirelle  
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6/5/8 (Item 1 from file: 434)

DIALOG(R) File 434:SciSearch(R) Cited Ref Sci

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07405295 Genuine Article#: D0634 Number of References: 0

**Title: USLS REPOSITORY FOR FIRMWARE ENGINEERING MATERIALS**

Author(s): SHRIVER BD

Corporate Source: IBM CORP, THOMAS J WATSON RES CTR, POB 218, HO-B04A/YORKTOWN

HTS//NY/10598

Journal: IEEE SOFTWARE, 1986, V3, N4, P3

Language: ENGLISH Document Type: EDITORIAL

Geographic Location: USA

Subfile: CC ENGI--Current Contents, Engineering, Technology & Applied  
Sciences



8/5/1 (Item 1 from file: 8)  
DIALOG(R)File 8:Ei Compendex(R)  
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06079826 E.I. No: EIP02276999031

**Title:** An on-line repository for embedded software  
**Author:** Yen, I.-Ling; Khan, Latifur; Prabhakaran, Balakrishnan; Bastani, Farokh B.; Linn, John  
**Corporate Source:** Dept. of Computer Science Univ. of Texas at Dallas, Dallas, TX, United States  
**Conference Title:** 13th International Conference on Tools with Artificial Intelligence  
**Conference Location:** Dallas, TX, United States **Conference Date:** 20011107-20011109  
**Sponsor:** IEEE Computer Society  
**E.I. Conference No.:** 59191  
**Source:** Proceedings of the International Conference on Tools with Artificial Intelligence 2001. p 314-321  
**Publication Year:** 2001  
**CODEN:** PCTIFX **ISSN:** 1063-6730  
**Language:** English  
**Document Type:** CA; (Conference Article) **Treatment:** A; (Applications); T; (Theoretical); X; (Experimental)  
**Journal Announcement:** 0207W1

**Abstract:** The use of off-the-shelf components (COTS) can significantly reduce the time and cost of developing large-scale software systems. However, there are some difficult problems with the component-based approach. First, the developers have to be able to effectively retrieve components. This requires the developers to have an extensive knowledge of available components and how to retrieve them. After identifying the components, the developers also face a steep learning curve to master the use of these components. We are developing an On-line **Repository for Embedded Software** (ORFS) to facilitate component management and retrieval. In this paper, we address the issues of designing software repository systems to assist users in obtaining appropriate components and learning to understand and use the components efficiently. We use an ontology to construct an abstract view of the organization of the components in ORES. The ontology structure facilitates repository browsing and effective search. We also develop a set of tools to assist with component comprehension, including a tutorial manager and a component explorer. 14 Refs.

**Descriptors:** \*Software engineering; Embedded systems; Online systems; Information retrieval systems; User interfaces; Learning systems; Requirements engineering; Knowledge based systems; Management information systems

**Identifiers:** Online repository; Embedded software; Off-the-shelf components; Component management; Component retrieval

**Classification Codes:**

723.4.1 (Expert Systems)  
723.1 (Computer Programming); 722.4 (Digital Computers & Systems);  
722.2 (Computer Peripheral Equipment); 723.4 (Artificial Intelligence);  
723.5 (Computer Applications)  
723 (Computer Software, Data Handling & Applications); 722 (Computer Hardware)  
.72 (COMPUTERS & DATA PROCESSING)

8/5/2 (Item 2 from file: 8)  
DIALOG(R)File 8:Ei Compendex(R)  
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05978149 E.I. No: EIP01556803291

**Title:** High-quality customizable embedded software from COTS components  
**Author:** Bastani, F.B.  
**Corporate Source:** Department of Computer Science University of Texas at Dallas, Richardson, TX 75083-0688, United States  
**Conference Title:** 20th IEEE Symposium on Reliable Distributed Systems (SRDS 2001)

Conference Location: New Orleans, LA, United States Conference Date: 20011028-20011031

Sponsor: IEEE

E.I. Conference No.: 58900

Source: Proceedings of the IEEE Symposium on Reliable Distributed Systems 2001. p 174-175

Publication Year: 2001

CODEN: PRDSFK ISSN: 1060-9857

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications)

Journal Announcement: 0113W3

Abstract: An environment being developed to facilitate the use of Commercial-Off-The-Shelf (COTS) components and third party software is described. Called iAPEX (Infrastructure for Advanced Programming for Embedded Computer Systems), the environment includes automated code transformation and synthesis, automated qualification, and a framework for adapting a system to changes in its environment without sacrificing performance. iAPEX is sharply focused on embedded systems, although it encompasses a comprehensive, integrated solution that spans the entire product life-cycle. (Edited abstract)

Descriptors: \*Computer software; Embedded systems; Software engineering; Computer architecture; Codes (symbols); Websites; Computer simulation

Identifiers: **Embedded software**; Commercial-off-the-shelf components; Online repository for embedded software

Classification Codes:

723.5 (Computer Applications); 723.2 (Data Processing)

723 (Computer Software, Data Handling & Applications)

72 (COMPUTERS & DATA PROCESSING)

8/5/3 (Item 3 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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05788018 E.I. No: EIP01025535852

Title: **Ellipse: An object-oriented and database-coupled expert system development environment**

Author: Malhotra, Rashmi; Malhotra, D.K.

Corporate Source: St. Joseph's Univ, Philadelphia, PA, USA

Source: Journal of Intelligent Systems v 10 n 4 2000. p 345-376

Publication Year: 2000

CODEN: JISYEH ISSN: 0334-1860

Language: English

Document Type: JA; (Journal Article) Treatment: L; (Literature Review/Bibliography); T; (Theoretical)

Journal Announcement: 0103W4

Abstract: In this study, we describe a relatively new approach to address the shortcomings of traditional expert-system environments - the information-based approach - namely, using a combination of OOP, DBMS, and expert systems. This study illustrates the design of an object-oriented expert system shell (Ellipse) that uses a combination of knowledgebases and databases. Ellipse offers a generalized, flexible system that supports embeddability, rapid prototyping, backward-chaining, explanation facility, linkage to databases, and implementation independence. Further, to illustrate the capability of the shell, we present the design and development of MFA-Mutual Fund Advisor. (Author abstract) 84 Refs.

Descriptors: Expert systems; Object oriented programming; **Database systems**; **Embedded systems**; **Software** prototyping

Identifiers: Flexible systems; Intensional knowledge; Extensional knowledge

Classification Codes:

723.4.1 (Expert Systems)

723.4 (Artificial Intelligence); 723.1 (Computer Programming); 723.3 (Database Systems)

723 (Computer Software); 722 (Computer Hardware)

72 (COMPUTERS & DATA PROCESSING)

8/5/4 (Item 1 from file: 2)

DIALOG(R)File 2:INSPEC

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7321975 INSPEC Abstract Number: C2002-08-6110J-030

**Title: A component-based approach for embedded software development**

Author(s): I-Ling Yen; Goluguri, J.; Bastani, F.; Khan, L.; Linn, J.

Author Affiliation: Texas Univ., Dallas, TX, USA

Conference Title: Proceedings Fifth IEEE International Symposium on Object-Oriented Real-Time Distributed Computing. ISIRC 2002 p.402-10

Editor(s): Bacellar, L.; Puschner, P.; Hong, S.

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 2002 Country of Publication: USA xiv+466 pp.

ISBN: 0 7695 1558 4 Material Identity Number: XX-2002-01435

U.S. Copyright Clearance Center Code: 0-7695-1558-4/02/\$17.00

Conference Title: Proceedings of ISORC 2002. 5th International Symposium on Object Oriented Real-Time Distributed Computing

Conference Sponsor: IEEE Comput. Soc. Tech. Committee on Distributed Process.; OMG; IFIP WG 10.4

Conference Date: 29 April-1 May 2002 Conference Location: Washington, DC, USA

Language: English Document Type: Conference Paper (PA)

Treatment: Practical (P)

**Abstract:** The rapid growth in the demand of embedded systems and the increased complexity of embedded software pose an urgent need for advanced embedded software development techniques. Software technology is shifting toward semi-automated code generation and integration of systems from components. Component-based development (CBD) techniques can significantly reduce the time and cost for developing software systems. However, there are some difficult problems with the CBD approach. Component identification and retrieval as well as component composition require extensive knowledge of the components. Designers need to go through a steep learning curve in order to effectively compose a system out of available components. We discuss an integrated mechanism for component-based development of embedded software. We develop an On-line **Repository for Embedded Software** (ORES) to facilitate component management and retrieval. ORES uses an ontology-based approach to facilitate repository browsing and effective search. Based on ORES, we develop the code template approach to facilitate semi-automated component composition. A code template can be instantiated by different sets of components and, thus, offers more flexibility and configurability and better reuse. Another important aspect in embedded software is the nonfunctional requirements and properties. In ORES, we capture nonfunctional properties of components and provide facilities for the analysis of overall system properties. (11 Refs)

Subfile: C

Descriptors: computer aided software engineering; embedded systems; object-oriented programming; software libraries; software reusability

Identifiers: component-based approach; embedded software development; semi-automated code generation; cost; component retrieval; Online **Repository for Embedded Software**; ontology-based approach; search; code template; software reuse; ORES

Class Codes: C6110J (Object-oriented programming); C6110B (Software engineering techniques); C6115 (Programming support)

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8/5/5 (Item 2 from file: 2)

DIALOG(R)File 2:INSPEC

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7036276 INSPEC Abstract Number: C2001-10-6150C-024

**Title: Languages, Compilers, and Tools for Embedded Systems. ACM SIGPLAN Workshop LCTES 2000. Proceedings (Lecture Notes in Computer Science Vol.1985)**

Editor(s): Davidson, J.; Min, S.L.

Publisher: Springer-Verlag, Berlin, Germany

Publication Date: 2001 Country of Publication: Germany viii+220 pp.

ISBN: 3 540 41781 8 Material Identity Number: XX-2001-00888

Conference Title: Languages, Compilers, and Tools for Embedded Systems.  
ACM SIGPLAN Workshop LCTES 2000. Proceedings

Conference Date: 18 June 2000 Conference Location: Vancouver, BC,  
Canada

Language: English Document Type: Conference Proceedings (CP)

Treatment: Practical (P)

Abstract: The following topics are dealt with: embedded systems; formal  
methods and **databases** ; compilers; software tools; **embedded processors**  
; and memory management and architecture.

Subfile: C

Descriptors: buffer storage; computer architecture; embedded systems;  
formal specification; multimedia communication; program compilers;  
programming languages; software tools; storage management

Identifiers: embedded systems; formal methods; databases; compilers;  
software tools; embedded processors; memory management; memory architecture

Class Codes: C6150C (Compilers, interpreters and other processors);  
C6110F (Formal methods); C6115 (Programming support); C6130M (Multimedia);  
C5220 (Computer architecture); C6120 (File organisation); C5400 (Analogue and digital computers and systems); C6140 (Programming languages)

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8/5/6 (Item 3 from file: 2)

DIALOG(R)File 2:INSPEC

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6742416 INSPEC Abstract Number: C2000-12-6120-002

**Title: A scalable, cost-effective, and flexible disk system using  
high-performance embedded-processors**

Author(s): Tomita, A.; Watanabe, N.; Takamoto, Y.; Inohara, S.; Maciel,  
F.; Odawara, H.; Sugie, M.

Author Affiliation: Central Res. Lab., Hitachi Ltd., Tokyo, Japan

Conference Title: Proceedings 2000 International Conference on Parallel  
Processing p.317-26

Editor(s): Lilja, D.J.

Publisher: IEEE Comput. Soc, Los Alamitos, CA, USA

Publication Date: 2000 Country of Publication: USA xx+590 pp.

ISBN: 0 7695 0768 9 Material Identity Number: XX-2000-02223

U.S. Copyright Clearance Center Code: 0 7695 0768 9/2000/\$10.00

Conference Title: Proceedings of the 2000 International Conference on  
Parallel Processing

Conference Sponsor: Int. Assoc. Comput. & Commun. (IACC)

Conference Date: 21-24 Aug. 2000 Conference Location: Toronto, Ont.,  
Canada

Language: English Document Type: Conference Paper (PA)

Treatment: Practical (P)

Abstract: As a scalable, cost-effective, and flexible solution for  
data-intensive systems, we are exploring active-network-storage (ANS),  
which is an array of ANS disk drives. The ANS drive improves flexibility by  
using a modular software design; that is, users can specify functions of  
the ANS drive by loading/unloading the corresponding modules on it. To keep  
the ANS drive cost-effective, users are allowed to choose whether native  
code modules or platform-independent Java-bytecode modules are executed on  
the drive. We forecast that a current high-performance embedded-processor  
is powerful enough to enable this modular design to be implemented and to  
provide a scalable, cost-effective, and flexible ANS system. We have  
confirmed our forecast by conducting an experiment with an ANS drive  
prototype with a 200 MHz **embedded - processor** running **database**  
sequential scanning and NFS, which are typical off-loaded functions with  
different characteristics. To evaluate scalability and cost-effectiveness  
of the ANS system, we estimated the throughput from measurements on our ANS  
prototype, and we compared it with the throughput that was measured on a  
450 MHz Pentium II Xeon server. Our estimation indicates that the scan  
throughput of the ANS system increases up to 71 MB/s while that of the  
server saturates at 25 MB/s because of its CPU bottleneck. The NFS  
read/write throughputs of two ANS drives surpassed the server maximum  
throughputs. (13 Refs)

Subfile: C

Descriptors: file servers; magnetic disc storage; storage management  
Identifiers: active-network-storage; array of ANS disk drives; ANS drive;  
ANS drive prototype; flexible disk system; high-performance  
embedded-processors; modular software design  
Class Codes: C6120 (File organisation); C5320C (Storage on moving  
magnetic media); C6150N (Distributed systems software)  
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8/5/7 (Item 1 from file: 233)  
DIALOG(R)File 233:Internet & Personal Comp. Abs.  
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00549214 99PM10-001

**Inside job -- Kidnap-prone execs are supposedly implanting Lo-Jack  
devices to track their movements**

Somerson, Paul

PC Computing , October 1, 1999 , v12 n10 p87, 1 Page(s)

ISSN: 0899-1847

Languages: English

Document Type: Articles, News & Columns

Geographic Location: United States

SOMERSON column presents author Paul Somerson's speculation about what society would be like in which microprocessors are implanted inside human beings. Mentions that British professor Kevin Warwick had a silicon-based transponder surgically inserted into his forearm last year. Explains that it generates a 64-bit number when zapped by a radio frequency (RF) transmitter. Adds that a receiver then looks it up in a **database**. Notes that body- **embedded processors** could eliminate the need for password and personal identification numbers, reduce the practice of waiting in lines, and lead to the building of homes and offices that automatically customize and adjust to individuals. Discusses the implications of computers' evolution into Internet terminals. Mentions ways in which the computer industry will convince people to have these microprocessors implanted within them. (MEM)

Descriptors: Computers and Society; Human-Computer Interaction;  
Microprocessor; Biology; Medicine

8/5/8 (Item 2 from file: 233)  
DIALOG(R)File 233:Internet & Personal Comp. Abs.  
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00540115 99IW07-012

**Enterprise computing**

Biggs, Maggie; Apicella, Mario; Hammond, Eric; Lattig, Michael

InfoWorld , July 5, 1999 , v21 n27 p47-52, 4 Page(s)

ISSN: 0199-6649

Languages: English

Document Type: Articles, News & Columns

Geographic Location: United States

Presents a special section on enterprise computing. Articles include: ``Sybase Unleashes Data on Linux'' (p47) by Maggie Biggs, describing mobile and **embedded database software**, Sybase SQL Anywhere Studio 6.0.2 for Linux (\$399) from Sybase Inc. of Emeryville, CA (800); ``Shaman `Magically' Updates Your Clients'' (p47, 52) by Mario Apicella, describing the Year 2000 remediation and asset management tool, Enterprise Shaman 3.2.3 (\$25,000) fro Shaman Corp. of San Francisco, CA (415); ``CCD's Validatio Confirms Y2K Readiness'' (p48, 52) by Eric Hammond, discussing Ye 2000 verification/validation software, Beyond 1999/Validate Versi 5.0 (\$0.04 to \$0.09 per line of code) from CCD Online Systems In of Arcadia, CA (626). Also includes: ``Pony Express Bucks Against the Outsourcing Trend'' (p50) by Michael Lattig on how Pony Exp Delivery Systems' decision to bring financial systems in-house. Includes three product summaries, one photo, and two screen displays. (MEM)

Descriptors: Enterprise Computing; Mobile Computing; Desktop Software;  
Year 2000; Asset Management; Corporate Strategy; Outsourcing

8/5/9 (Item 3 from file: 233)  
DIALOG(R)File 233:Internet & Personal Comp. Abs.  
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00540112 99IW07-009

**Sybase unleashes data on Linux**

Biggs, Maggie

InfoWorld , July 5, 1999 , v21 n27 p47, 1 Page(s)

ISSN: 0199-6649

Company Name: Sybase

URL: <http://www.sybase.com>

Product Name: Sybase SQL Anywhere Studio 6.0.2 for Linux

Languages: English

Document Type: Software Review

Grade (of Product Reviewed): B

Hardware/Software Compatibility: IBM PC Compatible

Geographic Location: United States

Presents a favorable review of Sybase SQL Anywhere Studio 6.0.2 for Linux (\$399), mobile and **embedded database software** from Sybase Inc. of Emeryville, CA (800). Runs on IBM PC Compatibles with Microsoft Windows 95, Windows 98, Linux 5.1, Sun Solaris, HP-UX and many others. Says that it extends mobile data management to a wide array of mobile device types. Cites features such as ease of use, good replication performance, and cost-effectiveness. Notes, however, that its tools are limited for the Red Hat Linux 5.1 and 5.2 platforms. Concludes that it is a good choice for enterprises seeking to leverage existing hardware and the Linux platform to support mobile users' database requirements. Received a rating of four on a scale of one to five. Includes one product summary and one screen display.  
(MEM)

Descriptors: Database; Mobile Computing; Enterprise Computing;  
Upgrade; Cross-Platform Computing; Unix

Identifiers: Sybase SQL Anywhere Studio 6.0.2 for Linux; Sybase

10/5/1 (Item 1 from file: 8)  
DIALOG(R)File 8:Ei Compendex(R)  
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06086133 E.I. No: EIP02287007632

**Title: Functional verification of the POWER4 microprocessor and POWER4 multiprocessor systems**

Author: Ludden, John M.; Roesner, Wolfgang; Heiling, Gerry M.; Reysa, John R.; Jackson, Jonathan R.; Chu, Bing-Lun; Behm, Michael L.; Baumgartner, Jason R.; Peterson, Richard D.; Abdulhafiz, Jamee; Bucy, William E.; Klaus, John H.; Klema, Danny J.; Le, Tien N.; Lewis, F. Danette; et al.

Corporate Source: IBM Server Group Burlington facility, Essex Junction, VT 05451, United States

Source: IBM Journal of Research and Development v 46 n 1 January 2002. p 53-76

Publication Year: 2002

CODEN: IBMJAE ISSN: 0018-8646

Language: English

Document Type: JA; (Journal Article) Treatment: T; (Theoretical)

Journal Announcement: 0207W2

Abstract: This paper describes the methods and simulation techniques used to verify the microarchitecture design and functional performance of the IBM POWER4 processor and the POWER4-based Regatta system. The approach was hierarchical, based on but considerably expanding the practice used for verification of the CMOS-based IBM S/390 Parallel Enterprise Server trademark G4. For POWER4, verification began at the abstract, high-level design phase and continued throughout the designer and unit levels, the multi-unit level, and finally the multiple-chip system level. The abstract (high-level design) phase permitted early validation of the POWER4 processor design prior to its commitment to HDL. The designer and unit-level stages focused on ensuring the correctness of the microarchitectural components. Multi-unit-level verification, performed on storage and I/O components as well as on the processor, confirmed architectural compliance for each of the chips and subsystems. Finally, system-level verification tested multiprocessor coherence and system-level function, including processor-to-I/O communication and validation of multiple hardware configurations. In parallel with design and functional validation, verification of reliability functions, performance, and degraded configurations was also performed at most of the levels in the hierarchy. 9 Refs.

Descriptors: Microprocessor **chips**; Computer architecture; **CMOS** integrated circuits; Computer **hardware**; Reliability theory; **Database** systems

Identifiers: Microarchitecture

Classification Codes:

714.2 (Semiconductor Devices & Integrated Circuits); 922.2 (Mathematical Statistics); 723.3 (Database Systems)

714 (Electronic Components & Tubes); 722 (Computer Hardware); 922 (Statistical Methods); 723 (Computer Software, Data Handling & Applications)

71 (ELECTRONICS & COMMUNICATION ENGINEERING); 72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS)

10/5/2 (Item 2 from file: 8)  
DIALOG(R)File 8:Ei Compendex(R)  
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05965925 E.I. No: EIP01536781034

**Title: The data organizer: A high traffic node for tracking detector data**

Author: Annovi, A.; Bagliesi, M.G.; Bardi, A.; Carosi, R.; Dell'Orso, M.; Giannetti, P.; Morsani, F.; Pietri, M.; Varotto, G.

Corporate Source: Istituto Naz. di Fisica Nucl., Pisa, Italy

Conference Title: Nuclear Science Symposium (NSS) 2000

Conference Location: Lyon, France Conference Date: 20001015-20001020

Sponsor: IEEE

E.I. Conference No.: 58814

Source: IEEE Transactions on Nuclear Science v 48 n 4 I August 2001. p 1313-1317

Publication Year: 2001

CODEN: IETNAE ISSN: 0018-9499

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications)

Journal Announcement: 0113W1

Abstract: The data organizer is part of a real-time tracker for hadron collider experiments. It constitutes the tracker interface to the data acquisition (DAQ): a high-speed data-traffic node, where thousands of words are received and simultaneously organized in an internal structured data base, to be later quickly retrieved and delivered in response to specific requests. It is capable of processing data at a rate of 40 MHz, thanks to the use of fast and very dense devices. The use of programmable logic allows easy upgrades, evolution, and fast reconfiguration to adapt the interface to different trigger and DAQ needs. The hardware-dedicated structure optimizes speed, size, and cost. 8 Refs.

Descriptors: Data acquisition; File organization; Interfaces (computer); Colliding beam accelerators; **Database** systems; Computer **hardware**; Field **programmable** gate arrays; Pattern recognition; Algorithms

Identifiers: Data organizer; Collider detector facility

Classification Codes:

932.1.1 (Particle Accelerators)

723.2 (Data Processing); 722.2 (Computer Peripheral Equipment); 932.1 (High Energy Physics); 723.3 (Database Systems); 723.5 (Computer Applications)

723 (Computer Software, Data Handling & Applications); 722 (Computer Hardware); 932 (High Energy Physics; Nuclear Physics; Plasma Physics)

72 (COMPUTERS & DATA PROCESSING); 93 (ENGINEERING PHYSICS)

10/5/3 (Item 3 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

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05837669 E.I. No: EIP01226527725

Title: **Quantitative analysis of FPGA-based database searching**

Author: Shirazi, N.; Benyamin, D.; Luk, W.; Cheung, P.Y.K.; Guo, S.

Corporate Source: Xilinx, Inc., San Jose, CA 95124-3400, United States

Source: Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology v 28 n 1-2 May/June 2001 2001. p 85-96

Publication Year: 2001

CODEN: JVSPED ISSN: 0922-5773

Language: English

Document Type: JA; (Journal Article) Treatment: T; (Theoretical)

Journal Announcement: 0106W5

Abstract: This paper reports two contributions to the theory and practice of using reconfigurable hardware to implement search engines based on hashing techniques. The first contribution concerns technology-independent optimisations involving run-time reconfiguration of the hash functions; a quantitative framework is developed for estimating design trade-offs, such as the amount of temporary storage versus reconfiguration time. The second contribution concerns methods for optimising implementations in Xilinx FPGA technology, which achieve different trade-offs in cell utilisation, reconfiguration time and critical path delay; quantitative analysis of these trade-offs are provided. 10 Refs.

Descriptors: **Database** systems; Field **programmable** gate arrays; Computer **hardware**; Search engines; Critical path analysis; Delay circuits; Algorithms; Optimization; Information retrieval

Identifiers: Reconfigurable hardware

Classification Codes:

723.3 (Database Systems); 721.2 (Logic Elements); 921.5 (Optimization Techniques); 903.3 (Information Retrieval & Use)

723 (Computer Software, Data Handling & Applications); 721 (Computer Circuits & Logic Elements); 722 (Computer Hardware); 921 (Applied Mathematics); 903 (Information Science)

72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS); 90 (ENGINEERING, GENERAL)



10/5/4 (Item 4 from file: 8)  
DIALOG(R)File 8:EI Compendex(R)  
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05698897 E.I. No: EIP00115396024

**Title: Analog VLSI time-delay neural network implementation for phoneme recognition**

Author: Gatt, E.; Micallef, J.; Chilton, E.  
Corporate Source: Univ of Malta, Msida, Malta  
Conference Title: Proceedings of the 2000 6th IEEE International Workshop on Cellular Neural Network and their Applications (CNNA 2000)  
Conference Location: Catania, Italy Conference Date: 20000523-20000525  
E.I. Conference No.: 57521  
Source: Proceedings of the IEEE International Workshop on Cellular Neural Networks and their Applications 2000. IEEE, Piscataway, NJ, USA, 00TH8509. p 315-320

Publication Year: 2000

CODEN: 85RZAL

Language: English

Document Type: CA; (Conference Article) Treatment: G; (General Review)

Journal Announcement: 0012W3

**Abstract:** The paper proposes an analog VLSI neural network chip, which can be cascaded in order to develop a time-delay neural network system for phoneme recognition. Backpropagation learning has been adopted to train the network to recognize phoneme frames extracted from the TIMIT database. A prototype chip, implemented using CMOS 2.0 mu m, double metal, double poly technology is also described, together with its specifications.  
(Author abstract) 7 Refs.

**Descriptors:** \*Neural networks; Speech recognition; Pattern recognition; Microprocessor chips; Learning systems; Backpropagation; VLSI circuits

**Identifiers:** Phoneme recognition; Time delay systems

**Classification Codes:**

723.4 (Artificial Intelligence); 751.5 (Speech); 723.5 (Computer Applications); 714.2 (Semiconductor Devices & Integrated Circuits)

723 (Computer Software); 751 (Acoustics); 714 (Electronic Components)

72 (COMPUTERS & DATA PROCESSING); 75 (ACOUSTICAL TECHNOLOGY); 71 (ELECTRONICS & COMMUNICATIONS)

10/5/5 (Item 5 from file: 8)  
DIALOG(R)File 8:EI Compendex(R)  
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05409403 E.I. No: EIP99114894476

**Title: S88 batch control in action**

Author: Brooks, Sean

Corporate Source: Fine Organics' Seal Sands Facility, Hartlepool, Engl

Source: Control (Chicago, Ill) v 12 n 8 1999. p 69-71

Publication Year: 1999

ISSN: 1049-5541

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 9912W3

**Abstract:** Fine Organics specializes in the development and production of high-value organic intermediates for the pharmaceutical and agrochemical industries. In 1995, increasing demand for the plant's products led to plans for expansion of the Tesside facility. To obtain maximum advantage from Module Production Building 3's (MPB3) planned flexibility. Fine organics decided to equip it with the latest flexible batch production computer technology using an APACS batch control system from Moore Process Automation Solutions. The new technology is S88-compliant.

**Descriptors:** Computer control systems; Chemical plants; Factory automation; Chemical reactors; Programmable logic controllers; Display devices; Graphical user interfaces; Database systems; Computer software; Personnel

**Identifiers:** Batch production computer control technology; Visual display

units; Batch management

Classification Codes:

723.5 (Computer Applications); 802.1 (Chemical Plants & Equipment);  
731.1 (Control Systems); 732.1 (Control Equipment); 722.2 (Computer  
Peripheral Equipment); 723.3 (Database Systems)

723 (Computer Software); 802 (Chemical Apparatus & Plants); 731  
(Automatic Control Principles); 732 (Control Devices); 722 (Computer  
Hardware)

72 (COMPUTERS & DATA PROCESSING); 80 (CHEMICAL ENGINEERING); 73  
(CONTROL ENGINEERING)

10/5/6 (Item 6 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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05143645 E.I. No: EIP98104427913

**Title: Prediction of AC performance of double-polysilicon bipolar  
transistors from E-Test parameters: An experiment**

Author: Kelly, Sean C.; Griffith, Edel C.; Power, James A.; O'Neill, Mike  
Corporate Source: Raheen Industrial Estate, Limerick, UK

Conference Title: Proceedings of the 1998 IEEE International Conference  
on Microelectronic Test Structures

Conference Location: Kanazawa, Jpn Conference Date: 19980323-19980326

Sponsor: IEEE

E.I. Conference No.: 49127

Source: IEEE International Conference on Microelectronic Test Structures  
1998. IEEE, Piscataway, NJ, USA, 98CH36157. p 147-152

Publication Year: 1998

CODEN: PIMTE4

Language: English

Document Type: CA; (Conference Article) Treatment: X; (Experimental)

Journal Announcement: 9812W3

Abstract: AC characterization of silicon bipolar and BiCMOS processes for  
RF applications is necessary because of the ever increasing speed of  
operation of the bipolar devices. The data acquisition and parameter  
extraction steps associated with AC characterization and modelling are time  
consuming and tedious and cannot easily be implemented as part of standard  
process monitor measurements. This paper will discuss a methodology for  
relating the readily available E-Test parameter database to the AC  
parameters which are more difficult to obtain. The work was done on a 0.6  
 $\mu$ m BiCMOS process which is suited to mixed mode RF chip designs. (Author  
abstract) 4 Refs.

Descriptors: Bipolar transistors; **Semiconducting** silicon; **CMOS**  
integrated circuits; **Database** systems

Identifiers: Double-polysilicon bipolar transistors

Classification Codes:

712.1.1 (Single Element Semiconducting Materials)

714.2 (Semiconductor Devices & Integrated Circuits); 712.1  
(Semiconducting Materials); 723.3 (Database Systems)

714 (Electronic Components); 712 (Electronic & Thermionic Materials);  
723 (Computer Software)

71 (ELECTRONICS & COMMUNICATIONS); 72 (COMPUTERS & DATA PROCESSING)

10/5/7 (Item 7 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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05009812 E.I. No: EIP98054183991

**Title: Simple pre-processors significantly improve LZ 1 compression**

Author: Craft, David J.

Corporate Source: Austin IBM Microelectronics, Austin, TX, USA

Conference Title: Proceedings of the 1998 Data Compression Conference,  
DCC

Conference Location: Snowbird, UT, USA Conference Date:  
19980330-19980401

Sponsor: IEEE

E.I. Conference No.: 48280

Source: Data Compression Conference Proceedings 1998. IEEE, Piscataway, NJ, USA. p 538

Publication Year: 1998

CODEN: DDCCF9 ISSN: 1068-0314

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications)

Journal Announcement: 9807W1

Abstract: The UNICODE pre-processor operates once some pre-set threshold number of even-ordered predecessor byte value identities occurs. These pre-processors are extremely simple to implement, in hardware or software, and can yield significant data compression improvements. For ALDC 2, IBM's fast CAM-based hardware LZ 1 implementation employing a 1024 Byte History size, the compression achieved on database records improves by 10% typically, and is more than doubled on some binary image bitmap data. UNICODE compression is improved by nearly 40% on UNICODE versions of the text in the CALGARY corpus, while the compression on other kinds of data is not significantly impacted. On-chip data rates for hardware pre/post processors of this type can easily attain 100 MBytes/second in contemporary CMOS technology, exactly matching the ALDC hardware LZ 1 speeds. The chip area required for such circuitry is under 1.0 sq mm, so such enhancements can be added to ALDC for very little extra cost.

Descriptors: Data compression; Algorithms; Image coding; Computer hardware ; Database systems; CMOS integrated circuits

Identifiers: UNICODE pre processor; Data sequences; Decompression Classification Codes:

723.2 (Data Processing); 723.3 (Database Systems); 714.2 (Semiconductor Devices & Integrated Circuits)

723 (Computer Software); 722 (Computer Hardware); 714 (Electronic Components)

72 (COMPUTERS & DATA PROCESSING); 71 (ELECTRONICS & COMMUNICATIONS)

10/5/8 (Item 8 from file: 8)

DIALOG(R)File 8: Ei Compendex(R)

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04461175 E.I. No: EIP96083271682

Title: On Trial at the summer Olympic games: smart cards

Author: Myers, Ware

Source: Computer v 29 n 7 Jul 1996. p 88-91

Publication Year: 1996

CODEN: CPTRB4 ISSN: 0018-9162

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 9610W1

Abstract: Smart cards have their beginnings in the development of microprocessors and semiconductor memory in the 1970s. Since then, more than 150 million smart-card microcontrollers has been shipped. Today, over 350 million smart card are in use around the world, however, few Americans have ever used one. The future of the smart card in the US is riding on a high-profile trial at the Olympics. 1 Refs.

Descriptors: Smart cards; Information technology; Marketing; Database systems; CMOS integrated circuits; ROM ; Optical data storage; Security of data; Technological forecasting

Identifiers: Magnetic stripe card; Microcontroller cards; Memory cards; Optical cards

Classification Codes:

722.4 (Digital Computers & Systems); 723.5 (Computer Applications); 911.4 (Marketing); 723.3 (Database Systems); 714.2 (Semiconductor Devices & Integrated Circuits); 722.1 (Data Storage, Equipment & Techniques)

722 (Computer Hardware); 723 (Computer Software); 911 (Industrial Economics); 714 (Electronic Components)

72 (COMPUTERS & DATA PROCESSING); 91 (ENGINEERING MANAGEMENT); 71 (ELECTRONICS & COMMUNICATIONS)

10/5/9 (Item 9 from file: 8)  
DIALOG(R)File 8:Ei Compendex(R)  
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04388850 E.I. No: EIP96043140638

**Title: Circuit sensitivity analysis in terms of process parameters**

Author: van Dort, M.J.; Klaassen, D.B.M.

Corporate Source: Philips Research Lab, Eindhoven, Neth

Conference Title: Proceedings of the 1995 International Electron Devices Meeting, IEDM'95

Conference Location: Washington, DC, USA Conference Date: 19951210-19951213

Sponsor: IEEE

E.I. Conference No.: 44542

Source: Technical Digest - International Electron Devices Meeting 1995. IEEE, Piscataway, NJ, USA, 95CH35810. p 941-944

Publication Year: 1995

CODEN: TDIMD5 ISSN: 0163-1918

Language: English

Document Type: CA; (Conference Article) Treatment: A; (Applications); T; (Theoretical)

Journal Announcement: 9606W3

Abstract: A new methodology for analyzing the spreads at circuit level is described by simulating the response surfaces of long-channel transistor. For the extrapolation of the results of the long-channel devices to devices with arbitrary dimensions, the scaling rules of MOS MODEL are used. This allows a quasi-43D analysis without the need to actually perform process and device simulations in three dimensions. The method is verified on a large database of a CMOS process containing the parameter sets of MOSFETs and applied at circuit level modeling the gate delay of a ring oscillator. 3 Refs.

Descriptors: MOSFET devices; Sensitivity analysis; Computer simulation; Semiconductor device models; Functions; Calculations; CMOS integrated circuits; Database systems; Gates (transistor); Three dimensional

Identifiers: Circuit sensitivity analysis; Response functions; Gate delay; Ring oscillator; Short channel parameters

Classification Codes:

714.2 (Semiconductor Devices & Integrated Circuits); 723.5 (Computer Applications); 921.5 (Optimization Techniques); 723.3 (Database Systems)

714 (Electronic Components); 921 (Applied Mathematics); 723 (Computer Software)

71 (ELECTRONICS & COMMUNICATIONS); 92 (ENGINEERING MATHEMATICS); 72 (COMPUTERS & DATA PROCESSING)

10/5/10 (Item 10 from file: 8)  
DIALOG(R)File 8:Ei Compendex(R)  
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04003239 E.I. No: EIP94121505323

**Title: Parallel mainframe: cheap chips, moderate upgrade, expensive solution**

Author: Ricciuti, Mike

Source: Datamation v 40 n 11 Jun 1 1994. p 70-71

Publication Year: 1994

CODEN: DTMNAT ISSN: 0011-6963

Language: English

Document Type: JA; (Journal Article) Treatment: G; (General Review)

Journal Announcement: 9502W1

Abstract: IBM has built a new technology extension onto the mainframe to boost the performance of OLTP and database queries. Its new Parallel Transaction Server (PTS) and Parallel Query Server (PQS) are special-purpose proprietary mixes of hardware and software aimed at augmenting mainframe processing at lower costs than buying a new mainframe. The strategy adds more processing power without giving up the comfort and security of MVS and without dumping MVS applications. PTS and PQS are based on CMOS processors, the first S/390 systems to abandon the old, expensive bipolar processors. These parallel servers, however, do not match

'mainframe alternative' Unix system pricing. Users will have to weigh them against a very attractive used 3090 market.

Descriptors: Digital computers; Parallel processing systems; Computer hardware ; Computer software; CMOS integrated circuits; Database systems; Costs; Distributed computer systems; Query languages; Technology

Identifiers: Mainframe; CMOS processors; Parallel mainframe; Processing power

Classification Codes:

722.4 (Digital Computers & Systems); 723.1 (Computer Programming); 714.2 (Semiconductor Devices & Integrated Circuits); 723.3 (Database Systems); 911.1 (Cost Accounting); 901.4 (Impact of Technology on Society)

722 (Computer Hardware); 723 (Computer Software); 714 (Electronic Components); 911 (Industrial Economics); 901 (Engineering Profession)

72 (COMPUTERS & DATA PROCESSING); 71 (ELECTRONICS & COMMUNICATIONS); 91 (ENGINEERING MANAGEMENT); 90 (GENERAL ENGINEERING)

10/5/11 (Item 11 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

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02645255 E.I. Monthly No: EIM8809-049147

Title: ALARM MONITORING.

Author: Rudzicz, Matthew

Corporate Source: GE Canada, Can

Conference Title: Conference Proceedings - Third Annual Canadian Programmable Control Conference & Exhibition. 1987 Conference Theme: Programmable Controls - the Expanding Role.

Conference Location: Hamilton, Ont, Can Conference Date: 19871110

Sponsor: IEEE, Hamilton Section, Hamilton, Ont, Can

E.I. Conference No.: 11493

Source: Publ by IEEE, New York, NY, USA. Available from IEEE Service Cent (cat n 87-TH0201-4), Piscataway, NJ, USA 3p

Publication Year: 1987

Language: English

Document Type: PA; (Conference Paper)

Journal Announcement: 8809

Abstract: An upgrade to the alarm-monitoring system of a manufacturing plant is described. The alarms are generated by a variety of devices throughout the plant, including programmable controllers, relay panels, and field devices such as pushbuttons. There were customized requirements such as grouping of alarms, time-thresholds, disabling faults, and first-in faults. For the backbone of the shop-floor communications system, a token-passing bus allowing real-time response was chosen. Built-in diagnostics provided monitoring of the entire hardware link. A programmable controller was then used to create a database, and the results were both displayed graphically and printed out.

Descriptors: ALARM SYSTEMS--\*Monitoring; INDUSTRIAL PLANTS-- Components ; CONTROL SYSTEMS, PROGRAMMED ; DATABASE SYSTEMS

Identifiers: ALARM-MONITORING SYSTEM; SHOP-FLOOR COMMUNICATIONS SYSTEM; TOKEN-PASSING BUS; FIRST-IN FAULTS; BUILT-IN DIAGNOSTICS

Classification Codes:

914 (Safety Engineering); 731 (Automatic Control Principles); 723 (Computer Software)

91 (ENGINEERING MANAGEMENT); 73 (CONTROL ENGINEERING); 72 (COMPUTERS & DATA PROCESSING)

10/5/12 (Item 12 from file: 8)

DIALOG(R)File 8:EI Compendex(R)

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02041749 E.I. Monthly No: EI8611105240 E.I. Yearly No: EI86007614

Title: TECHNIQUE FOR SPECIFYING LOGICAL TRIGGER CONDITIONS FOR A DATA COLLECTION DEVICE.

Author: Anon

Source: IBM Technical Disclosure Bulletin v 29 n 2 Jul 1986 p 905-910

Publication Year: 1986  
 CODEN: IBMTAA ISSN: 0018-8689  
 Language: ENGLISH  
 Document Type: JA; (Journal Article) Treatment: A; (Applications); T; (Theoretical)  
 Journal Announcement: 8611  
 Abstract: This article describes a technique which allows the data collection **device** to be **programmed** to begin capturing **data** based on values present in the data stream being monitored. By monitoring the data stream for a particular event, as indicated by a logical expression passed to the data collection device, it is possible to begin the data collection process only after the 'triggering' condition has been met. This technique allows better use of available memory for capturing data by not storing data which have been previously determined to be of no useful value. The method described may be implemented easily by the microprocessor present in the data collection device.  
 Descriptors: \*AUTOMOBILES--\*Electronic Equipment; LOGIC DEVICES; DATA PROCESSING  
 Identifiers: LOGICAL TRIGGER; DATA COLLECTION; DATA STREAM; MICROPROCESSOR; TECHNICIAN TERMINAL  
 Classification Codes:  
 662 (Automotive Design & Manufacture); 715 (General Electronic Equipment); 723 (Computer Software); 721 (Computer Circuits & Logic Elements)  
 66 (AUTOMOTIVE ENGINEERING); 71 (ELECTRONICS & COMMUNICATIONS); 72 (COMPUTERS & DATA PROCESSING)

10/5/13 (Item 13 from file: 8)  
 DIALOG(R) File 8: Ei Compendex(R)  
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01539173 E.I. Monthly No: EI8407067114 E.I. Yearly No: EI84063782  
 Title: **SMART CONTROLLER CHIP SET SIMULTANEOUSLY JUGGLES HARD-, FLOPPY-DISK CURVES.**  
 Author: Kaplinsky, Cecil; Thomsen, Guy  
 Corporate Source: Signetics's MOS, Microprocessor Div, Sunnyvale, Calif, USA  
 Source: Electronic Design v 32 n 9 May 3 1984 p 313-318, 320, 322-323  
 Publication Year: 1984  
 CODEN: ELODAW ISSN: 0013-4872  
 Language: ENGLISH  
 Journal Announcement: 8407  
 Abstract: An intelligent multiple-disk controller chip, supported by its companion disk phase-locked loop chip, is introduced that can control any combination of four hard and floppy disks. It is adaptable enough to control upcoming disk interfaces. The chip can be programmed by the user with high-level commands, sent through a host processor via locations in host memory that are called event control areas. Inside the event control area are programmable record-processing functions, which in effect relieve the host of deciding whether a given record is the one it is looking for. Thus the functions speed overall system performance. Such programmability puts even sophisticated systems within the controller's reach. For instance, by tapping some of the **chip**'s **programmable** functions, designers of **data - base** management systems will be able to increase speed by an order of magnitude.  
 Descriptors: \*INTEGRATED CIRCUITS--\*Applications; COMPUTER INTERFACES--Control; DATA STORAGE, MAGNETIC--Disk; COMPUTERS, MICROPROCESSOR  
 Identifiers: EVENT CONTROL AREAS; SMART CONTROLLER CHIP; PHASE-LOCKED LOOP CHIP  
 Classification Codes:  
 713 (Electronic Circuits); 714 (Electronic Components); 723 (Computer Software); 732 (Control Devices)  
 71 (ELECTRONICS & COMMUNICATIONS); 72 (COMPUTERS & DATA PROCESSING); 73 (CONTROL ENGINEERING)

10/5/14 (Item 14 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)  
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01213611 E.I. Monthly No: EIM8207-021761

**Title: TACTICAL INFORMATION EXCHANGE SYSTEM (TIES) EXECUTIVE SOFTWARE.**  
Author: Palatucci, G.; Nowicki, C.  
Corporate Source: US Nav Air Dev Cent, Warminster, Pa, USA  
Conference Title: Collection of Technical Papers - 4th AIAA/IEEE Digital Avionics Systems Conference.  
Conference Location: St. Louis, Mo, USA Conference Date: 19811117  
Sponsor: AIAA, New York, NY, USA; IEEE, New York, NY, USA  
E.I. Conference No.: 00408  
Source: AIAA Paper Publ by AIAA (CP818), New York, NY, USA AIAA 81-2257,  
p 194-205

Publication Year: 1981  
CODEN: AAPRAQ  
Language: English  
Document Type: PA; (Conference Paper)  
Journal Announcement: 8207  
Descriptors: \*AVIONICS--\*Computer Applications  
Identifiers: EXECUTIVE SOFTWARE; TACTICAL INFORMATION EXCHANGE SYSTEM;  
DIGITAL AVIONICS; TELECOMMUNICATION NETWORK NODES; COMMUNICATION;  
NAVIGATION; IDENTIFICATION; CNI LINKS; MODULAR **PROGRAMMABLE** **HARDWARE** ;  
**DATA** **BASE**  
Classification Codes:  
652 (Aircraft); 715 (General Electronic Equipment); 723 (Computer Software)  
65 (AEROSPACE ENGINEERING); 71 (ELECTRONICS & COMMUNICATIONS); 72 (COMPUTERS & DATA PROCESSING)

10/5/15 (Item 15 from file: 8)

DIALOG(R) File 8: Ei Compendex(R)  
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00817371 E.I. Monthly No: EI7905031583 E.I. Yearly No: EI79016939

**Title: PROGRAMMING LANGUAGES FOR RELATIONAL DATA BASE SYSTEMS.**  
Author: Prenner, Charles J.; Rowe, Lawrence A.  
Corporate Source: Univ of Calif, Berkeley  
Source: AFIPS Conference Proceedings v 47, Anaheim, Calif, Jun 5-8 1978.  
Publ by AFIPS, Montvale, NJ, 1978 p 849-855

Publication Year: 1978  
CODEN: AFPGBT ISSN: 0095-6880  
Language: ENGLISH  
Journal Announcement: 7905  
Abstract: Techniques are discussed for improving the programming environment for data base applications. The hypothesis is that a superior environment can be realized by incorporating the **data base** operations as **part** of the **programmable** language itself. An outline is given of some limitations associated with previously proposed environments. These stem primarily from the fact that the programming language is totally unaware of the data base. An illustration is given of some of the benefits provided by an integrated approach through the application of current techniques from programming language research. 41 refs.

Descriptors: \*COMPUTER PROGRAMMING LANGUAGES; DATA BASE SYSTEMS  
Classification Codes:  
723 (Computer Software)  
72 (COMPUTERS & DATA PROCESSING)

10/5/16 (Item 1 from file: 2)

DIALOG(R) File 2: INSPEC  
(c) 2002 Institution of Electrical Engineers. All rts. reserv.

04277072 INSPEC Abstract Number: B9212-6210L-110, C9212-6160B-005

**Title: Standardised communications- components of SPS ( database programmable control)**  
Author(s): Gramme, U.

Author Affiliation: Comsy GmbH, Offenbach, Germany  
Journal: Elektronik vol.41, no.19 p.84-9  
Publication Date: 15 Sept. 1992 Country of Publication: West Germany  
CODEN: EKRKAR ISSN: 0013-5658  
Language: German Document Type: Journal Paper (JP)  
Treatment: General, Review (G)

Abstract: The decentralisation of manufacturing processes means that several units, in different locations, are used simultaneously in the control of a production installation. These must all be compatible. A list is given of the various components available for communication with the master computer. This list includes connection, polled service verification, programmed data acquisition, interlocked control, and programmed alarm report. (8 Refs)

Subfile: B C

Descriptors: distributed databases; local area networks; manufacturing computer control; protocols

Identifiers: standardised communications; host computer communication; database programmable control; decentralisation of manufacturing processes; master computer; connection; polled service verification; programmed data acquisition; interlocked control; programmed alarm report

Class Codes: B6210L (Computer communications); C6160B (Distributed DBMS); C5620L (Local area networks); C7480 (Production engineering)

10/5/17 (Item 2 from file: 2)

DIALOG(R) File 2:INSPEC

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02619005 INSPEC Abstract Number: C86018151

**Title: The design of a logic simulation accelerator**

Author(s): Siegel, S.; Kaszynski, M.E.

Author Affiliation: Zycad Corp., St. Paul, MN, USA

Journal: VLSI Design vol.6, no.10 p.76-80

Publication Date: Oct. 1985 Country of Publication: USA

CODEN: VDESDP ISSN: 0279-2834

Language: English Document Type: Journal Paper (JP)

Treatment: Practical (P)

Abstract: Zycad has announced a new logic simulation engine called the Sprintor, a 'personal' accelerator that plugs directly into a bus slot in a personal computer, workstation, or MicroVAX. The Sprintor can now provide logic simulation of up to 16000 modeling elements, or 25 KG (kilogates). Speed approximates 200000 events per second, or 600 KG/sec. A greatly simplified architecture enables a reduce chip count while maintaining compatibility with the logic evaluator. A computer-aided engineering system was assembled expressly for the Sprintor Design Project. A Mentor Graphics Idea workstation was chosen for schematic entry and **database** management.

**CMOS** and **TTL parts** from Mentor's libraries were then translated into ZILOS format using a Mentor-to-ZILOS translator; ZILOS software had been selected (primarily for its macro orientation) as the front-end interface for an accelerated simulator, the author's Logic Evaluator. A VAX 11/785 maintained the interface to the accelerator, compiling the necessary network descriptions on each invocation of a simulation run. A review is given of how a fully optimized design was produced in less than 9 months after the original goals were specified. (0 Refs)

Subfile: C

Descriptors: logic CAD; satellite computers; special purpose computers

Identifiers: Zycad; design; logic simulation accelerator; logic simulation engine; Sprintor; personal computer; workstation; MicroVAX; 25 KG; 200000 events per second; 600 KG/sec; simplified architecture; reduce chip count; computer-aided engineering system; Sprintor Design Project; Mentor Graphics Idea workstation; ZILOS; Logic Evaluator; VAX 11/785; fully optimized design

Class Codes: C5210B (Computer-aided logic design); C5250 (Microcomputer techniques); C7410D (Electronic engineering); C7430 (Computer engineering)

10/5/18 (Item 3 from file: 2)

DIALOG(R) File 2:INSPEC



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01603987 INSPEC Abstract Number: B80054516, C80035540

**Title:** Universal automated subsystem for coding complex forms of graphic data based on a programmable drawing board coordinate measuring device

**Author(s):** Aparin, G.P.; Samnilova, V.N.

**Journal:** Avtometriya no.3 p.99-102

**Publication Date:** May-June 1980 **Country of Publication:** USSR

**CODEN:** AVMEBI **ISSN:** 0320-7102

**Translated in:** Optoelectronics, Instrumentation and Data Processing

**Country of Publication:** USA

**CODEN:** OIDPE4 **ISSN:** 8756-6990

**Language:** Russian **Document Type:** Journal Paper (JP)

**Treatment:** Experimental (X)

**Abstract:** A universal graphics coding subsystem is described which satisfies the criteria of ergonomics, flexibility, productivity, reliability and cost. The subsystem is based on a programmable drawing board with coordinate measurement on-line to a minicomputer. The board has an interchangeable problem-oriented cursor and coordinate detectors interfaced through special-purpose coding and storage modules to the computer bus. The structure and functions of the subsystem are described.

(8 Refs)

**Subfile:** B C

**Descriptors:** computer graphic equipment; encoding

**Identifiers:** automated subsystem; graphic data; coordinate measuring device; ergonomics; flexibility; productivity; reliability; programmable drawing board; storage modules; computer bus; graphic coding subsystem; coding modules

**Class Codes:** B6120B (Codes); C5540 (Terminals and graphic displays)

10/5/19 (Item 4 from file: 2)

DIALOG(R) File 2:INSPEC

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00211321 INSPEC Abstract Number: C71001548

**Title:** Vision information center: A user-oriented data base

**Author(s):** Eichhorn, M.M.; Reinecke, R.D.

**Author Affiliation:** Harvard Medical School, Boston, MA, USA

**Journal:** Science vol.169, no.3940 p.29-31

**Publication Date:** 3 July 1970 **Country of Publication:** USA

**CODEN:** SCIEAS **ISSN:** 0036-8075

**Language:** English **Document Type:** Journal Paper (JP)

**Abstract:** In response to the information needs of scientists and physicians who work in the field of vision, the authors have developed a direct-access, on-line specialized information retrieval system which combines computer-assisted instruction and bibliographic retrieval. An essential feature of the system is its accessibility via remote computer terminal. The key to the data base is a thesaurus which contains specific terminology arranged in a hierarchical structure. The thesaurus is used both for indexing the literature and for retrieving the information stored in the computer. Courses of instruction on selected subjects in ophthalmology have been programmed and form part of the data base. A search of the literature retrieval system by single subject, or by a combination of several subjects chosen from the thesaurus, retrieves the appropriate bibliographic citations. (11 Refs)

**Subfile:** C

**Descriptors:** information retrieval systems; information services

**Class Codes:** C7210 (Information services and centres)

10/5/20 (Item 1 from file: 94)

DIALOG(R) File 94:JICST-Eplus

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01336771 JICST ACCESSION NUMBER: 91A0672506 FILE SEGMENT: JICST-E  
**Report on database construction promotion and its technological**

development. Database construction of programmable peripheral devices of microcomputers. ( Sponsor : Database Promotion Center, Japan ).

Nihonshisutemuhausukyokai

Maikuro Konpyuta no Puroguramaburu Shuhen Debaisu no Detabesuka. Heisei 2 Nendo, 1991, PAGE.81P

JOURNAL NUMBER: N19912006H

UNIVERSAL DECIMAL CLASSIFICATION: 002.5:659.2 681.325/.327

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Journal

ARTICLE TYPE: Original paper

MEDIA TYPE: Printed Publication

ABSTRACT: This DB stores knowledge of details and application fields for peripheral devices of microcomputers. By constructing systems applicable to this DB, developing environment of the systems using peripheral devices can be intensified. While this DB is classified into physical information node, logical information node, and explanation information node, standard DB structure is defined from the above DB structure. Formats of DB are shown by its detail to construct each information : and preparation procedures of DB, and sequence development suport system for device initiation as a DB utilizaiton case are also described. As reference materials, format cases of peripheral devices, and automatic generation of initializing sequence of programmable devices are recorded. (1991.3)

DESCRIPTORS: database; microcomputer; computer peripheral equipment; application program; support program

BROADER DESCRIPTORS: digital computer; computer; hardware; equipment; computer program; software

CLASSIFICATION CODE(S): AC08000K; JC04010C

10/5/21 (Item 2 from file: 94)

DIALOG(R)File 94:JICST-EPlus

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01110203 JICST ACCESSION NUMBER: 90A0600908 FILE SEGMENT: JICST-E

Database construction for programmable peripheral devices of microcomputers. Report on database construction promotion and technological development. ( Sponsor : Database Promotion Center ).

Nihonshisutemuhausukyo

Maikuro Konpyuta no Puroguramaburu Shuhen Debaisu no Detabesu Kochiku. Heisei 1 Nendo, 1990, PAGE.40P

JOURNAL NUMBER: N19902216Z

UNIVERSAL DECIMAL CLASSIFICATION: 681.3:061.68

LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan

DOCUMENT TYPE: Journal

ARTICLE TYPE: Original paper

MEDIA TYPE: Printed Publication

ABSTRACT: Medium-scale research and development enterprises using microcomputers, have problems with development environment. This data base aims at training of manpower and improved development environment. Technical knowledge of microcomputer peripheral devices and their applications are stored. This paper describes outline, details, structure, format and procedure of database construction, problems in commercial application and the solution. (1990.3).

DESCRIPTORS: technology development; human resource; corporate environment; database; microcomputer; computer peripheral equipment; information arrangement technique

BROADER DESCRIPTORS: research and development; development; resource; environment; digital computer; computer; hardware; equipment; documentation; information management; management

CLASSIFICATION CODE(S): JD03030U

10/5/22 (Item 3 from file: 94)

DIALOG(R)File 94:JICST-EPlus

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00969435 JICST ACCESSION NUMBER: 90A0018532 FILE SEGMENT: JICST-E  
**Report on promotion of database construction and technology development.**  
**Construction of database about programmable , peripheral devices**  
**for microcomputers. (Sponsor : Database Promotion Center, Japan)**  
NIHONSHISUTEMUHAUSUKYOKAI  
Maikuro Konpyuta no Puroguramaburu Shuhen Debaisu no Detabesuka. Showa 63  
Nendo, 1989, PAGE.42P  
JOURNAL NUMBER: N19892892B  
UNIVERSAL DECIMAL CLASSIFICATION: 681.3:061.68  
LANGUAGE: Japanese COUNTRY OF PUBLICATION: Japan  
DOCUMENT TYPE: Journal  
ARTICLE TYPE: Original paper  
MEDIA TYPE: Printed Publication  
DESCRIPTORS: database; computer peripheral equipment; microcomputer  
BROADER DESCRIPTORS: equipment; digital computer; computer; hardware  
CLASSIFICATION CODE(S): JD03030U

10/5/23 (Item 1 from file: 6)  
DIALOG(R)File 6:NTIS  
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1928598 NTIS Accession Number: AD-D017 653/7  
**Telephone Line Selector and Call Accountant**  
(Patent)  
Berenato, D. A.  
Department of the Navy, Washington, DC.  
Corp. Source Codes: 001840000; 110050  
Report No.: PAT-APPL-8-043 069; PATENT-5 400 395  
Filed 2 Apr 93 patented 21 Mar 95 7p  
Languages: English Document Type: Patent  
Journal Announcement: GRAI9606  
Supersedes AD-D015 958, PAT-APPL-8-043 069.  
This Government-owned invention available for U.S. licensing and,  
possibly, for foreign licensing. Copy of patent available Commissioner of  
Patents, Washington, DC 20231.  
NTIS Prices: Not available NTIS  
Country of Publication: United States  
A telephone line selection and accounting system is provided. The system  
has an electronically **programmable** read only memory ( **EPROM** ) **database**  
of rate information for each carrier. Upon initiation of a user call, the  
database is first updated using automatic rate update tones (if available)  
from each carrier. The database rate information is then used to select the  
lowest cost carrier. The system provides the capability to connect to a  
carrier using either distinct lines or local access numbers over a common  
line. During the call, total elapsed time and cost is displayed at the user  
phone. Once the call is complete, call accounting information is stored for  
later retrieval and analysis. In addition to the automatic rate update  
tones, the rate information database may be updated either by changing  
EPROMs or by manually entering new rate information from a user phone.  
(KAR) p. 1.  
Descriptors: \*Access; \*Telephone lines; \*Read only memories; \*Patents;  
Data bases; Accounting; Rates; Display systems; Costs; User needs;  
Automatic; Selection  
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Section Headings: 45C (Communication--Common Carrier and Satellite); 88B  
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ology)

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1864901 NTIS Accession Number: DE95003255  
**Experimental and theoretical basis for advanced tokamaks**  
Chan, V. S.

General Atomics, San Diego, CA.  
Corp. Source Codes: 092012000; 9525697;  
Sponsor: Oak Ridge National Lab., TN (United States).; Lawrence Livermore  
National Lab., CA (United States).; Department of Energy, Washington, DC.  
Report No.: GA-A-21813; CONF-9408189-1  
Sep 94 15p  
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Journal Announcement: GRAI9510; ERA9515  
ISPP workshop on tokamak concept improvement, Varenna (Italy), 29 Aug - 3  
Sep 1994. Sponsored by Department of Energy, Washington, DC.  
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In this paper, arguments will be presented to support the attractiveness  
of advanced tokamaks as fusion reactors. The premise that all improved  
confinement regimes obtained to date were limited by magnetohydrodynamic  
stability will be established from experimental results. Accessing the  
advanced tokamak regime, therefore, requires means to overcome and enhance  
the beta limit. We will describe a number of ideas involving control of the  
plasma internal profiles, e.g. to achieve this. These approaches will have  
to be compatible with the underlying mechanisms for confinement  
improvement, such as shear rotation suppression of turbulence. For  
steady-state, there is a trade-off between full bootstrap current operation  
and the ability to control current profiles. The coupling between current  
drive and stability dictates the choice of sources and suggests an optimum  
for the bootstrap fraction. We summarize by presenting the future plans of  
the US confinement devices, DIII-D, PBX-M, C-Mod, to address the advanced  
tokamak physics issues and provide a database for the design of  
next-generation experiments.

Descriptors: Tokamak Devices ; Bootstrap Current; Data Base  
Management; MHD Equilibrium; Modifications; Performance  
Identifiers: EDB/700412; NTISDE  
Section Headings: 77A (Nuclear Science and Technology--Fusion Devices  
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**Toshiba's Selected Papers on Science and Technology, 1989. Volume 1, No.  
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Corp. Source Codes: 021559000  
c1989 139p  
Languages: English  
Journal Announcement: GRAI9009  
See also PB90-121989.

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NTIS Prices: PC A07/MF A01  
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The volume contains papers on science and technology including: New  
magneto optic head with a built-in generator for a bias magnetic field;  
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GaAs gate array with a new large-noise-margin SLCF circuit; A supervised  
simulation system for process and device designs based on a geometrical  
data interface; Concurrent program synthesis with reusable components using  
temporal logic; Advanced MAP for real-time process control; Voice, data and  
video integrated broad-band metropolitan area network; Magnetic properties  
and recording characteristics of barium ferrite media (invited); processing

of CMOS VLSI; A 25-ns 1-Mbit CMOS SRAM with loading-free bit lines; A 30-uA data-retention pseudostatic RAM with virtually static RAM mode; An electron beam test system linked with a CAD **database** ; A **CMOS chip** pair for digital TV; An expert system for power system fault analysis and restoration; Switching surge of shunt reactor caused by SF6 circuit breaker operation.

Descriptors: \*Electronics; \*Reviews; \*Technology innovation; Magnetic heads; Magneto-optics; Photodiodes; Xerography; Gates(Circuits); Films; Inspection; Transmission lines; Electrical faults; Circuit breakers

Identifiers: \*Foreign technology; \*Science; Computer aided design; Software engineering; Automatic programming; Chips(Electronics); Computer aided manufacturing; Expert systems; Protocols; Communications networks; CMOS; Very large scale integration; Random access memory; NTISTFTC

Section Headings: 49GE (Electrotechnology--General); 49H (Electrotechnology--Semiconductor Devices); 88E (Library and Information Sciences--Reference Materials)

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12621027 PASCAL No.: 96-0313357

**SVT hit buffer**

BELFORTE S; DELL ORSO M; DONATI S; GAGLIARDI G; GALEOTTI S; GIANNETTI P;  
MORSANI F; PASSUELLO D; PUNZI G; RISTORI L; SPINELLA F; ZANETTI A M  
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Proceedings of the 1995 Nuclear Science Symposium & Medical Conference,  
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Country of Publication: United States

Language: English

The Hit Buffer is part of the Silicon Vertex Tracker (1), a trigger processor dedicated to the reconstruction of particle trajectories in the Silicon Vertex Detector (2) and the Central Tracking Chamber of the Collider Detector at Fermilab. The Hit Buffer is a high speed data-traffic node, where The Hit Buffer is capable to process data at a rate of 25 MHz, thanks to the use of special fast devices like Cache-Tag RAMs and high performance Erasable Programmable Logic Devices from the XILINX XC7300 family.

English Descriptors: Hit buffer; Silicon vertex tracker; Data traffic node; Cache tag random access memory; Erasable **programmable** logic devices ; Theory; **Database** systems; Data processing; Random access storage; Logic devices; Associative storage; Data acquisition; Particle detectors

French Descriptors: Theorie; Systeme base donnee; Traitement donnee; Memoire acces direct; Dispositif logique; Memoire associative; Saisie donnee; Detecteur particule

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